



Arka Educational and Cultural Trust ®
JAIN INSTITUTE OF TECHNOLOGY, DAVANGERE-577 003
(A Unit of Jain Group of Institutions, Bangaluru)

JIT,EEE News Letter



BI ANNUAL AUGUST 2022 - DECEMBER 2022

Visit us at: www.jitd.in

Email: jitdvg2011@gmail.com



Department of Electrical and Electronics Engineering

“VISION”

“To create technically superior, highly competent, ethically strong and socially responsible Electrical and Electronics Engineers with innovative and entrepreneurship qualities.”

“MISSION”

To impart excellent Electrical and Electronics Engineering Education by providing Innovative teaching, career focused education and experiential learning.

To create an environment for innovative thinking and contribute to the advancement of science and technology to meet the ever changing needs of society.

To provide student centric platform to produce socially responsible, competent engineers and entrepreneurs with ethical standards.

EDITORIAL TEAM

Prof. Pradeep Kanavi, Assistant Professor

Mr. Sandeep Patil M S, Instructor

PROGRAM OUTCOMES

- An ability to apply knowledge of mathematics & science to electrical engineering.
- An ability to identify, formulate and solve complex electrical engineering problems.
- An ability to design electrical systems to meet desired needs with appropriate considerations for public health and safety.
- An ability to use research based knowledge to design and conduct experiments.
- An ability to use techniques, skills and modern electrical engineering tools like MATLAB, CAED, etc. for engineering practice.
- To understand the impact of engineering solutions in a global, economic, and societal contexts.
- Understand the impact of the electrical engineering solutions in environmental contexts & demonstrate the knowledge for sustainable development.
- An understanding of professional and ethical responsibility.
- An ability to function on multidisciplinary teams.
- Ability to communicate effectively with the engineering community and with society.
- Develop an appreciation for principles of managerial skills and entrepreneurship.
- Ability to develop confidence for self-education and to understand the value of life-long learning.

PROGRAM EDUCATIONAL OBJECTIVES (PEOs)

- ♦ To become the resource of Electrical and Electronics engineering.
- ♦ To work in multidisciplinary areas involving core engineering and engaged.
- ♦ To be successful in taking leadership roles and entrepreneurial positions.

PROGRAM SPECIFIC OUTCOMES (PSOs)

- ♦ Apply the knowledge of mathematics, Science and electrical engineering fundamentals to solve complex problems in electrical machines, control systems, power systems and electronics.
- ♦ Specify, architect and analyze power systems that efficiently generate, transmit, distribute and utilize electrical power in the present context.
- ♦ Analyze, design & maintenance of modern electrical drives system and modern lighting system to conserve electrical energy.

Message from the Principal



It's my pleasure to introduce this latest issue of the JIT, EEE Newsletter. I would like to congratulate Editorial team for bringing out a beautiful and informative EEE newsletter which has showcased the various activities, events in the department, and the achievements of students. and faculties. I look forward to achieve greater heights in the years to come.

Dr. Ganesh D B
Principal & Director, JIT, Davangere



Message from the Head of the Department

Welcome to the Department of Electrical and Electronics Engineering at JIT Davangere. We started our journey in the year of 2011 with an intake of 60 students. The department has full-fledged laboratories as prescribed by VTU syllabus with modern equipment's. We have grown our expertise ,competence .

Core Electrical and Electronics Engineering curriculum and research. We have a strong undergraduate program in Electrical and Electronics Engineering in B.E. We conduct many activities throughout the academic year like Workshops, Seminars, technical talks, skill enhancement programs and other co-curricular activities under the department Form "POTENZA" .We are happy to bring out our department EEE newsletter, which showcases all the activities of the department. I congratulate the team for their continuous effort.

Dr. Santosh M Nejakar
Associate Professor & Head,
Dept. of Electrical & Electronics Engineering,JIT

"NBA EXTERNAL MOCK AUDIT"

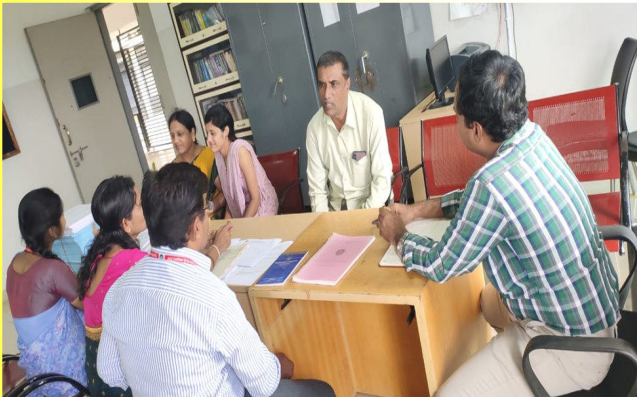
7th October 2022



NBA external mock audit was conducted in Electrical and Electronics department . The audit was done by Dr. P S Puttaswamy, Retired Professor, PES College of Engineering, Mandya . Dr. Santosh M Nejakar, HOD of EEE department , staffs were present HOD of EEE department presented the NBA process followed in the department. NBA Coordinator along with the staff members exhibited respective Criteria files and P files. Sir verified each of the P-Files, Criteria files and its supporting documents and gave valuable suggestions.

Parents and Teachers meet

4th August 2022



Parent Teacher meet was conducted on 4th August 2022 for 6th semester students in EEE department. Many parents participated in the meet and enquired about the performance of their wards. Information about Placement Training Activities and Companies visited the college for campus recruitment was also conveyed to parents. All staffs including HOD were present during the meet.

"Industrial Visit Varahi hydro Power Plant"

25th & 26th November 2022



EEE department 7th sem students are visited Varahi Hydro Electric Power Plant, Kali River in Hosaangadi of Uttara Kannada district of Karnataka, as a part of industrial visit on 25th and 26th November 2022. Prof. Chetan H R & Ms. Sandeep Patil M S, Instructor accompanied the students. This industrial visit is to provide students an insight regarding internal working of power plant. We know, theoretical knowledge is not enough for making a good professional career. With an aim to go beyond academics. Industrial visit provides student a practical perspective on the world of work and also visited Eco Park Honavara.

"Intra Institutional Internship"

11th October to 29th October 2022

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

"Internship-2022"
with Industry Certification for Third Semester

Three week Internship

- First Week**
Electrical Safety
- Second Week**
Power System Training
- Third Week**
PC Assembly & Troubleshooting Technique

Contact:

Mrs. Sujata Eresimi
Assistant Professor

Mrs. Umarani T.G
Assistant Professor

Dr.Santosh .M. Nejakar
HOD EEE

Dr.Ganesh D B
Principal & Director

Starts From
11 October 2022
9.30:AM on wards
Venue: JIT Auditorium

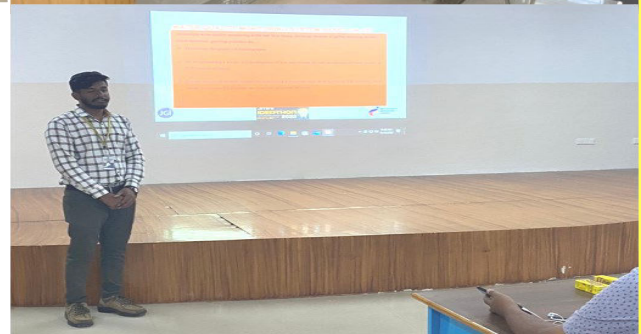
Jain EE Potenza **depteejit**





Department of Electrical & Electronics Engineering forum Potenza and collaboration with Abhiyatrix Soft Lab Pvt Ltd, Davanagere conducted “Intra/ inter Institutional Internship” program for 3rd semester students. The inaugurated by lighting the lamp Dr Rakesh L Director Ada Lovelace Centre Bangalore and Principal and director Dr Ganesh D B were present in function. Objectives of the Internship is to provide students with an opportunity to put into practice skills they have learned while in school. In addition, students should have an opportunity to enhance those skills, obtain the perspective of a work environment and benefit from a mentor or supervisor's experience and advice. The Internship provides students with the opportunity develop self-awareness, , Develop career awareness, direction and exploration of vocation. Internship give the Awareness of Electrical Safety, Hazards of electricity, Safety procedures and methods, Basics of Transmission & Distribution, Hands on scilab programming & Introduction to IOT & Embedded systems. Hands on session on interfacing programs.

“Ideathon 2022” 10th November 2022



Ideathon-2022 event conducted in department of EEE Total 15 teams participated and presented their ideas. Prof. Praveena Anaji, Prof. Pradeep Kanavi, Prof. Nagaraja Bodravara and Prof. Chetan H R appointed as evaluators. Event was very well organized by the Prof. Nandish B M , IIC coordinator.

Ideathons are intensive brainstorming events where individuals from different backgrounds, skills and interests converge to diagnose predefined problems, identify the best opportunities and ideate the most viable solution. An ideathon may require just ideas, a solution, a roadmap or an actionable plan.

“VTU sponsored Faculty Development Program 13th & 17th December 2022

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JGI JAIN INSTITUTE OF TECHNOLOGY, DAVANGERE
(A Unit of Jain Group of Institutions, Bengaluru)
(Approved by AICTE, New Delhi, Recognized by Government of Karnataka, Affiliated to VTU, Belagavi)
#323, Near Veereshwara Punyashrama, Davangere - 577003

INAUGURAL FUNCTION OF VTU SPONSORED
FIVE DAY FACULTY DEVELOPMENT PROGRAM
ON
“ABILITY ENHANCEMENT COURSES”
13th - 17th DECEMBER, 2022
IN COLLABORATION WITH
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
&
DEPT. OF ELECTRICAL & ELECTRONICS ENGINEERING
SJBIT-BANGALORE

CHIEF GUEST
Dr. S. Vidyashankar
Vice-Chancellor, VTU, Belagavi

GUEST OF HONOUR
Dr. Basavaraj Madiggond
Chairman-BOS (E&EE), VTU Belagavi,
HOD EEE HSIT Nidasoshi

Date : 13-12-2022. Time : 10-00 am
Venue : JITD Auditorium

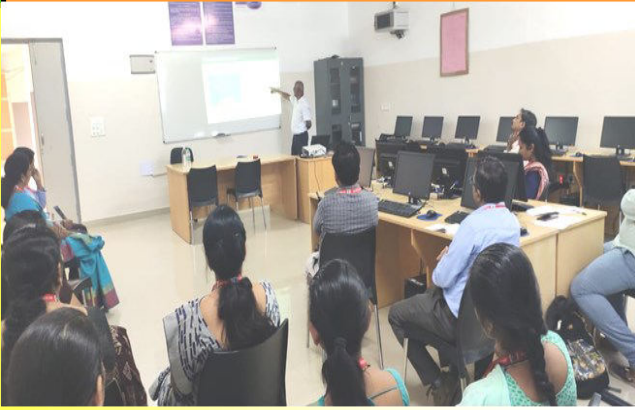
CONVENER
Dr. Santosh M Nejakar
Convener & Head, EEE
Jain Institute of Technology, Davangere

PRINCIPAL & DIRECTOR
Dr. Ganesh D B
Principal and Director,
Jain Institute of Technology, Davangere

CO-ORDINATORS
Mr. Marulasiddappa H B Mrs. Preetha P S
Mr. Chetan H R

VTU sponsored Five day Faculty Development Program on “Ability Enhancement Courses” organized by Department of E&E, JITD in collaboration with Department of EEE, SJBIT, Bangalore was inaugurated by lighting the lamp. Dr. S. Vidyashankar, Vice Chancellor, VTU, Belagavi, addressed the participants & the audience regarding new education policy in virtual mode. Guest of honour Dr. Basavaraj V Madiggond, Chairman, BOS(EEE), VTU, Belagavi & HOD, EEE, HSIT, Nidososhi grace the occasion. Dr. Ganesh D B, Principal & Director, JITD honored the guest. Convener & Head of EEE Department Dr. Santosh M Nejakar, Dr. Madhukeshwara N, Dean Academics, Coordinators Prof. Marulasiddappa. H.B, Prof. Preetha P S, Prof. Chetan H R & Heads of various departments, Teaching & Non-teaching staffs & participants of various colleges were present at the event. After the inauguration Dr. J P Sridhar, Associate Professor, Dept. Of EEE , SJBIT, Bangalore delivered a session on Introduction to SciLab and Xcos fundamentals. Afternoon session was delivered by Mr. Mohan B S, Assistant Professor, Dept. Of EEE, SJBIT about Circuit Laboratory using PSPICE.





Second day of VTU sponsored Faculty Development Program, Morning session is delivered by resource person Mr. Nandish B M, Assistant Professor, Department of EEE, JITD on “Introduction to Scilab for Transformers and Generators” followed by hands on session in the afternoon. Third day of VTU sponsored Faculty Development Program, morning session was delivered by resource person Mrs. Sujata Eresimi, Assistant Professor, Department of EEE, JITD on “Scilab for Electric Motors” followed by hands on session. Fourth day of VTU sponsored Faculty Development Program, Morning session was delivered by resource person Dr. A B Raju, Professor and Head, EEE Department, KLE Technological University, Hubballi on use of Scilab XCos for electrical circuits and afternoon session was delivered by Mrs. Umarani T G, Assistant Professor, Department of EEE, JITD on “Scilab for Power Electronics” followed by hands on session. Fifth day of VTU sponsored Faculty Development Program, morning session was delivered by resource person Mr. Pradeep Kanavi, Assistant Professor, Department of EEE, JITD on “Scilab for Mathematics” followed by hands on session.

“Valedictory VTU sponsored Faculty Development Program” 17th December 2022



The valedictory of Five day Faculty Development Program on “Ability Enhancement Courses” was done on 17 Dec 2022 at 11am in JITD auditorium Dr B E Rangaswamy, Registrar, VTU, Belagavi was the chief guest. Dr.Sadashiva Halabhavi, Special office, VTU, Belagavi and Dr. Manjappa Sarathy, Director, Research and Consultancy, Sahyadri College of Engineering and Management, Mangalore were the guest of honour. Principal and Director of the institution Dr Ganesh D B, Dr Santosh M Nejekar, Convener, Dr.Madhukeshwara N, Dean Academics, Coordinator Prof. Chetan H R and heads of various departments, teaching and non teaching staffs and participants of different colleges grace the event. Dr. B. E. Rangaswamy, Registrar, VTU, Belagavi and Dr.Sadashiva Halabhavi, Special office, VTU, Belagavi joined the event virtual mode. All the guests appreciated the college, department and management for conducting such a wonderful fruitful event. Participants also appreciated the FDP arrangements, efforts of coordinators, knowledge of Resource Persons involved.

“Forum Potenza Inauguration and Fresher's day - 2022”

23rd December 2022

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Potenza Forum Inauguration & Freshers Day-2022

"Our Alumni our Pride"

23 Dec 2022
 10:00AM Onwards
 Venue: JIT Auditorium

Chief Guest:
 Ms. Rachitha S A
 Associate Software Engineer
 Earnst & Young (EY GDS), Bangalore

Mr. Vishwanatha B G
 Associate QA Engineer
 Test Yantra Software Solutions, Bangalore

Mr. Chetan H R
 Mr. Nandish B M
 Forum Coordinators

Dr. Santosh M. Nejakar
 HOD, EEE

Dr. Ganesh D B
 Principal & Director

f Jain EE Potenza depteejit WWW.JITD.IN



Department of Electrical & Electronics Engineering forum Potenza was inaugurated and fresher's day-2022 was also celebrated. Chief Guests Ms. Rachitha S A, Associate Software Engineer, Earnst and Young (EY GDS), Bangalore and Mr. Vishwanatha B G, Associate QA Engineer, Test Yantra Software Solutions, Bangalore gave information about job opportunities available for Electrical Engineers. Both the Chief Guests were the alumni of the department. Principal and Director Dr. Ganesh D B, HOD of department Dr. Santosh M Nejakar, Forum Co-ordinators Mr. chetan H R and Mr. Nandish B M were present at the event. All the Teaching , non teaching staffs and students participated with enthusiasm. Toppers of also rewarded during the event.

“KSCST-IP Cell Inauguration”

27th December 2022



KSCST IP CELL was Inaugurated in the JITD. Chief Guest Prof. H. Hemanth Kumar (Executive Secretary, KSCST, IISc Campus, Bangalore), Guests of Honour Dr, Manjappa Sarathi (Director, R&D,. SCEM., Mangalore) & Mrs. Prabhavathi Rao (Visveswaraya Trade Promotion Centre, Facilitator, Intellectual Property Initiatives, DIC, Bangalore, GoK), Speaker Mr. Nagarjuna M G (KSCST, IISc , Bangalore) Dr. Ganesh D B, Principal and Director have inaugurated the IP Cell and Conveyed the significance of IPR in the Career. JITD has also signed MOU with KSCST. Convener & Dean of JITD–KSCST-IP Cell and HOD, E&E , Dr. Santosh M Nejakar, staffs and students were present in the function.

“Students Academic Achievements”



Place	Name	Semester	Percentage
1 st	Vijayalakshmi S	6 th	89.38%
2 nd	Tanuja A L	6 th	84.75%
1 st	C P Ravi	4 th	83.88%
2 nd	Malik Rihaan N	4 th	79.50%
1 st	Rahul C S	2 rd	89.3%
2 nd	Chinmayi B	2 rd	87.7%

Paper Publications by the Faculty

Publisher Name	Tittles of the paper	Details
Vijayalakshmi A K	“An approach to contingency severity prediction using Data analytic Techniques in a P Power System”., Paper ID:3620	1 st International Conference on Smart and Sustainable Technologies in energy and power Sectors (SSTEPS-2022) during 7 th -9 th 2022,organized by Department of Electrical Engineering and Technology, Central University of Haryana-123029

“Faculty Achievements”

Resource Person for the “Ability Enhancement Courses”

4th August 2022



Electrical & Electronics Engineering Department, JITD faculties Prof. Nandish B M and Prof. Umarani were participated as a resource for the six days workshop on “Ability Enhancement courses & IPCC laboratory” topic on “Scilab for Transformers & Generators” organized by SJB Institute of Technology, Bengaluru, Collaboration with EEE dept. Jain Institute of Technology, Davanagere.

“Honouring the Students”



The “Building Prototype Electrical Module” Competition was held by the Department of Electrical & Electronics Engineering under forum Potenza at JITD , for students in Forum Potenza. Mr. Kiran and Mr. Shanshank, seventh semester students won the first prize.

“Glimpses of Activities”





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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGG.

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