



Arka Educational and Cultural Trust ®
JAIN INSTITUTE OF TECHNOLOGY, DAVANGERE-577 005
(A Unit of Jain Group of Institutions, Bangaluru)



JIT,EEE News Letter

ANNUAL AUG 2019 - JUNE 2020

Visit us at: www.jitd.in

Email: jitdvg2011@gmail.com



Department of Electrical & Electronics Engineering

“VISION”

“To achieve Technically superior man power development and to build highly competent, ethically strong, socially responsible new generation of Electrical and Electronics Engineer.”

“MISSION”

- ♦ To impart excellent Electrical and Electronics Engineering Education with innovative teaching, international career focused education and experiential learning.
- ♦ To evolve as a Centre of excellence in R&D in country and contribute to the advancement of science and technology to meet the ever changing needs of society.
- ♦ To nurture career improvement and to facilitate young Electrical and Electronics Engineers to acquire technical exposure.
- ♦ To create outstanding professionals and to produce quality engineers with high ethical standards and professionalism.

Department of Electrical & Electronics Engineering

PROGRAM OUTCOMES

Graduates of the Electrical engineering program are expected to demonstrate:

- An ability to apply knowledge of mathematics & science to electrical engineering
- An ability to identify, formulate and solve complex electrical engineering problems.
- An ability to design electrical systems to meet desired needs with appropriate considerations for public health and safety.
- An ability to use research based knowledge to design and conduct experiments.
- An ability to use techniques, skills and modern electrical engineering tools like MATLAB, CAED, etc. for engineering practice.
- To understand the impact of engineering solutions in a global, economic, and societal contexts.
- Understand the impact of the electrical engineering solutions in environmental contexts & demonstrate the knowledge for sustainable development
- An understanding of professional and ethical responsibility.
- An ability to function on multidisciplinary teams.
- Ability to communicate effectively with the engineering community and with society.
- Develop an appreciation for principles of managerial skills and entrepreneurship
- Ability to develop confidence for self-education and to understand the value of life-long learning.



Dr. Manjunatha T S.

Principal & Director JIT

Message from the Principal

It's my pleasure to introduce this latest issue of the JIT, EEE Newsletter. I would like to congratulate Editorial team for bringing out a beautiful and informative EEE newsletter which has showcased the various activities, events in department, and the achievements of students and faculties. I look forward to achieve greater heights in the years to come.

Message from the HOD

Welcome to the Department of Electrical and Electrical Engineering at JIT Davanagere. We started our journey in the year of 2011 with an intake of 60 students. The department has full-fledged laboratories as prescribed by VTU syllabus with modern equipment's. We have grown our expertise and competence in the core Electrical and Electrical Engineering curriculum and research. We have a strong undergraduate program in Electrical and Electrical Engineering in B.E. We conduct many activities throughout the academic year like Workshops, Seminars, technical talks, skill enhancement programs and other co-curricular activities under the department Form "POTENZA".

Our department offers science-based engineering curriculum. The primary focus of our curriculum is to impart technical know-how to students, promote their problem solving skills and innovation of new technologies. The course contents are periodically updated for introducing new scientific and technological developments. Staff members are active in research work and have published research papers in reputed journals and encouraging the students to undertake various research projects.

Our department has a distinguished record in both teaching and research. Faculty members have excellent academic credentials and are highly regarded. We look forward to contribute in solving the technological challenges of the society with



Dr. Chandrashekar K

HOD, Electrical & Electronics Engineering

INAUGURAL FUNCTION OF FORUM POTENZA-2K19

5th october2019



EEE Forum 'POTENZA'-2K19 inaugurated by Mr. Subhash G K ,Assistant Engineer, KPTCL, Ranebenur, and Dr.Manjunatha T.S., Principal, JIT, Davanagere, on 05-10-2019 at JIT auditorium.

Mr. Subhash G K chief guest expert talk on Karnataka Power Transmission Corporation Limited , over view of that company. KPTCL is the sole electricity transmission and distribution. This electricity transmission and distribution entity was corporatized to provide efficient and reliable electric power supply to the people of Karnataka state. KPTCL scope of work includes the handling of large projects in the field of energy. And also talk about the working management and responsibilities of the students to electric power conservation, Dr.Chandrashekar. K, HOD, Dept. of Electrical and Electronics Engineering also addressed the students on the occasion. Prof. Chetan H R, Prof. Nandish B M Forum coordinators, Ms. Ranjitha Jadav Student coordinator were also present in function.

NEWS LETER-2018 RELEASE

5th october2019

JIT EEE News letter –2018 released by the chef guest

Mr. Subhash G K ,Assistant Engineer, KPTCL, Ranebenur, and Dr.Manjunatha T.S., Principal, JIT, Davanagere, on 05-10-2019 at JIT auditorium.

INDUSTRIAL VISIT ON LINGANMAKKI POWER HOUSE

5th September 2019



The EEE departments, JIT, Davanagere, organized the Industrial Visit on Linganamakki Power house and dam to the 3rd year students, this field visit to provide an exposure to students about practical working environment of power house and dam.

INDUSTRIAL VISIT ON VARAHI HYDRO ELECTRIC POWER PROJECT

5th September 2019



The EEE departments, JIT, Davanagere, organized the Industrial Visit Varahi Hydro-electric power Project to the Final year students, this field visit to provide an exposure to student's good opportunity to gain full awareness about power project working practices. Through industrial visit students get awareness about new technologies.

ALUMINI MEET IN EEE DEPT.
25th October 2021



The department of EEE ,Jain institute of technology, Davanagere organized Alumni Meet “Samnvaya-2K19” for the outgoing students held on 25/10/2019 in JIT auditorium. Alumni Meet is a gathering of passed out students of a JIT and it is a place. where the institution feels proud on seeing its successful alumni. During their meet, the alumni community shares their experience in the outside world, which they faced after stepping out of the institution.

GRADUATION DAY IN JIT
6th July 2019



Jain institute of technology, Davanagere conducted Graduation Day on July 6th 2019 held at Dr. B. Shyamsundar Shetty Buntara Bhavana Davanagere. Graduates who passed out during the academic year 2018 -19 received their graduation certificates from the chief guest. Dr. Basavara Banakar, Register, Davanagere. The chief guest speech he said “It is astonishing to know that you were part of the team that India accounts for nearly one-third of the world's engineering graduates each year and offers a million students the opportunity to pursue technology courses annually”. He concluded his speech by stressing on the fact “Graduation is only a concept. In real life each and every day you graduate. Graduation is a process that goes on until the last day of your life”. The Management honored the toppers by Medals and Certificates.

Dr. Manjunatha T S, Principal JIT, administered the institutional oath to the graduates and in his address said that the fresh graduates are the brand ambassadors of the institute and urged them to know the technological world in real time and help the present students of the institute in finding training and placement.

SHRUNGA COLLEGE SPORTS FEST 6th July 2019



Jain institute of technology, Davanagere conducted Annual day Shrunga Sports events in JIT campus on July 6th 2019. EEE first year s Cricket team won the First prize.

TEACHER'S DAY IN EEE DEPT.

5th September 2019



The teachers' day was celebrated in the EEE dept. JITD to mark the birth anniversary of its second President Sarvepalli Radhakrishnan, Teacher's day marks the teaching practices and rights of teachers, Prof. Marulasidappa H B Assistant Professor EEE dept. addressed the students on the occasion. Dr.Chandrashekar. K, HOD, Dept. of Electrical and Electronics Engineering and the staff and students were present in the function.

List of paper publications by EEE faculty for the Academic Years (2019-2020)

SL. No	Name of the Faculty	Title	Name of the Journal/Conference /Event / Publisher	Volume/ Issue/ Journals/ conference	Year
1	Prof. Preetha P S	A bat behavior approach for energy management system of a real time industrial load for minimizing the operating cost	IEEE		2019
2	Prof. Preetha P S	GSM based transformer monitoring & controlling system	IJRTI	Volume 4, issue 6 2019	2019
3	Prof. Praveena Anaji	Simulation of single phase uni-polar inverter using sliding mode controller	IJSDR	Volume 4, issue 5 may 2019	2019
4	Prof. Nandesh K N	Simulation of DC-DC converter for the power quality enhancement	IJSDR	ISSN: 2455-2631, VOLUME-4, ISSUE 5, pp258-260	MAY 2019
5	Prof. Nandesh K N	simulation of buck & boost converter for the power quality improvement	IJSDR	ISSN: 2455-2631, VOLUME-4, ISSUE 5, pp – 261-264	May 2019

List of paper publications by EEE faculty for the Academic Years (2019-2020)

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6	Prof. Nandish B M	Simulation of single phase unipolar inverter using Mat lab	IJRTI	Volume 4, issue 5, ISSN: 2456-3315	2019
7	Prof. Nandish B M	Simulation of single phase unipolar inverter using sliding mode controller	IJSDR	Volume 4, issue 5	2019

ALUMNI'S FEEDBACK

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JAIN INSTITUTE OF TECHNOLOGY



Department of Electrical and Electronics Engineering

It was my immense pleasure to have experienced a great educational journey in JIT. I couldn't thank enough and be anymore grateful to JIT for placing me currently in a position where I can proudly boast its name. Presently I am pursuing my masters in Chalmers University of technology, Sweden. I will soon be joining as a Function Developer at Volvo Group Trucks Technology. All I can now think about is how helpful and generous our lecturers were at JIT to get me where I am today. They made me stand positive and supported us at our nooks and corners for our overall development. JIT made me build enough confidence to look into my future 5 years down the lane with a passionate and determined vision. Last but not the least, I praise and am greatly indebted to my entire teaching and non-teaching staff, the whole system of JIT and its infrastructure for delivering us our 4 years of best engineering days.

PRAVEEN HASBAVI

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JAIN INSTITUTE OF TECHNOLOGY



Department of Electrical and Electronics Engineering

Firstly, it was awesome experience at JIT because I spent my great time there and the campus was just cool and peaceful, the faculty support was exceeding and they are giving their immense support to build our Carrier and helps us to succeed. I would like to heartily thank all the lecturers and staff of JIT, especially to department of Electrical and Electronics. Thank you♥

DHANANJAYA M K
Design Engineer
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ACADEMIC TOPPER'S

SL.No	Name	Semester	Marks	Percentage
01	Savitha H T	8 th	644	92%
02	Pooja T S	8 th	623	87.5%
03	Revanasiddaiah S	6 th	634	79.2%
04	Ranjitha K Jadhav	6 th	632	79%
05	Niveditha N B	4 th	603	75.37%
06	Anupama S H	4 th	601	75%

STUDENTS PLACEMENT DETAILS

SL.No	Name	Company
01	Ganesh M P	Ola Financial Services PVT Ltd.
02	Megharaj P N	Code True, Bangalore
03	Naven Kumar H B	Flux auto India PVT Ltd.
04	Pavitra P M	Pixel Software
05	Pooja Y N	Tyco Electronics
06	Sachin Y M	CGE, Bangalore
07	Saleem K E	Aggne, Bangalore
08	Sanjana U P	Bion Safety controls PVT Ltd.

DEPARTMENT ACTIVITIES PHOTO GLIMS





EDITORIAL TEAM

Prof. Shailaja B M
Assistant Professor
Mr. Sandeep Patil M S
Instructor