



Arka Educational and Cultural Trust[®]
JAIN INSTITUTE OF TECHNOLOGY
Davanagere
(A Unit of Jain Group of Institutions, Bangalore)

JIT EEE-NEWSLETTER

BI ANNUAL JANUARY - JUNE 2024

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



"VISION"

"TO CREATE TECHNICALLY SUPERIOR, HIGHLY COMPETENT, ETHICALLY STRONG AND SOCIALLY RESPONSIBLE ELECTRICAL AND ELECTRONICS ENGINEERS WITH INNOVATIVE AND ENTREPRENEURSHIP QUALITIES."

"MISSION"

M₁ - TO IMPART EXCELLENT ELECTRICAL AND ELECTRONICS ENGINEERING EDUCATION BY PROVIDING INNOVATIVE TEACHING, CAREER FOCUSED EDUCATION AND EXPERIENTIAL LEARNING.

M₂-TO CREATE AN ENVIRONMENT FOR INNOVATIVE THINKING AND CONTRIBUTE TO THE ADVANCEMENT OF SCIENCE AND TECHNOLOGY TO MEET THE EVER CHANGING NEEDS OF SOCIETY.

M₃-TO PROVIDE STUDENT CENTRIC PLATFORM TO PRODUCE SOCIALLY RESPONSIBLE, COMPETENT ENGINEERS AND ENTREPRENEURS WITH ETHICAL STANDARDS.

EDITORIAL TEAM

PROF. PRADEEP KANAVI
ASSISTANT PROFESSOR

MR. SANDEEP PATIL M S
FOREMAN



Visit us at: www.jitd.ac.in

PROGRAM OUTCOMES

1. **Engineering knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and computer science and engineering to the solution of complex engineering problems.
2. **Problem analysis:** Identify, formulate, review research literature, and analyze complex computer engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
3. **Design/development of solutions:** Design solutions for complex computer engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
4. **Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
5. **Modern tool usage:** Create, select, and apply appropriate techniques, resources, and modern computer engineering and IT tools including prediction and modelling complex computer engineering activities with an understanding of the limitations.
6. **The engineer and society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
7. **Environment and sustainability:** Understand the impact of the professional computer engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
8. **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the computer engineering practice.
9. **Individual and team work:** Function effectively as an individual, and as member or leader in diverse teams, and in multidisciplinary settings.
10. **Communication:** Communicate effectively on complex computer engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
11. **Project management and finance:** Demonstrate knowledge and understanding of the computer engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
12. **Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

PROGRAM SPECIFIC OUTCOMES (PSO's)

A graduate of the Electrical & Electronics Engineering Program will demonstrate:

PSO1: Apply the knowledge of mathematics, science and electrical engineering fundamentals to solve complex problems in electrical machines, control systems, power systems and electronics.

PSO2: Specify, architect and analyze power systems that efficiently generate, transmit, distribute and utilize electrical power in the present context.

PSO3: Analyze, design & maintenance of modern electrical drives system and modern lighting system to conserve electrical energy.

PROGRAM EDUCATIONAL OBJECTIVES (PEO's)

A graduate of the Electrical & Electronics Engineering Program will demonstrate:

PEO1: To become the resource of Electrical and Electronics engineering.

PEO2: To work in multidisciplinary areas involving core engineering and engaged in lifelong learning for successful professional career.

PEO3: To be successful in taking leadership roles and entrepreneurial positions.

MESSAGE FROM THE CHAIRMAN

We are a school that prides itself not in fostering academic excellence but also in making our learners append at the school satisfying, fulfilling and joyous. I intend to take the JGI Group to the next level, employ more effective methods to make the students think differently and instill in them with positive attitude towards inclusive growth that benefits them and the society at large.

DR. CHENRAJ ROYCHAND
CHAIRMAN JGI
BENGALURU



MESSAGE FROM THE PRINCIPAL

It's my pleasure to introduce this latest issue of the JIT, EEE Newsletter. I would like to congratulate Editorial team for bringing out a beautiful and informative EEE newsletter which has showcased the various activities, events in the department, and the achievements of students and faculties. I look forward to achieve greater heights in the years to come.

DR. GANESH D B
PRINCIPAL & DIRECTOR JIT
DAVANAGERE



MESSAGE FROM THE HEAD OF THE DEPARTMENT

Welcome to the Department of Electrical and Electronics Engineering at JIT Davangere. We started our journey in the year of 2011 with an intake of 60 students. The department has full-fledged laboratories as prescribed by VTU syllabus with modern equipment's. We have grown our expertise, competence.

Core Electrical and Electronics Engineering curriculum and research. We have a strong undergraduate program in Electrical and Electronics Engineering in B.E. We conduct many activities throughout the academic year like Workshops, Seminars, technical talks, skill enhancement programs and other co-curricular activities under the department Form "POTENZA". We are happy to bring out our department EEE newsletter, which showcases all the activities of the department. I congratulate the team for their continuous effort.

DR. SANTOSH .M. NEJAKAR
PROFESSOR & HEAD,
DEPT. OF ELECTRICAL & ELECTRONICS ENGINEERING, JITD



FORUM POTENZA-2K24 INAUGURATION AND FRESHERS DAY CELEBRATION

12TH JANUARY 2024



The Department of Electrical & Electronics Engineering forum Potenza had a vibrant inauguration along with the celebration of Fresher's Day in 2024. The event seems to have been quite enriching with informative sessions from distinguished guests. Mr. Shashikumar R, Assistant Engineer from BESCOM, shared insights into the operation and maintenance of BESCOM, while Mr. Prajwal N T, an alumnus from Capgemini, shed light on the job opportunities available in the IT sector for electrical engineers.

The presence of guest Dr. Manjappa Sarathi, Advisor from the Jain Group of Institutions, along with the HOD Dr. Santosh M Nejakar and other dignitaries, added significant value to the event. The President of the function Dr. Ganesh D B, Principal and Director of JITD, highlighted the importance of the occasion.

Recognition of academic excellence through rewarding toppers and students who completed NPTEL certifications is commendable. Additionally, organizing fun activities for 3rd semester students must have added a lively touch to the event, fostering a sense of camaraderie and engagement among participants. Overall, it seems like a successful and memorable event for everyone involved.



STUDENT ACCOMPLISHMENT

11TH JANUARY 2024



Congratulations to Mohammed Zaid from the 5th semester of the Electrical and Electronics Engineering department, JITD for his remarkable achievement in winning the 1st prize at the Hackathon event of Encrypt-24. The event was organized by the Department of Artificial Intelligence and Machine Learning at GM Institute of Technology, Davangere. His participation and victory in this highly competitive event truly highlight his talent, skills, and dedication. It is truly inspiring to see the recognition and support given by the management, principal, HOD, and staff of the department.

CLIMATE CLOCK INAUGURATION

8TH SEP 2023

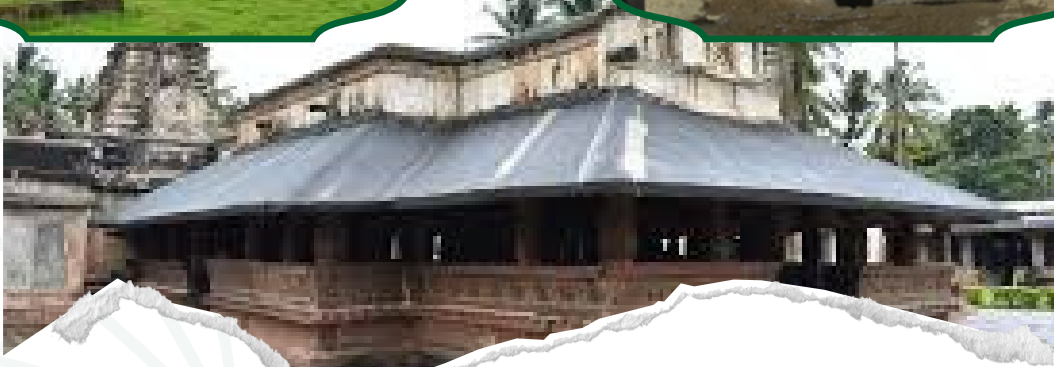
Jain institute of Technology Davanagere has been appreciated and recognized by the Energy Swaraj Foundation for our efforts in receiving over 1000 ELT (Energy Literacy Training) certificates. The gesture of sending a Climate Clock as a token of appreciation emphasizes our commitment to energy literacy and environmental sustainability.

The Inauguration of the Climate Clock by Dr. Ganesh D.B, Principal and Director, and Dr. Manjappa Sarathi, Advisor, along with all the HODs, Deans, and coordinators, showcases the collective effort and dedication towards this cause. Mrs. Preetha P S, the Convener, highlighted the importance of raising awareness about climate change and energy conservation through the Climate Clock. Dr. Santosh M Nejar, HOD of the EEE department, has encouraged and motivated students to save energy in all possible ways, reflecting our institute's proactive approach to instilling eco-consciousness. The unity and collaboration shown by coordinators from various departments in supporting this initiative is commendable. This initiative not only contributes to environmental sustainability but also fosters a sense of responsibility and stewardship among students and staff.



HERITAGE WALK

10 AND 11TH OF FEB 2024



3rd semester students of the Department of Electrical and Electronics Engineering at JITD recently took part in a Heritage Walk to Banavasi as part of their Social Connect and Responsibility curriculum course. This excursion involved interacting with the residents of Banavasi, allowing students to gain a deeper understanding of the local culture and history. By discussing the area's history, students were able to better appreciate the importance of preserving cultural heritage.

Prof. Marulasiddappa H B and Prof. Praveena Anaji guiding the students during the heritage walk, showcasing the institute's dedication to providing a well-rounded education through hands-on learning experiences. Their presence ensured that students received support and guidance throughout the trip, maximizing the educational benefits of the excursion.

"TOWARDS VIKSIT BHARAT: STUDENTS LEADING THE WAY"

26TH FEB 2023

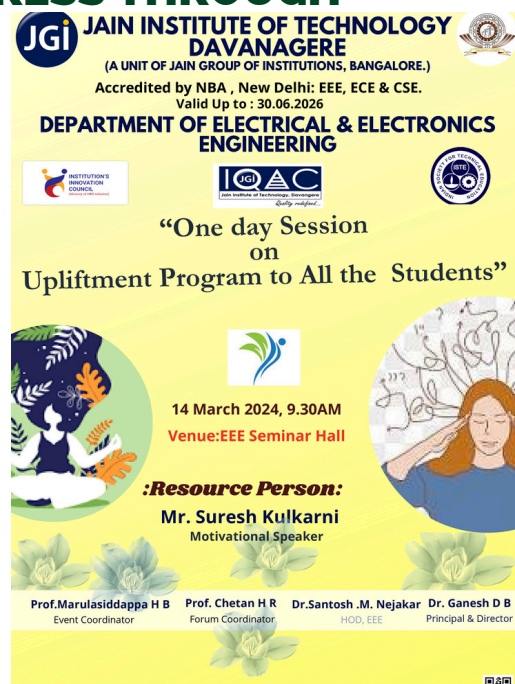


As part of the "Yuva Samvad" event organized by the NSS unit of UBDTCE on 24/02/2024, students Nayana, Manasa, and Sahana from the 7th semester of the electrical and electronics engineering department, JTD, participated in Viksit Bharath 2047. Their chosen topic was a thriving and sustainable economy. They presented their ideas to an audience that included Principal and Director Dr. Ganesh D B, Advisor Dr. Manjappa Sarathi, Dr. Santosh M Nejakar, HOD of EEE, Prof Veeresh Kumar K S, NSS officer of JTD, Mrs. Preetha P S, and NSS coordinator of EEE. The students were appreciated and congratulated for their efforts.

EMPOWERING MINDS: INSPIRING ADDRESS THROUGH UPLIFTMENT PROGRAM

14TH MAR 2023

The Department of Electrical and Electronics Engineering at JTD organized the "Upliftment Program," aimed at fostering personal growth and goodwill among students. The highlight of the event was a motivating speech by Santosh Kulakarni, who emphasized the importance of cultivating goodwill and striving for personal betterment, particularly inspiring for the first-year students. His innovative approach of using drawing as an engagement tool likely enhanced the effectiveness of the session.



The event received strong support from the department and institution, marked by the presence of Dr. Ganesh D. B., Principal and Director, Dr. Manjappa Sarathi, Advisor, and Dr. Santosh M. Nejakar, Head of the Department of EEE, along with other faculty members and students. This collective effort underscores the institution's commitment to the holistic development of students, focusing not only on academic success but also on personal growth and values. Such initiatives are vital in shaping well-rounded individuals ready to tackle future challenges.



TRIUMPH AT E-BELAKU 2024

3RD MAY 2024



Girish H K and Muskan, 7th-semester students from the Department of Electrical and Electronics Engineering (EEE), JITD, participated in a paper presentation and won the First Prize at the E-BELAKU 2024 National Level Technical Symposium. The event took place on 03/05/2024 at Adichunchanagiri Institute of Technology, Chikkamangaluru. A total of eight student batches from EEE, JITD participated in the competition, showcasing their technical prowess and innovative ideas.

The Principal, HOD, and staff members congratulated all participating students, particularly the prize winners, for their remarkable effort and enthusiasm. This achievement highlights the students' dedication and the department's support in fostering a competitive and encouraging academic environment.

VIBRANT VOICES: INSPIRING DEMOCRACY THROUGH ART

5TH MAR 2024



Computer Science and Engineering Department of Jain Institute of Technology, Davanagere Organized the "Drawing and Painting Competition" As per the AICTE campaign *Mera Pehela Vote Desh keliye* This program objective is to spread awareness among students to vote for the Nation Development. EEE students Ms. Sowjanya 5th semester and Ms. Apsara 7th semester won the 1st and 2nd prize. Around 50 students participated from all Engineering department of College.

INNOVATE & ILLUMINATE: PROJECT EXPO 2024

9TH MAY 2024

The Project Expo (P-Expo) 2024, a highly anticipated event showcasing innovative projects, was successfully held on 9th May 2024. The expo was organized at GF 003 well-facilitated venue that offered ample space for students to display their projects and for attendees to engage with the participants. The event was honored by the presence of Dr. Ashok H N, a distinguished Professor from UBDT College of Engineering (UBDTCE), Davanagere. With his extensive academic background and expertise in engineering, Dr. Ashok H N provided valuable insights and feedback to the participants.



KANNADA SAMBRAMA: A TRIBUTE TO TRADITION AND CULTURE

22ND MAY 2024



Department of Electrical and Electronics Engineering celebrated "Kannada Sambrama" - a Kannada theme in ethnic day competition of the institution. Kannada sambrama represents Kannada traditions, art, music and cuisine that defines the essence of kannada culture. Students performed to kannada related songs. Students gave respect to kannada famous personalities in all the fields of kannada. Department cultural coordinator Prof. Dhanush S and his team took the appreciation from the audience. Head of the department Dr. Santosh M Nejekar congratulated the entire team for their best performance.

FAREWELL 2024: HONORING THE JOURNEY AND EMBRACING THE FUTURE

4TH JUN 2024



The Department of Electrical and Electronics Engineering (EEE) organized a farewell event for the final year students of the batch 2024 at the JIT auditorium. The event was graced by Mrs. Minakshi, Director of CRC, Davangere, as the Chief Guest, who addressed the students and encouraged them to use their knowledge to serve society.

Key dignitaries present at the event included Dr. Ganesh D B, Principal and Director, Dr. Manjappa Sarathi, Advisor, Dr. Santosh M Nejar, Head of the Department (HOD), Prof. Chetan H R, Forum Coordinator, and Mr. Karthik, Student Forum Coordinator. The event was attended by teaching and non-teaching staff, along with students.

During the farewell, outgoing students shared their experiences and memories from their time at the institution. The HOD and other staff members extended their best wishes to the graduating students, hoping for their bright and successful future.

EEE STUDENTS EXPLORE VARAHI HYDRO POWER PLANT ON INDUSTRIAL VISIT

28TH JUN 2024



The 6th semester students of the Department of Electrical and Electronics Engineering (EEE) at Jain Institute of Technology, Davanagere, had a visit to the Varahi Hydro Power Plant on June 28, 2024. The visit aimed to provide students with practical insights into the functioning and operations of a hydroelectric power plant, complementing their theoretical knowledge.

The visit to Varahi Hydro Power Plant was a part of the institute's ongoing efforts to provide experiential learning opportunities to its students, preparing them for successful careers in engineering and technology.

STUDENTS SECURE PLACEMENTS AT APOLLO TYRES



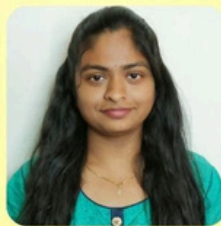
STUDENTS SECURE PLACEMENTS AT QSPIDERS



NANDA KUMAR B M



VINAY GOWDA



SAHANA T M



RAHUL M



VARUN R



M R YASHWANTH



VEERESH B S



KARTHIK F MALAGIMANI



CHANDHANA S N



NAYANA H A



MANASA R



DEEKSHITH N R



Best Wishes From the Management, Principal,
Placement officer, HOD and Staff

STUDENTS SECURE PLACEMENTS AT BRICKSLANE & WISTRON



CHANDHANA S N

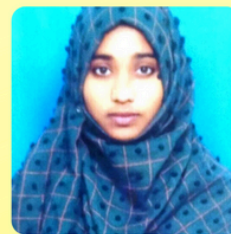


DEEKSHITH N R

Best Wishes From the Management, Principal,
Placement officer, HOD and Staff



SAHANA B



AFTEEN TAJ H



VARNA K N



Best Wishes From the Management, Principal,
Placement officer, HOD and Staff

ACADEMIC TOPPERS

7TH SEMESTER TOPPERS -MAY-2024



Varun R

(87.75%)



Bhagya M

(86.62%)



Ayesha Nausheen

(82%)



Muskhan

(82%)

5TH SEMESTER TOPPERS -MAY-2024



Rahul C S

(89.12%)



Rakshitha A M

(86.5%)



Soujanya H

(86.25%)

3RD SEMESTER TOPPERS -MAY-2024



Vinay S B

(90.22%)



K Ankita

(82.33%)



Lakshmi K

(80.66%)



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