Department of Electronics & Communication Engineering.

Encore

2023-2024 Technical Magazine

The greatest glory in living lies not in

Never falling, but in rising

Every time we fall."

-Nelson Mandela

About The Department

Dr. Santosh Herur,
HOD, Dept of ECE,
JIT,Davangere.

Department of Electronics and Communication was started in the year 2011-12. The department emphasizes on imparting advanced technical knowledge in various fields like signal processing, VLSI Technology, Embedded system, wireless technology, microwave engineering, optical communication, and microelectronics and control systems. Highly qualified, experienced and dedicated staff members are the backbone of the department. Well equipped, sophisticated, precision and state of the art laboratories are center of attraction in the campus. Another major strength of the department is its extensive collection of technical books, periodical and journals, ENCORE association of the department of E & CE aims at targeting innovations in the field of advanced communication and networking.

The Department has MOU with Karunadu Technologies Pvt. Ltd, Bengaluru, Abiyantrix, Soft Solutions, Davangere, Sai Tech, Mysore. The MOU organizations provide Internship training and conduct workshops on recent trends and organize seminars for the students to enhance their industrial exposure.

Editor's desk

Mrs. ANUSHA K Assistant Professor Dept. of ECE, JIT, Davangere	Mr. SUNIL M Assistant Professor Dept. of ECE, JIT, Davangere

*Warm greetings to the readers of **Encore**!!*

We are pleased to share with you the Technical Magazine for the Academic year 2022-23. We express our considerable gratitude and appreciation to everyone who have contributed to this Newsletter magazine. These contributions have required a generous amount of time and effort. It is this willingness to share knowledge, concerns and special insights with fellow beings that has made this magazine possible.

Finally, we would like to conclude with these words "Learn from yesterday, live for today, hope for tomorrow. the important thing is not to stop questioning."- Albert Einstein Embrace each challenge in your life as an opportunity for self-transformation.

Happy reading

Thank you
ANUSHA .K
SUNIL .M

Vision of the Department

Endeavours to achieve academic excellence by providing value based education to meet the ever changing needs of the society.

Mission of the Department

❖ To emphasize on student focus learning environment.

❖ To prepare graduates who surpass in the engineering career, qualified to acquire technical knowledge and ethical values.

❖ To provide a platform to inculcate team spirit and leadership qualities to succeed in professional Career.

Program Specific Outcomes (PSOs)

❖ PSO1: Analyze, simulate and design electronic and communication circuits for given specifications.

❖ PSO2: Design and test processor based systems

Program Educational Objectives (PEOs)

❖ PEO1: To create graduates with a strong base in Electronics & Communication Engineering.

❖ PEO2: To produce technically competent graduates with the ability to understand, analyze & develop electronic systems.

❖ PEO3: Ability to work with professional ethics and as a team member to realize the goals of the projects.

❖ PEO4: Excellence in professional career by attaining higher education.

Institute Academic Calendar of Events

ODD SEMESTER

JGi ARKA Educational & Cultural Trust (Regd.)
Jain Institute of Technology, Davanagere.

ACADEMIC CALENDAR

III SEMESTER B.E. 2023-24 (NOV 2023 – FEB 2024)

Commencement of the Semester: 15/11/2023   **Last working day: 20/02/2024**

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
NOVEMBER 2023			15	16	17	18	19	15th – Commencement of Classes	30th-Kanakadasa Jayanthi
	20	21	22	23	24	25	26		
	27	28	29	30					
DECEMBER 2023					1	2	3	22nd –National Mathematics Day	25th- Christmas
	4	5	6	7	8	9	10		
	11	12	13	14	15	16	17		
	18	19	20	21	22	23	24		
	25	26	27	28	29	30	31		
JANAUARY 2024	1	2	3	4	5	6	7	1st-Internal Assessment 1st,2nd& 3rd 13th-Parents meet	26th – Republic Day
	8	9	10	11	12	13	14		
	15	16	17	18	19	20	21		
	22	23	24	25	26	27	28		
	29	30	31						
FEBRUARY 2024				1	2	3	4	Lab IA : 12th to 14th 2nd Internal Assessment 15th,16th& 17th	
	5	6	7	8	9	10	11		
	12	13	14	15	16	17	18		
	19	20							

Total No. of Working days : 81

VTU ODD Semester Exams

| Practical Exams	21-02-2024 To 29-02-2024	Theory Exams	04-03-2024 To 23-03-2024
	Holidays		Parents Meeting
	Internal Assessment Tests		

Co-ordinator 25/10/23
(Prof. Nataraja G)

Dean Academics
(Dr. Madhakeshwara N)

IQAC Director
(Dr. Santosh Herur)

Principal 25/10/23
(Dr. Ganesh D B)

Institute Academic Calender of Events

ODD SEMESTER

ARKA Educational & Cultural Trust (Regd.)

JGi Jain Institute of Technology, Davanagere.

ACADEMIC CALENDAR

V SEMESTER B.E. 2023-24 (OCT 2023 – MAR 2024)

Commencement of the Semester: 25/10/2023

Last working day: 09/03/2024

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
NOVEMBER 2023						25	26	25th – Commencement of Classes	30th-Kanakadasa Jayanthi
	27	28	29	30					
DECEMBER 2023					1	2	3	22nd – National Mathematics Day 1st-Internal Assessment 28th,29th & 30th	25th- Christmas
	4	5	6	7	8	9	10		
	11	12	13	14	15	16	17		
	18	19	20	21	22	23	24		
	25	26	27	28	29	30	31		
JANUARY 2024	1	2	3	4	5	6	7	6th-Parents meet 2nd- Internal Assessment 29th,30th & 31st	26th – Republic Day
	8	9	10	11	12	13	14		
	15	16	17	18	19	20	21		
	22	23	24	25	26	27	28		
	29	30	31						
FEBRUARY 2024				1	2	3	4	Lab IA : 26th to 28th 28th – National Science Day	
	5	6	7	8	9	10	11		
	12	13	14	15	16	17	18		
	19	20	21	22	23	24	25		
	26	27	28	29					
MARCH 2024					1	2	3	2nd- Internal Assessment 29th,30th & 31st	8th - Mahashivarathri
	4	5	6	7	8	9	10		

Total No. of Working days : 87		
Internship	25-10-2023 To 23-11-2023	
VTU ODD Semester Exams		
Practical Exams	11-03-2024 To 20-03-2024	Theory Exams	22-03-2024 To 20-04-2024
	Holidays	Parents Meeting
	Internal Assessment Tests	
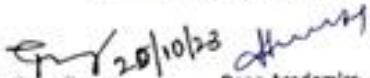
Co-ordinator
(Prof. Nataraja G)
20/10/23
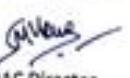
Dean Academics
(Dr. Madhukeshwara N)
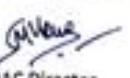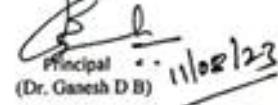
IQAC Director
(Dr. Santosh Henur)

Principal
(Dr. Ganesh D B)
20/10/23

Institute Academic Calendar of Events

ODD SEMESTER

ARKA Educational & Cultural Trust (Regd.)
JGi Jain Institute of Technology, Davanagere.

ACADEMIC CALENDAR
VII SEMESTER B.E. 2023-24 (SEPT 2023 – JAN 2024)

Commencement of the Semester: 14/08/2023

Last working day: 06/01/2024

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
SEPTEMBER 2023	11	12	13	14	15	16	17	11th -Commencement of Classes 15th -Engineer's day	18th - Ganesha Chaturthi 28th - Ed-Milaad
	18	19	20	21	22	23	24		
	25	26	27	28	29	30			
OCTOBER 2023	2	3	4	5	6	7	1 8	1st -Internal Assessment 16th,17th & 18th 21st -Graduation Day	2nd - Gandhi Jayanthi 14th - Mahalaya Amavase 23rd -Aayudha pooja 24th -Vijayadashami 28th -Valmiki Jayanthi
	9	10	11	12	13	14	15		
	16	17	18	19	20	21	22		
	23	24	25	26	27	28	29		
	30	31							
NOVEMBER 2023			1	2	3	4	5	4th -Parents meet 2nd Internal Assessment 23rd,24th & 25th	1st - Kannada Rajyotsava 14th - Balipadyami 30th -Kanakadasa Jayanthi
	6	7	8	9	10	11	12		
	13	14	15	16	17	18	19		
	20	21	22	23	24	25	26		
	27	28	29	30					
DECEMBER 2023					1	2	3	22nd – National Mathematics Day Lab IA 28th to 30th	25th - Christmas
	4	5	6	7	8	9	10		
	11	12	13	14	15	16	17		
	18	19	20	21	22	23	24		
	25	26	27	28	29	30	31		
JANUARY 2024	1	2	3	4	5	6		3rd -Internal Assessment 1st,2nd & 3rd	

Total No. of Working days : 91	
Internship	14-08-2023 To 09-09-2023

VTU ODD Semester Exams

Practical Exams	08-01-2024 To 19-01-2024	Theory Exams	22-01-2024 To 09-02-2024

Holidays	Parents Meeting
Internal Assessment Tests	

Co-ordinator 11/8/23
(Prof. Nataraja G)

Dean Academics
(Dr. Madhukeshwara N)

IQAC Director
(Dr. Santosh Herur)

Principal 11/08/23
(Dr. Ganesh D B)

Department Academic Calendar of Events

ODD SEMESTER

ARKA Educational & Cultural Trust (Regd.)
JGi Jain Institute of Technology, Davanagere.
DEPARTMENTACADEMIC CALENDAR
VII SEMESTER B.E. 2023-24 (SEPT 2023 – JAN 2024)

Commencement of Classes: 11/09/2023   **Last working day: 06/01/2024**

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
SEPTEMBER 2023	11	12	13	14	15	16	17	11ᵗʰ -Commencement of Classes 15ᵗʰ -Engineer's day	18ᵗʰ - Ganesha Chaturthi 28ᵗʰ – Ed-Milaad
	18	19	20	21	22	23	24		
	25	26	27	28	29	30			
OCTOBER 2023							1	1ˢᵗ -Internal Assessment 16ᵗʰ,17ᵗʰ& 18ᵗʰ 21ˢᵗ -Graduation Day 3rd week Internship Presentation	2ⁿᵈ- Gandhi Jayanthi 14ᵗʰ - Mahalaya Amavase 23ʳᵈ -Aayudha pooja 24ᵗʰ- Vijayadashami 28ᵗʰ -Valmiki Jayanthi
	2	3	4	5	6	7	8		
	9	10	11	12	13	14	15		
	16	17	18	19	20	21	22		
	23	24	25	26	27	28	29		
	30	31							
NOVEMBER 2023			1	2	3	4	5	4ᵗʰ -Parents meet 2ⁿᵈ Internal Assessment 23ʳᵈ,24ᵗʰ& 25ᵗʰ 4th week Project Phase – I Presentation	1ˢᵗ- Kannada Rajyotsava 14ᵗʰ - Balipadyami 30ᵗʰ -Kanakadasa Jayanthi
	6	7	8	9	10	11	12		
	13	14	15	16	17	18	19		
	20	21	22	23	24	25	26		
	27	28	29	30					
DECEMBER 2023					1	2	3	Lab IA 18ᵗʰ to 30ᵗʰ	25ᵗʰ- Christmas
	4	5	6	7	8	9	10		
	11	12	13	14	15	16	17		
	18	19	20	21	22	23	24		
	25	26	27	28	29	30	31		
JANUARY 2024	1	2	3	4	5	6		3ʳᵈ -Internal Assessment 1ˢᵗ,2ⁿᵈ& 3ʳᵈ	

Total No. of Working days : 91	
Internship	14-08-2023 To 09-09-2023
VTU ODD Semester Exams	

Practical Exams	08-01-2024 To 19-01-2024	Theory Exams	22-01-2024 To 09-02-2024

Holidays		Parents Meeting
Internal Assessment Tests		

Academic Co-ordinator
Prof. Savita Mamadapur

IQAC Director
(Dr. Santosh Herur)
Jain Institute of Technology
Davangere-577001

Institute Academic Calendar of Events

EVEN SEMESTER

ARKA Educational & Cultural Trust (Regd.)
Jain Institute of Technology, Davanagere. IQAC
323, Near VeereshwaraPunyashrama, Avaragere, Davangere- 577005.

ACADEMIC CALENDAR
IV SEMESTER B.E 2023-24 (APRIL 2024 – AUGUST 2024)

Commencement of the semester: 22/04/2024   Last working day: 07/08/2024

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
APRIL	22	23	24	25	26	27	28	22rd –Commencement of Classes	
	29	30							
MAY			1	2	3	4	5	6th –Shrunga Inauguration	
	6	7	8	9	10	11	12	22nd –Ethnic Day	1st – Labour's day
	13	14	15	16	17	18	19	24th –Sports Day	10th Basava Jayanthi
	20	21	22	23	24	25	26	25th – Shrunga	
	27	28	29	30	31				
JUNE						1	2	1st Internal Assessment	
	3	4	5	6	7	8	9	3rd ,4th ,5th	17th – Bakrid
	10	11	12	13	14	15	16	8th – Parents meet	
	17	18	19	20	21	22	23		
	24	25	26	27	28	29	30		
JULY	1	2	3	4	5	6	7	2nd Internal Assessment	
	8	9	10	11	12	13	14	4th ,5th & 6th	17th – Last Day of
	15	16	17	18	19	20	21	3rd Internal Assessment	Muharram
	22	23	24	25	26	27	28	29th ,30th, & 31st	
	29	30	31						
AUGUST				1	2	3	4	1st,2nd & 3rd –Lab IA	
	5	6	7						

Total Number of Working days : 89
Internship
VTU EVEN SEMESTER EXAMS

Practical Exams	08-08-2024 To 17-08-2024	Theory Exams	19-08-2024 To 12-09-2024
	Holidays		Parents Meeting
	Internal Assessment Tests		

Co-ordinator   Dean Academics   IQAC Director   Principal
(Dr. Nataraja G)   (Dr. Madhukeshwara N)   (Dr. Santosh Herur)   (Dr. Ganesh D B)

Institute Academic Calendar of Events

EVEN SEMESTER

ARKA Educational & Cultural Trust (Regd.)
JGi Jain Institute of Technology, Davanagere. IQAC
 # 323, Near VeereshwaraPunyashrama, Avaragere, Davangere- 577005.

ACADEMIC CALENDAR
VI SEMESTER B.E 2023-24 (APRIL 2024 – AUGUST 2024)

Commencement of the semester: 29/04/2024

Last working day: 31/07/2024

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
APRIL	29	30						29th –Commencement of Classes	
MAY	6	7	1	2	3	4	5	6th –Shrunga Inauguration	
	13	14	8	9	10	11	12	22nd –Ethnic Day	
	20	21	15	16	17	18	19	24th –Sports Day,	1st – Labour's day
	27	28	22	23	24	25	26	25th – Shrunga	10th Basava Jayanthi
			29	30	31				
JUNE	3	4	5	6	7	1	2	1st Internal Assessment	
	10	11	12	13	14	8	9	3rd ,4th ,5th	
	17	18	19	20	21	15	16	8th – Parents meet	17th – Bakrid
	24	25	26	27	28	22	23		
						29	30		
JULY	1	2	3	4	5	6	7	2nd Internal Assessment	
	8	9	10	11	12	13	14	4th ,5th & 6th	
	15	16	17	18	19	20	21	25th,26th & 27th –Lab IA	
	22	23	24	25	26	27	28	3rd Internal Assessment	17th – Last Day of Muharram
	29	30	31					29th,30th, & 31st	

Total Number of Working days : 77

VTU EVEN SEMESTER EXAMS

Practical Exams	01-08-2024 To 10-08-2024	Theory Exams	12-08-2024 To 14-09-2024
	Holidays		Parents Meeting
	Internal Assessment Tests		

Co-ordinator
(Dr. Nataraja G)

Dean Academics
(Dr. Madhukeshwara N)

IQAC Director
(Dr. Santosh Herur)

Principal
(Dr. Ganesh D B)

Institute Academic Calendar of Events

EVEN SEMESTER

ARKA Educational & Cultural Trust (Regd.)
Jain Institute of Technology, Davanagere. IQAC
323, Near VeereshwaraPunyashrama, Avaragere, Davangere- 577005.

ACADEMIC CALENDAR
VIII SEMESTER B.E 2023-24 (FEB 2024 – MAY 2024)

Commencement of Semester: 12/02/2024   Last working day: 11/05/2024

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
FEBRUARY	12	13	14	15	16	17	18	12ᵗʰ –Commencement of Classes 28ᵗʰ –National Science Day	
	19	20	21	22	23	24	25		
	26	27	28	29					
MARCH					1	2	3	1ˢᵗ Internal Assessment 11ᵗʰ,12ᵗʰ & 13ᵗʰ 30ᵗʰ –Parents meet	8ᵗʰ –Maha shivaratri 29ᵗʰ – Good Friday
	4	5	6	7	8	9	10		
	11	12	13	14	15	16	17		
	18	19	20	21	22	23	24		
	25	26	27	28	29	30	31		
APRIL	1	2	3	4	5	6	7	2ⁿᵈ Internal Assessment 4ᵗʰ,5ᵗʰ & 6ᵗʰ 3ʳᵈ Internal Assessment 27ᵗʰ 29ᵗʰ & 30ᵗʰ	9ᵗʰ – Chandramana Ugadi 11ᵗʰ – Quteb-E-Ramzan
	8	9	10	11	12	13	14		
	15	16	17	18	19	20	21		
	22	23	24	25	26	27	28		
	29	30							
MAY			1	2	3	4	5	2ⁿᵈ –Ethnic Day & Shrunga Inauguration. 3ʳᵈ –Sports Day 4ᵗʰ –Shrunga 6ᵗʰ –Farewell	1ˢᵗ – Labour's day 10ᵗʰ –Basaveshwara Jayanthi
	6	7	8	9	10	11			

Total Number of Working days :72		
VTU EVEN SEMESTER EXAMS		
Theory Exams	13-05-2024 To 21-05-2024	Internship Viva Voce/Project Viva	23-05-2024 To 30-05-2024

	Holidays	Parents Meeting
	Internal Assessment Tests	

Co-ordinator
(Dr. Nataraja G)

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IQAC Director
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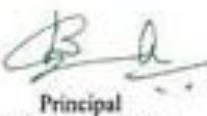
Principal
(Dr. Ganesh D B)

Department Academic Calendar of

EVEN SEMESTER

ARKA Educational & Cultural Trust (Regd.)

JGi Jain Institute of Technology, Davanagere. IQAC

323, Near VeereshwaraPunyashrama, Avaragere, Davangere- 577005.

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
DEPARTMENT CALENDAR OF EVENTS

IV SEMESTER B.E 2023-24 (APRIL 2024 – AUGUST 2024)

Commencement of the semester: 22/04/2024   Last working day: 07/08/2024

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
APRIL	22	23	24	25	26	27	28	22nd –Commencement of Classes	
	29	30							
MAY			1	2	3	4	5	6th –Shrunga Inauguration	
	6	7	8	9	10	11	12	22nd –Ethnic Day	
	13	14	15	16	17	18	19	24th –Sports Day	1st – Labour's day
	20	21	22	23	24	25	26	25th – Shrunga	10th –Basava Jayanthi
	27	28	29	30	31			4th Week – Forum Activity	
JUNE						1	2	1st Internal Assessment	
	3	4	5	6	7	8	9	3rd,4th,5th	
	10	11	12	13	14	15	16	8th – Parents meet	17th – Bakrid
	17	18	19	20	21	22	23	4th Week – Forum Activity	
	24	25	26	27	28	29	30		
JULY	1	2	3	4	5	6	7	2nd Internal Assessment	
	8	9	10	11	12	13	14	4th,5th & 6th	17th – Last Day of
	15	16	17	18	19	20	21	2nd Week – Forum Activity	Muharram
	22	23	24	25	26	27	28	3rd Internal Assessment	
	29	30	31					29th,30th, & 31st	
AUGUST				1	2	3	4	1st,2nd & 3rd –Lab IA	
	5	6	7						

Total Number of Working days : 89
Internship
VTU EVEN SEMESTER EXAMS

Practical Exams	08-08-2024 To 17-08-2024	Theory Exams	19-08-2024 To 12-09-2024
	Holidays		Parents Meeting
	Internal Assessment Tests		

Academic Co-ordinator
(Prof. Savita Mamadapur)

Professor & Head
(Dr. Santosh Herur)

Department Academic Calendar of

EVEN SEMESTER

ARKA Educational & Cultural Trust (Regd.)

JGi Jain Institute of Technology, Davanagere. IQAC

323, Near VeereshwaraPunyashrama, Avaragere, Davangere- 577005.

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
DEPARTMENT CALENDAR OF EVENTS
VI SEMESTER B.E 2023-24 (APRIL 2024 – AUGUST 2024)

Commencement of the semester: 29/04/2024　　　　**Last working day: 31/07/2024**

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
APRIL	29	30						29th –Commencement of Classes	
MAY			1	2	3	4	5	6th –Shrunga Inauguration 22nd –Ethnic Day 24th –Sports Day, 25th – Shrunga 4th Week – Forum Activity	1st – Labour's day 10th Basava Jayanthi
	6	7	8	9	10	11	12		
	13	14	15	16	17	18	19		
	20	21	22	23	24	25	26		
	27	28	29	30	31				
JUNE						1	2	1st Internal Assessment 3rd ,4th, 5th 8th – Parents meet 4th Week – Forum Activity	17th – Bakrid
	3	4	5	6	7	8	9		
	10	11	12	13	14	15	16		
	17	18	19	20	21	22	23		
	24	25	26	27	28	29	30		
JULY	1	2	3	4	5	6	7	2nd Internal Assessment 4th ,5th & 6th 25th,26th & 27th –Lab IA 2nd Week – Forum Activity 3rd Internal Assessment 29th ,30th, & 31st	17th – Last Day of Muharram
	8	9	10	11	12	13	14		
	15	16	17	18	19	20	21		
	22	23	24	25	26	27	28		
	29	30	31						

Total Number of Working days : 77

VTU EVEN SEMESTER EXAMS			
Practical Exams	01-08-2024 To 10-08-2024	**Theory Exams**	12-08-2024 To 14-09-2024

	Holidays		Parents Meeting
	Internal Assessment Tests		

Academic Co-ordinator
(Prof. Savita Mamadapur)

Professor & Head
(Dr. Santosh Herur)

TECH TALK

Electric VLSI Design Tool is an open-source Electronic Design Automation (EDA) software used for learning and developing integrated circuit designs. In VLSI engineering, EDA tools play a vital role in converting circuit ideas into manufacturable IC layouts. Electric is widely adopted in academic institutions because it provides a complete VLSI design environment without the high licensing cost of commercial tools. The tool was originally developed at **Sun Microsystems** and later supported by the **Free Software Foundation**, making it freely available for education and research.

Electric differs from conventional layout tools because it follows a **connectivity-based design approach** rather than simple drawing. In this method, every wire and component is electrically intelligent, which helps in automatically identifying errors during design. The tool supports both **schematic capture and IC layout** within the same platform, allowing designers to move easily from circuit design to physical layout. This integrated approach helps students clearly understand the relationship between logical circuits and their physical realization on silicon.

The Electric VLSI Design Tool supports standard hardware description languages such as **Verilog** and **VHDL**, enabling digital circuit modeling and verification. It also provides built-in facilities for **Design Rule Check (DRC)** and **Layout Versus Schematic (LVS)** checking, which are essential steps in VLSI design to ensure that the layout meets fabrication rules and matches the original schematic. These features make Electric suitable for understanding the complete VLSI design flow.

The typical VLSI design flow using Electric begins with circuit specification and schematic design, followed by functional simulation to verify correctness. After simulation, the designer creates the IC layout using CMOS design rules. The layout is then verified using DRC to check manufacturing constraints and LVS to ensure consistency between the schematic and layout. Performing all these steps within a single tool provides strong conceptual clarity for beginners in VLSI.

Electric is primarily used in **VLSI laboratory courses, research projects, and introductory IC design training**. It is especially helpful in institutions where access to commercial EDA tools like **Cadence Virtuoso** or **Synopsys Design Compiler** is limited due to

cost. Through Electric, students gain hands-on experience in CMOS logic design, layout techniques, and verification methods.

One of the major advantages of Electric is that it is free, open-source, and platform independent, making it accessible to all learners. Its graphical user interface is beginner-friendly and well suited for teaching fundamental VLSI concepts. However, Electric has some limitations, such as reduced automation and limited suitability for very large or industry-level designs. Therefore, while it is excellent for learning and prototyping, it is not commonly used in large-scale semiconductor industries.
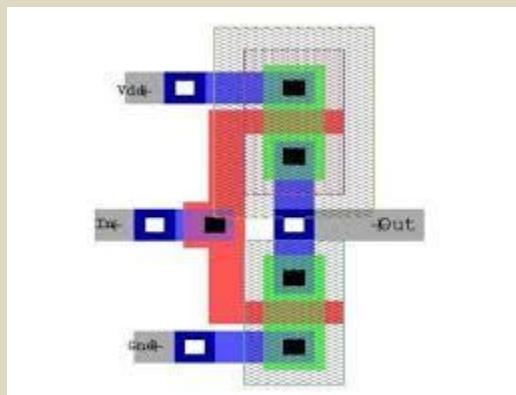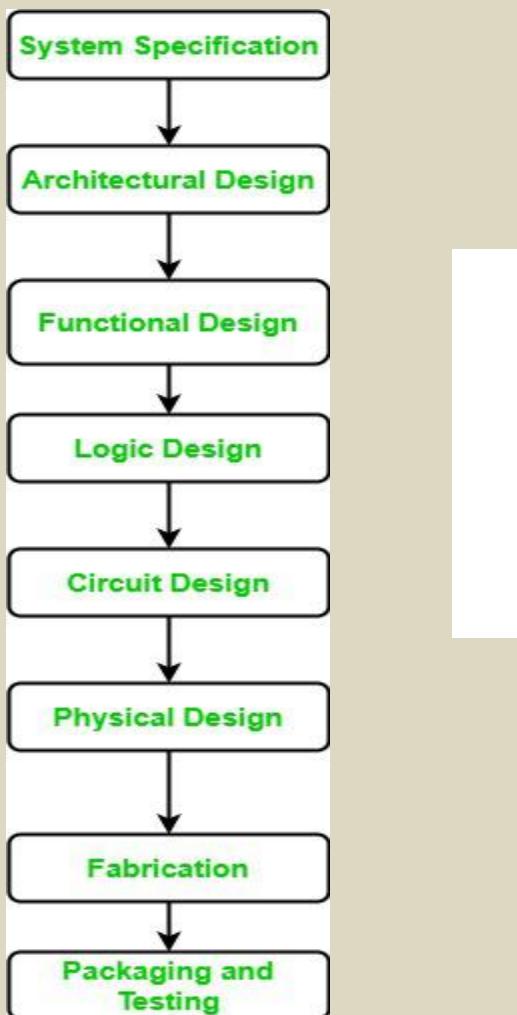
Mr. Ravi Rayappa
Assistant Professor
Dept. of ECE.

"A Talk on Placement Training"

Images of Talk on Placement Training.

Dr. Prakash M Walavalkar, Dean Training & Placement, JITD, addressed the second year students of ECE department about placement training provided by Q-spider on C Programming skills. Dr. Santosh Herur, HOD, Dept. of ECE and Prof.

Deepthi G B, Placement coordinator, Dept. of ECE also addressed and encouraged the students to make best use of various trainings provided by training and placement cell and attend future placement drives with confidence.

"Faculty's Participation":

Mrs. Savita Mamadapur, Assistant Professor, Dept. of ECE has successfully completed Innovation Ambassador (IA) training both 'Foundation Level and Advanced Level' conducted in online mode by MoE's Innovation Cell and AICTE during the IIC Calendar year 2022-23.

Image of Faculty Participation.

"Faculty – Outside World Interaction"

Images of Faculty- Outside Interaction.

Dr. Santosh Herur, HOD, Dept. of ECE , invited as Resource Person to carryout sessions for the one day workshop on **'Embedded System Basics'** at Government Engineering College, Haveri on 16th August 2023.

"Student's Achievement: Campus Placement"

Images of Student Achievement.

Prathibha M H and **Varsha V** of 6th sem ECE have been recruited for **Technologics Global Pvt. Ltd, Bangalore**, as a Trainee Application Engineer. Hearty congratulations and best wishes from College Management, Principal, Placement officer, HOD, Placement Coordinator, Teaching and Non- teachning staff members.

Faculty – Outside World Interaction as Resource Person
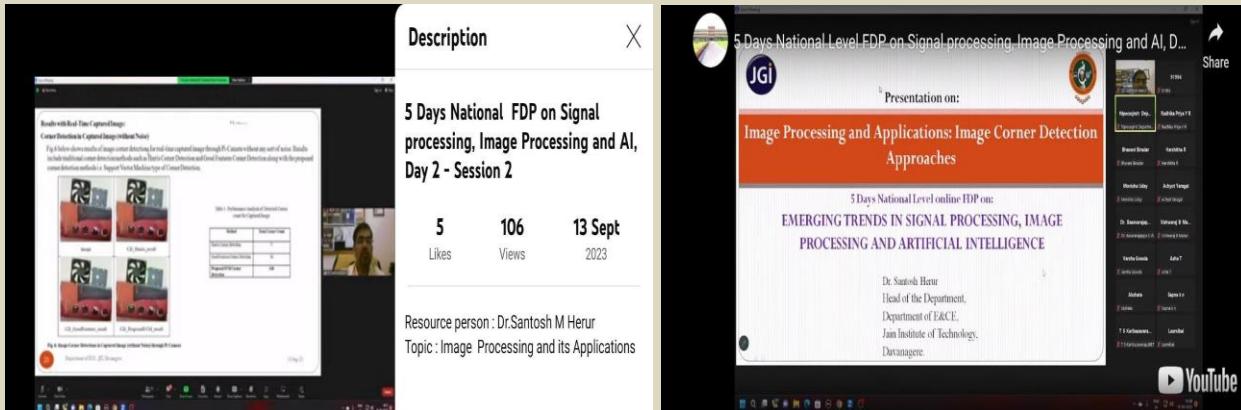
Images of Faculty- Outside Interaction.

Dr. Santosh Herur, HOD, Dept. of ECE , invited as Resource Person on 13th September 2023 for Five days **National Level Faculty Development Program** on "Emerging Trends in Signal Processing, Image Processing and Artificial Intelligence" conducted by SJMIT, Chitradurga from 12th to 16th September 2023 to present a session on **Image Processing and its Applications.**

A Social Initiative by forum ENCORE

Images of Social Initiative.

The students of I year ECE, donated amount Rs. 1,100/- (One Thousand One Hundred only) towards the International White Cane Day. It is a noble cause helping the blind people to provide essential kits for them. Dr. Santosh Herur,HOD, Dept. of ECE, Dr. Rajendra S Soloni, and Mr. Sharanabasaveshwara H B, Forum Coordinator encouraged the students to do such social initiatives.

A Hands-on session on Cadence tool

Images of Hands on Session On Cadence tool.

In the view of cadence pool drive, a Hands on session on cadence tool was held on 25/10/23 from 10:00 AM to 5:00 PM at EEE seminar hall for ECE and EEE students. The resource person Dr. srinivas Rao udara discussed about the analog and digital circuits design, hardware description language, VLSI tools and interview questions.

Cadence Pool Campus Drive

Image of Pool Campus Drive.

Final year students of ECE department attended Cadence Pool campus drive at GMIT, Davangere. Dr. Prakash M Walavalkar, Dean Training and Placement, Dr. Ravirayappa, Assistant Professor, Dept. of ECE and Prof. Deepthi G B, Placement Coordinator , Dept. of ECE, accompanied the students and guided them.

5 Days Faculty Development Program on "Ability Enhancement Course-IoT Lab"

Images of 5 Days Faculty Development program.

A Visvesvaraya Technological University(VTU), Belagavi and Indian Society for Technical Education(ISTE), New Delhi Sponsored 5 days Faculty Development Program on "Ability Enhancement Course-IoT Lab" was organized by the Department of Electronics and Communication Engineering, JITD in Collaboration with Manipal School of Information Sciences, Manipal from 06th to 10th November 2023. Hon'ble Vice-Chancellor, VTU, Belagavi, Dr. Vidyashankar S joined the inauguration of FDP virtually and conveyed his best wishes. He emphasized on the importance of IoT in the growing needs of Industry. He also assured that VTU will always encourage such training and learning programs. Dr. Manjappa Sarathi, Advisor, JITD suggested that this kind of practical sessions are required for faculty members to up skill themselves as per NEP curriculum. Dr. Ganesh D B, Principal & Director, JITD, highlighted on the essentiality of gaining practical knowledge to meet the needs of emerging trends like IoT as technical individuals. He said as per the industry demands the curriculum also constantly being updated by the university so it is the responsibility of we the faculty members to up skill our students as per recent industry trends. Speaking on the Occasion, He welcomed the participants from various institutions across Karnataka for the FDP at JITD. Dr.Santosh Herur, Professor & HOD, Dept. of ECE gave brief insights of FDP, its goals and objectives. After the inauguration of FDP a well-equipped IoT Lab has been inaugurated by the dignitaries at E&CE Department of JITD. Dr. Santosh Herur, Professor & HOD, Dept. of ECE & Convener of FDP, Resource Persons of the FDP, Dr. Prathviraj N, Prof. Raghudathesh G P, Prof. Raviteja B and coordinators of FDP, Dr. Rajendra Soloni and Prof. Sharanabasaveshwara H B, Deans and Heads of various Departments and Faculty members were present. A total of 45 Faculties attended the FDP.

On the Day-1 of FDP, In Morning Session the Resource Persons , Dr. Prathviraj and Prof. Raghudathesh G P gave Introduction to IoT and Prof. Raviteja B explained about Basics of Arduino. In Afternoon Session, Prof. Raviteja B explained about the Tinkercad tool and its uses and later Dr. Prathviraj, Prof. Raghudathesh G and Prof. Raviteja B carried out handson session on IoT Lab Experiment-1 as per 5th semester VTU 2021 Scheme. A total of 45 faculty members attended the session.

Faculty Development Program on "Ability Enhancement Course - IoT Lab" : Day-2

Images of 5 Days Faculty Development program-Day2.

On the Day-2 of FDP, In Morning Session the Resource Person, Prof. Raviteja B gave brief Introduction about DHT Sensor, its libraries and Node MCU-ESP8266 and also explained IoT Lab Experiment-2&3. In the afternoon Session, Resource Person, Prof. Raghudathesh G P explained about Basics of Raspberry Pi, its libraries and also explained about IoT lab experiments using Raspberry Pi. A total of 35 faculty members attended the session.

Faculty Development Program on "Ability Enhancement Course - IoT Lab" : Day-3
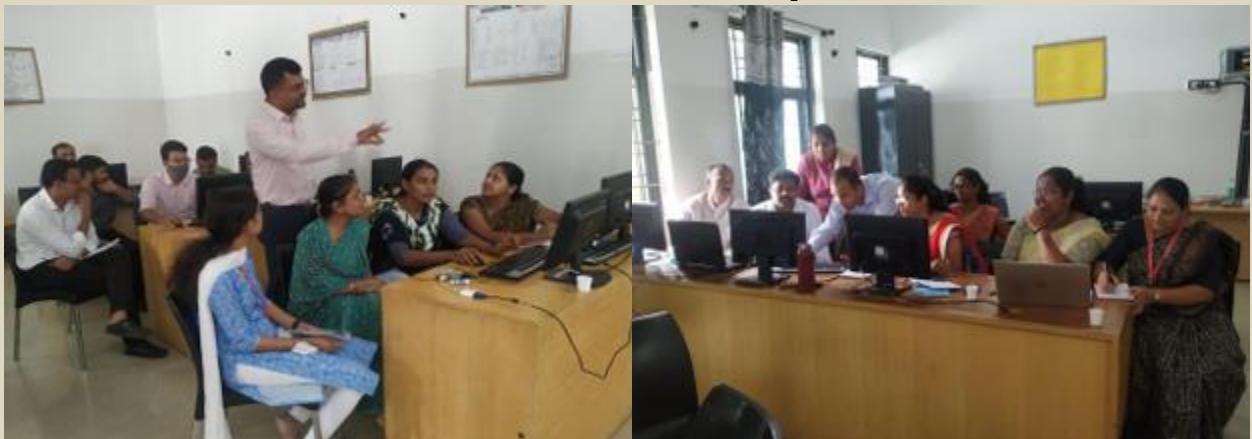
Images of 5 Days Faculty Development program-Day3.

On the Day-3 of FDP, In Morning Session the Resource Person, Dr. Prathviraj explained about CRUD operations on databases using My SQL server on Raspberry Pi. In the afternoon Session, Resource Person, Prof. Raviteja B explained about Thingspeak and other IoT softwares, its libraries and IoT Lab Experiments-4 & 5. And he also demonstrated interfacing of Bluetooth with ESP32S. A total of 35 faculty members attended the session.

Faculty Development Program on "Ability Enhancement Course - IoT Lab" : Day-4
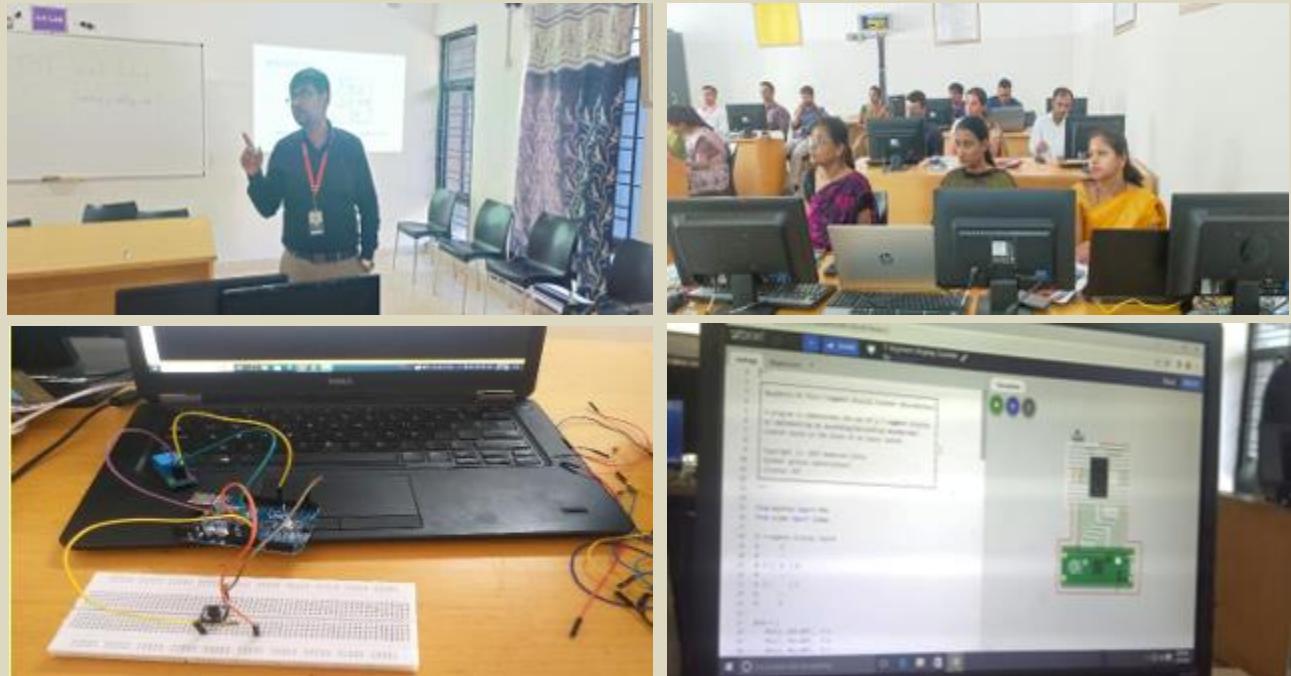
Images of 5 Days Faculty Development program-Day4

On the Day-4 of FDP, Dr. Santosh Herur, Professor & Head, Dept. of ECE and Convener of FDP conducted a session on IoT Security. He explained about architecture and Protocols of IoT and IIoT, ways to exploit IoT, different Malware attacks and techniques to secure IoT devices. He also demonstrated handson experiments on IoT. Later the participants carried out experiment based activities on IoT and Arduino. A total of 35 faculty members attended the session..

Faculty Development Program on "Ability Enhancement Course - IoT Lab" : Day-5
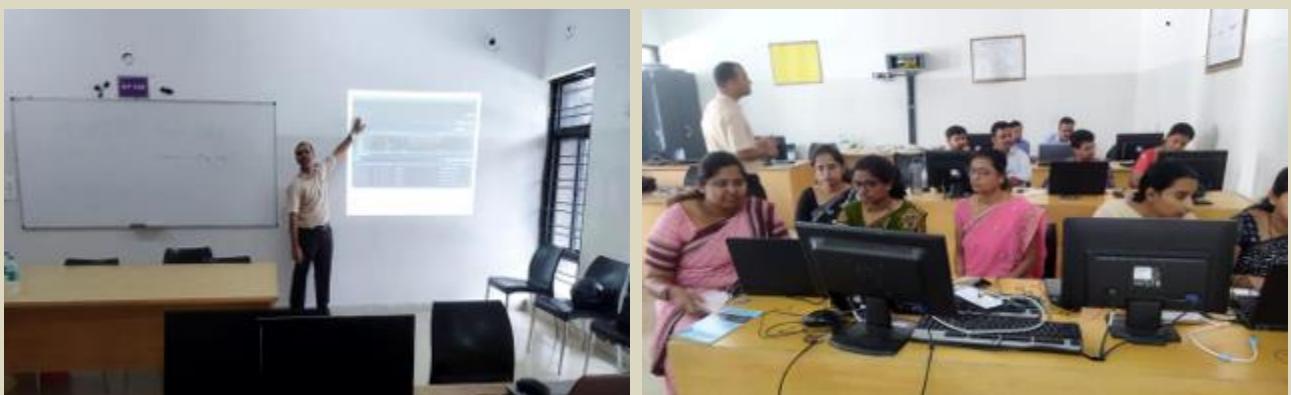
Images of 5 Days Faculty Development program-Day5.

On the Day-5 of FDP, Resource Person, Prof. Raviteja B highlighted on the concepts like TCP, UDP, OLED and also explained about IoT Lab Experiments-3,10 & 11 as per 5th semester VTU 2021 Scheme. A total of 35 faculty members attended the session.

VTU & ISTE sponsored 5-Days FDP on "Ability Enhancement Course - IoT Lab" Group Photo

Valedictory of 5 Days Faculty Development Program on "Ability Enhancement Course-IoT Lab"

Images of VTU & ISTE sponsored 5-Days FDP on "Ability Enhancement Course - IoT Lab"

A Visvesvaraya Technological University(VTU), Belagavi and Indian Society for Technical Education(ISTE), New Delhi Sponsored 5 days Faculty Development Program(FDP) on "Ability Enhancement Course-IoT Lab" was organized by the Department of Electronics and Communication Engineering, JITD in Collaboration with Manipal School of Information Sciences, Manipal from 06th to 10th November 2023. Dr. S K Shreedhara Murthy, BoS Member, Professor, Dept of E&CE were invited as the Guest of Honour. He briefed about the importance of IoT in the current industry trends and its application especially in the modernization of Agriculture. Dr. Ganesh D B, Principal & Director, JITD, highlighted on the needs of emerging trends in the Society. He also emphasized that it is essential to organize such FDP programs as they helps in upgrading both our faculties knowledge on new concepts that in turn helps in up skilling our students as per current industry requirements. Speaking on the Occasion, He congratulated the Resource persons and participants from various institutions across Karnataka for the Successful completion of FDP at JITD. Dr. Santosh Herur, Professor & HOD, Dept. of ECE gave brief highlights on the FDP. A total of 45 Faculties attended the FDP. Participants gave their valuable Feedback on the FDP. The Certificate of Participation were distributed to all the participants. Dr. Santosh Herur, Professor & HOD, Dept. of ECE & Convener of FDP, Resource Person of the FDP, Dr. Prathviraj N, Prof. Raghudathesh G P, Prof. Raviteja B and coordinators of FDP, Dr. Rajendra Soloni and Prof. Sharanabasaveshwara H B, Deans and Heads of various Departments and Faculty members were present.

Faculty Achievement

Sharanabasaveshwara H B, Assistant Professor, Dept. of ECE, JITD has participated in train the trainer organized by joint venture of ISTE & BNY Mellon.Completed the Skill Development Training Program on "Internet of Things –Phase IV" from 18/01/2023 to 25/02/2023 and scored above 90% in the final assessment under joint venture of ISTE & BNY Mellon.The ISTE has sponsored a kit worth of Rs. 8,000/- for the same.

Placement Drive

Images of Placement Drive.

Aptitude test was conducted for the Final year students of E&CE Department as a part of MITSOGO Placement drive. 20 students attended the online test.

"Webinar on " Interview Skills Mastery with AI Tools"

Images of Webinar on " Interview Skills Mastery with AI Tools.

Department of Electronics and Communication Engineering in association with forum Encore organized a webinar on Interview skills Mastery with AI Tools. Dr. Ravi Rayappa, Associate Professor, Dept. of E&CE, gave brief talk on importance of AI in interview skills. The resource person Mr. Vinod Kumar explained about resume making, email spam removing, PDF reading in short and also informed about getting ready for interview. Mr. Sharanabasaveshwara H B coordinated the event. A total of 145 participants attended the webinar including the students of III & V semester and faculty members.

Webinar on Women's Health Session with Gynecologist

Images of "Webinar on Women's Health Session with Gynecologist.

The teaching ,non-teaching female staff and female students of Jain Institute of Technology, Davanagere participated in Jain University's **"Women's Health Session with Gynecologist"** webinar on **7/12/2023**. The session, covering women's well-being, included insights into reproductive health, common concerns, lifestyle recommendations, and specific topics such as menstruation and menopause. Attendees received practical tips and preventive measures, fostering a community focus on personal well-being.

Session on "Industrial Market Demands for ECE Engineers

Images of "Session on "Industrial Market Demands for ECE Engineers"
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Department of Electronics and Communication Engineering in association with Samanvaya Alumini Association, JITD organized a session on "**Industrial Market Demands for ECE Engineers**". **Mr. Faraz Abdulla I**, Senior Lead Engineer at Hinduja Tech, Bangalore and

Alumini of JITD was invited as Resource Person for the session. He gave brief insights about demanding courses in E&CE that are having great industry exposure. He explained about the soft skills required for a fresher and how to improve them and what are the Industry demands at current scenario in detail. **Dr. Santosh Herur**, Professor & Head, Dept. of E&CE highlighted on various domains related to ECE in Industry, Corporate working atmosphere, how to focus and put efforts in gaining technical knowledge required to build career. All 3rd and 5th semester students of E&CE Dept. attend the session. **Dr.Ravirayappa**, Associate Professor, Dept. of E&CE coordinated the event.

INTEL INDIA STEM SKILLS PROGRAM FOR WOMEN: Day-1
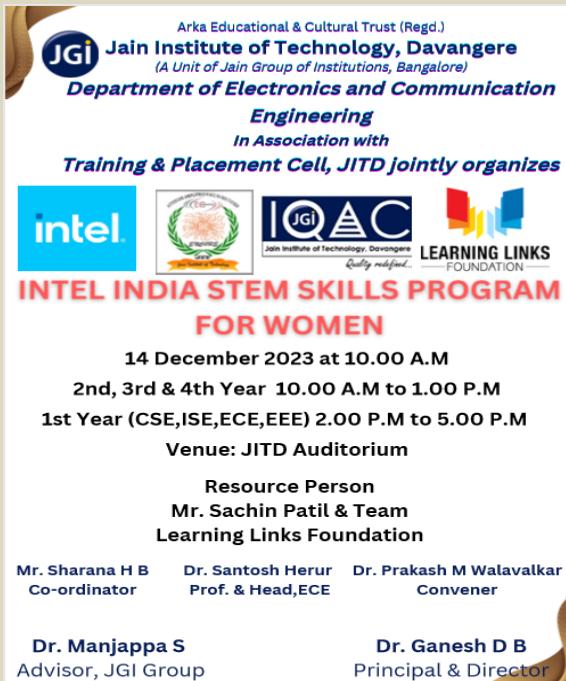
Images of INTEL INDIA STEM SKILLS PROGRAM FOR WOMEN: Day-1.

Department of Electronics and Communication Engineering in association with Training and Placement Cell, JITD has organized **INTEL INDIA STEM SKILLS PROGRAM FOR WOMEN** for all the girl students of circuit branches (CSE,ISE,ECE,EEE) on 14th December 2023. **Mr. Sachin Patil** and his team from Learning Link Foundation were invited as resource person for the event. He thought students about the basics of VLSI and informed the students the importance of VLSI and companies related to this stream and also explained the students about the soft skills and steps, methods to set goals. He also instructed students about time management. He helped students to register to the LMS from INTEL.

Dr. Ganesh D B, Principal & Director, JITD informed students to use this kind of platforms and have active participation in hackathons. **Dr. Manjappa S**, Advisor, JGI Group informed students regarding the importance of STEM and Women Empowerment. **Dr. Santosh Herur**, Professor & Head, Dept. of E&CE, **Dr. Prakash M Walavalkar**, Dean Training & Placement, JITD, Mr. Sharanabasaveshwara H B, Assistant Professor, E&CE and Placement coordinators of various department were present. A total of 465 students participated in the event.

INTEL INDIA STEM SKILLS PROGRAM FOR WOMEN : Day-2
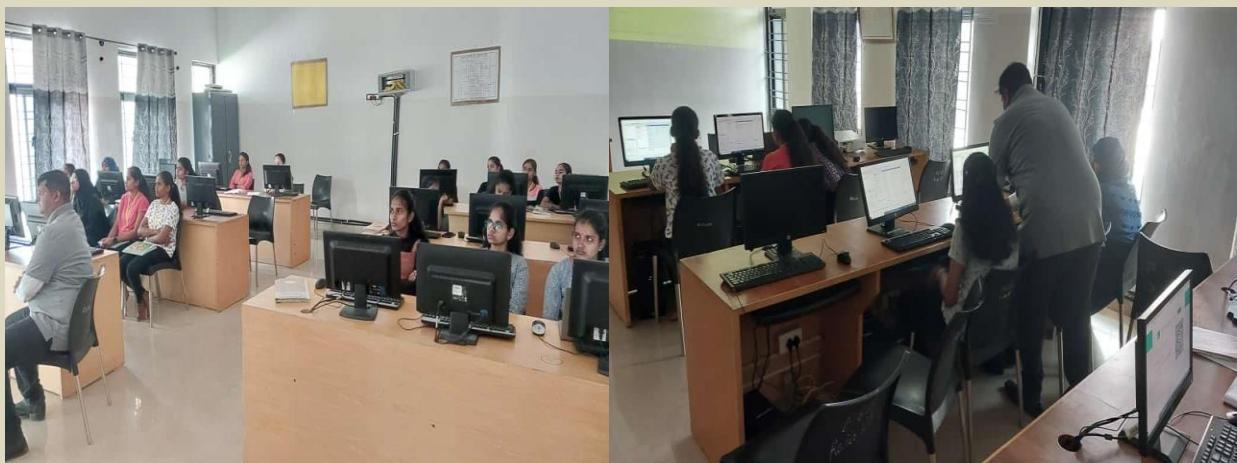
Mr. Sachin Patil and team delivered a lecture on **VLSI chip fabrication Process** on the 2nd day of the event (15-12-2023). He also helped in installing Intel Quartz tool in Lab and also for students.There was a hands-on session for students in the lab and 19 women students participated in the session. He also motivated the students to participate in the upcoming Hackathons of Intel.

Forum ENCORE Inauguration and Fresher's Day

Images of Forum ENCORE Inauguration and Fresher's Day

The Inauguration of Department of E&CE **Forum ENCORE** and Fresher's Day '**Fresh Beats-2K23**' was organized on 22nd December 2023 at JITD Auditorium. Chief Guest Mr. Dinesh Kumar, Alumni of E&CE, Entrepreneur, KPE Enterprise, Davangere, JITD Advisor, Dr. Manjappa Sarathi, Principal & Director, Dr. Ganesh D B, Head of Department, Dr. Santosh Herur, Forum Coordinator, Prof. Sharanabasaveshwara H B and Student Forum representatives inaugurated the event by lightening the Lamp.

Forum Coordinator, **Prof. Sharanabasaveshwara H B** gave brief insights about the events conducted under Forum ENCORE and upcoming events planned to conduct for the benefit of students.

Dr. Santosh Herur, Professor and Head, Dept. of E&CE addressed the gathering about career opportunities in ECE and motivated students to actively participate in all the Forum activities and Placement trainings to enhance Personal and Professional skills. He Highlighted on the mini-project implemented at department by final year students. He also urged students to come out of their comfort zone and work hard to achieve best things in life. Alumni and Chief guest of the event, **Mr. Dinesh Kumar** interacted with students and explained about the prerequisites to become an Entrepreneur, market strategies and skills required to start a business with examples.

Dr. Manjappa S, Advisor, JITD, addressed the students on benefits of accreditation in academics and encouraged students to focus on career, updating technical and personal skills as per industry demands. Academic toppers for the Academic Year 2022-23 were felicitated and the Students who have completed NPTEL course and students awarded with Silver Elite in such courses were honoured with cash prize by the dignitaries.

Dr. Ganesh D B, Principal and Director, JITD in his presidential remarks motivated students to make best use of Forum platform by actively participating in the events and to develop technical and professional skills through additional courses like NPTEL, SWAYAM etc. He also congratulated the academic toppers and students awarded with silver Elite in NPTEL courses. All the students, Teaching and non-teaching staff members of E&CE and heads of various department were present over the function.

Student's Achievement

Images of Student's Achievement.

Bhavana Gubber and **Chinmayi D G** of 3rd semester E&CE department has won **2nd Prize in IDEATHON event of AVISHKAR-23** , A Two Days State Level Symposium on "Artificial Intelligence for Biologists" organized by GMIT, Davangere conducted on 28th and 29th December 2023. Hearty congratulations to both the students.

A Webinar on "Mastering Cloud Database Management & Seamless Data Integra"
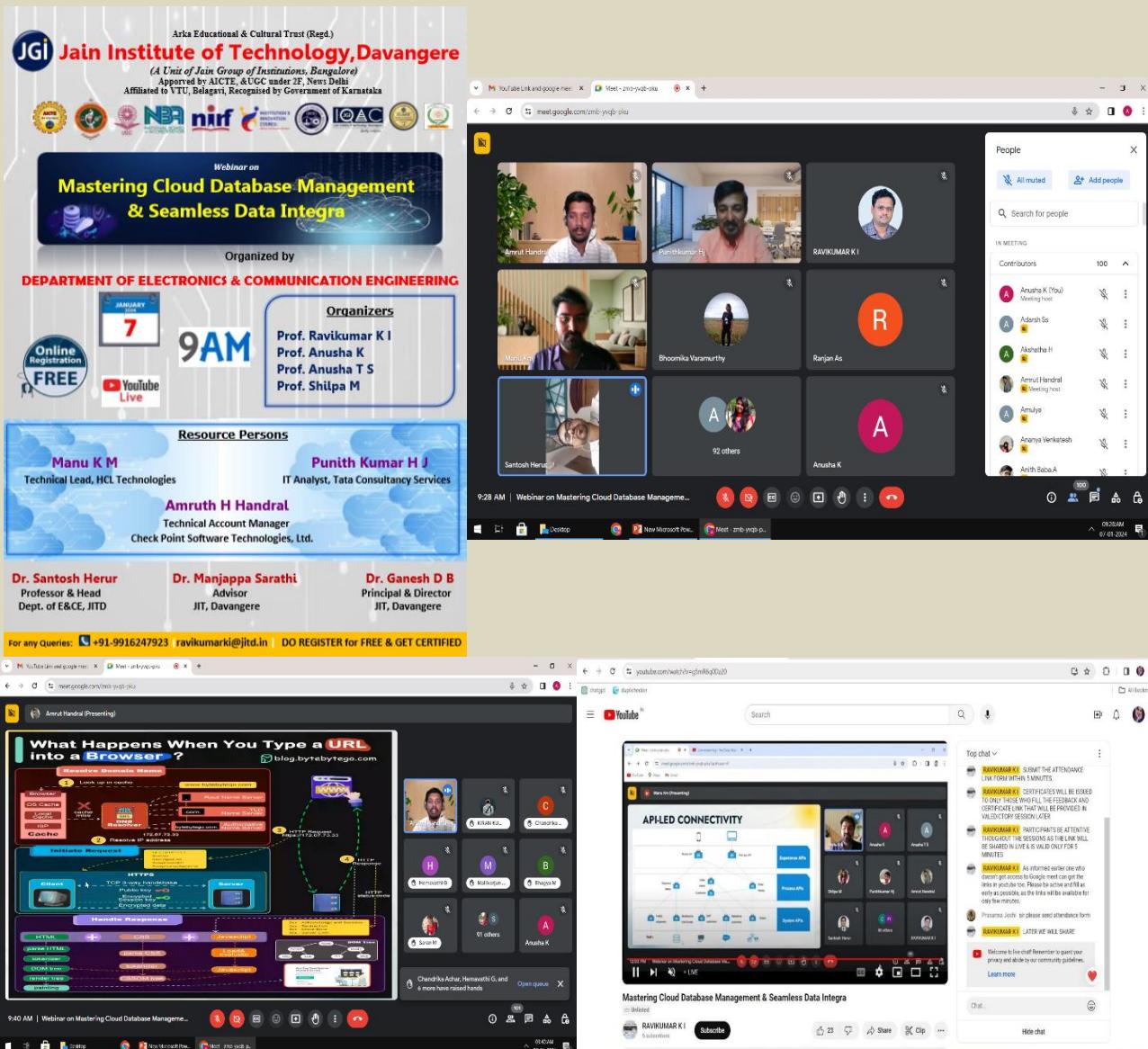
Images of Webinar on "Mastering Cloud Database Management & Seamless Data Integra".

A Webinar on **"Mastering Cloud Database Management & Seamless Data Integra"** was organized by the Department of Electronics & Communication Engineering on 07ᵗʰ January 2024 in Online mode. The Alumni's of E&CE, **Mr. Amruth H Handral**, Technical Account Manager, Check Point Software Technologies, Ltd., Bangalore, **Mr. Punith Kumar H J,** IT Analyst, Tata Consultancy Services, Bangalore and **Mr. Manu K M,** Technical Lead, HCL Technologies, Bangalore were the Resource Persons of the Webinar.

Dr. Santosh Herur, Professor & Head, Dept. of E&CE addressed the participants regarding industry demands of Cloud computing and its career in IT. He also asked student's to get practical exposure to such booming technologies by making best use of this webinar. All the Resource persons conducted different sessions on Cloud Computing, Data Base Management

and Data Integration. During the Sessions they briefly explained about the trending tech topics like Cloud Computing, Cloud services, AWS,DNS Servers, SQL, its Architecture, Enterprise Integration, its Architectures, API's etc,. They also conducted hands-on session on data integration using Anypoint Software. Assignments were given and a online Quiz was conducted for the participants. **Prof. Ravikumar K I, Prof. Anusha K, Prof. Anusha T S and Prof. Shilpa M** organized this webinar. Over 250 Participants from Jain Institute of Technology, Davangere, Hubli, Belgaum, Nagarjuna College of Engineering and GMIT attended the webinar through Google meet and YouTube Live. Online Certificates were made to send to all the attendees. The webinar got a total of 588 views in YouTube Live.

Faculty's Participation

Images of Faculty Participation".

Mrs. Savita Mamadapur and Mr. Sharanabasaveshwara H B, Assistant Professor, Dept. of E&CE has Participated in the High-End-Workshop sponsored by DST-SERB under Accelerate Vigyan Scheme on "Recent Trends on Applied Machine Learning for Speech and Bio-Signal Processing" held during 02nd to 07th January 2024 at the Indian Institute of Information Technology, Dharwad.

Student's Participation
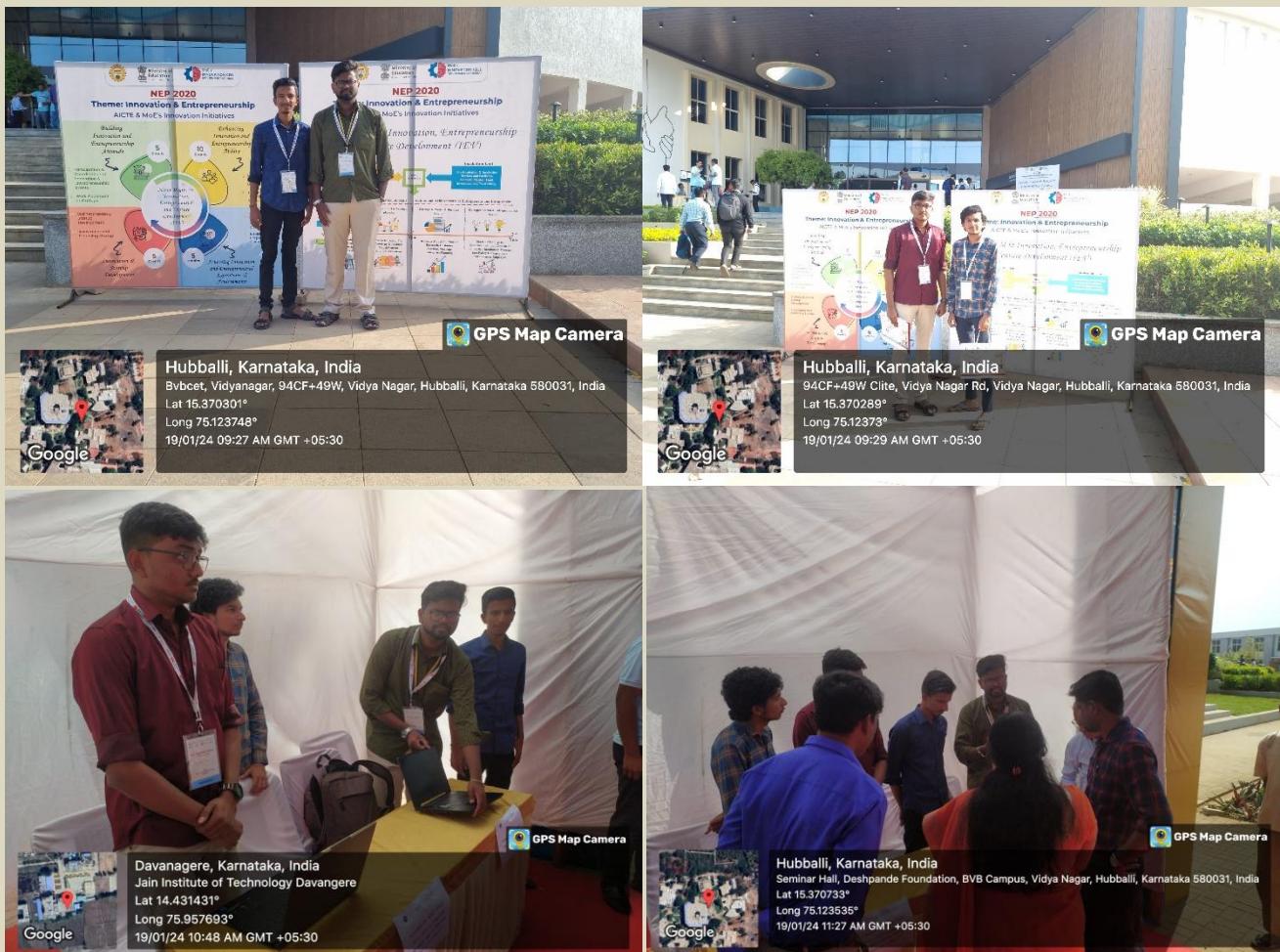
Images of Student Participation".

Two teams from Department of E&CE, under the guidance of Dr. Santosh Herur, Professor & Head, Dept. of E&CE and Dr. Ravirayappa, Associate Professor, Dept. of E&CE, Participated and Presented their ideas in **Yukti, Innovation/idea Challenge** at KLE University, Hubli.

JIT Celebrates Lord Sri Rama Prathistapana at Ayodhya.

Images of Lord Sri Rama Prathistapana at Ayodhya".

On the Occasion of Lord Sri Rama Prana Prastistapana at Ayodhya, the students and staff members of Jain Institute of Technology celebrated this special day at College by offering pooja to Lord Sri Rama and lightening Diya's in college campus. Sweets were distributed and annasantarpana was arranged by the students to remark this day. Dr. Ganesh D B, Principal & Director, Dr. Manjappa Sarathi, Advisor, JITD, Deans and heads of various departments, Teaching and Non-teaching staff members and students were present to witness this auspicious day.

JITD in Media:

Jain Institute of Technology, Davangere Students Innovative Projects Exhibition at KLEIT, Hubli, In Janthavani newspaper.

Student Achievement

Images of Student Achievement.

     Mr. Kiran Kumar S H, 5th Semester student from Electronics and communication Engineering Department, Attended Linked-in Learning Campus Ambassador Program in Linked-in Head-office, Bengaluru on 29/01/2024. And Successfully Completed Linked- in Campus Ambassador Program, and Became Certified Linked-in Learning Campus Ambassador for our college. Dr. Ganesh D.B, Principal and Director, Dr Prakash M Walavalkar, Dean-Training & Placement Cell, Dr. Santhosh Herur, Professor & Head, Department of ECE. Dr. Rajendra Soloni, Professor, Department of ECE, Congratulated him for his achievement.

Course Activity : Hobby Project Presentation

Images of Hobby Project Presentation.

Hobby Project Presentation was arranged in the department of E&CE as a part of course activity of the course Electronic Principles and Circuits on 1st February 2024. All 3rd semester students demonstrated their projects under the guidance of course coordinator Prof. Savitra C T. Principal & Director, Dr. Ganesh D B, Dr. Santosh Herur, Professor & Head, Dept. of E&CE and all the faculties of ECE Department evaluated the student projects, gave their valuable feedback and encouraged the students.

Student's Participation

Images of Student's Participation

All 3rd semester students of E&CE department attended a "Hands-on Generative AI workshop" in online mode.

HANDS ON GENERATIVE AI WORKSHOP
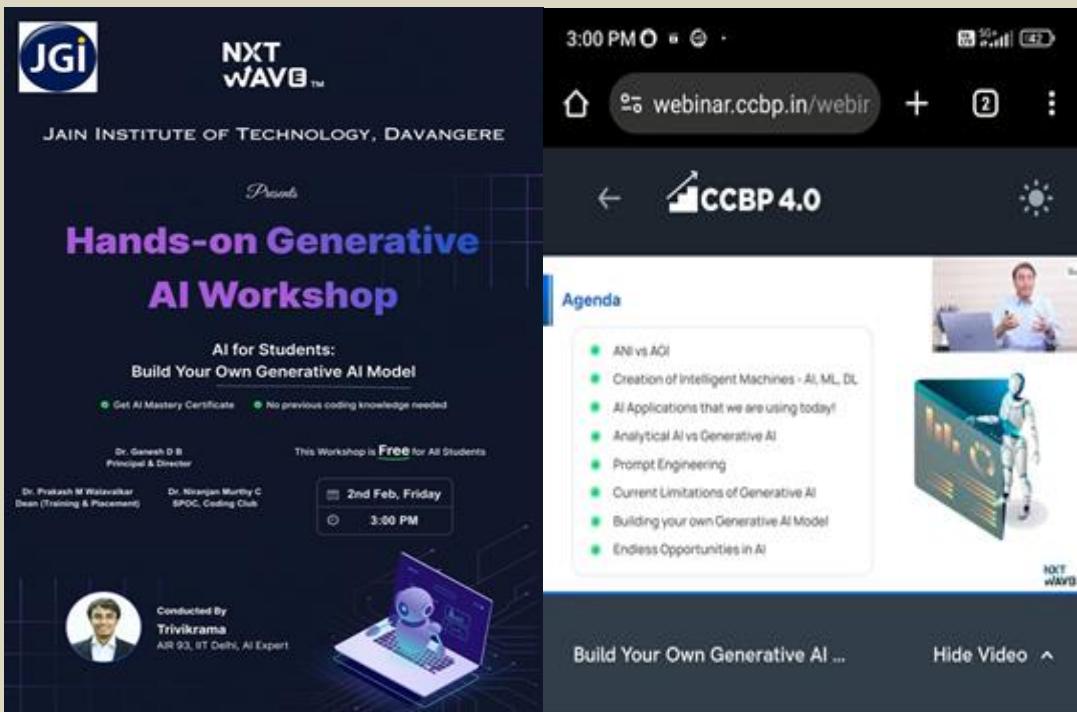
Images of Hands on Generative Ai Workshop.

Webinar have been organized under the banner of Training & Placement in association with coding club JITD Davangere, all the students of 3rd and 5th semester from various Departments had participated and received E-certificate.

Course Activity : Plantation & Adoption of a Tree

Images of Plantation & Adoption of a Tree.

3rd semester students of E&CE department planted rose plants as an activity on Module 1 : Plantation and Adoption of a Tree on 02nd February 2024. As Republic day theme, they also drew the map of BHARATH by saffron, white and green colour flowers. Prof. Sharanabasaveshwara H B, Prof. Rajeshwari H M Coordinated the activity.

Course Activity : Heritage Walk

Images of Heritage Walk.

3rd semester students of E&CE department conducted a 'Heritage walk' on 09th February 2024 as a part of their Course activity. The students visited the historical temples and places like Banavasi, Murudeshwara, sirsi and Yana. Prof Anusha T S and Mr. Pramod K V accompanied the students.

Faculty Outside World Interaction

Images of Faculty Outside World Interaction
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Dr. Santosh Herur, Professor & Head, Department of E&CE was invited as the chief guest to inaugurate the 5 Days Workshop on "PCB Design and Fabrication" organized by the Dept. of E&CE, Jain Polytechnic, Davangere on 12th February 2024.

Finalist : YUKTI Innovation Challenge 2023

An Innovation titled "INTELLIGENT AUTOMATION OF HOME, AGRICULTURE AND STREET LIGHT USING IOT" with Innovation ID- IR2022-795652 under the Mentorship of Dr. Ravirayappa, Associate Professor, Dept. of E&CE has been selected as finalist of YUKTI Innovation Challenge 2023 to participate in the Final Round of Assessment for Funding Assistance and Incubation Linkage Support from MoE's Innovation Cell & AICTE.

Out of 779 innovations gone through the pitching process, 370 are declared as finalists of the YUKTI Innovation Challenge 2023 held at KLE University, Hubli.

Image of Finalist : YUKTI Innovation Challenge 2023

Industrial Visit

Images of Industrial Visit.

  An Industrial Visit to "Deshpande Startups", Hubli was arranged by the department of E&CE to provide practical exposure to 5th Semester Students. Deshpande Startups is creating & nurturing an ecosystem in a non-metro with the objective to build an entrepreneurial ecosystem that serves the semi-urban and rural segments. In this Industrial visit, students gained information regarding the stages of startups, Problem statement, validation, solution, customer valuation, technology intervention, acoustic growth in startups. They also got insights about PCB designing, screen printing, conveyor and reflow and the further testing process on the PCB board like vibration test, dust test, resistance test. Prof. Shilpa M and Mr. Arun Kumar B N accompanied the students.

Course Activity : Sports BPEK359

Images of Course Activity.

Department of Electronics and Communication Engineering in association with Sports Department, JITD, organized Activity on Sports in line with VTU curriculum BPEK359. All the students of 3rd semester participated in an activity on sports. Boys participated in Volley Ball and Girls participated in Throw Ball. There were totally 70 students participating in the activity. Mr. Sharanabasaveshwara H B, Sports Coordinator, Dept. of ECE and Mr. Kotersh D S, Physical Director, JITD coordinated the event.

Voters awareness and Pledge program

NSS unit of Jain Institute of Technology, Davanagere, Organised Voters pledge program in EEE seminar in association with NSS unit JIT on 02-03-2024.Students were given awareness on the importance of voting by NSS program officer JIT.

Student's Achievement : Campus Placement

Congratulation posters for placed students.

Final year student's of E&CE Department, Mallikarjun M, Rakshith M Savanth, Mallikarjun S Patil and Vinayaka Ravindra Dandin has been recruited for the Brickslane Company. Hearty congratulations and best wishes to all the placed students from College Management, Principal, Advisor, Dean-Training & Placement, HOD, Placement Coordinators, Teaching and Non- teachning staff members.

Swachh Bharat Abhiyan and Waste Management 2024

Images of Swachh Bharat Abhiyan and Waste Management 202.

NSS unit of JIT Davangere in Association with Yuva brigade conducted swachh Bharat Abhiyan and waste management in JIT College campus the program involves awareness regarding cleanliness which was given by our chief guest Gajendra K S ,Director, Yuva brigade Davanagere. Student where divided into teams to conduct cleaning activity inside the college campus. Around 100 students participated in the event from various departments of college.

Conduction of Comprehensive Viva
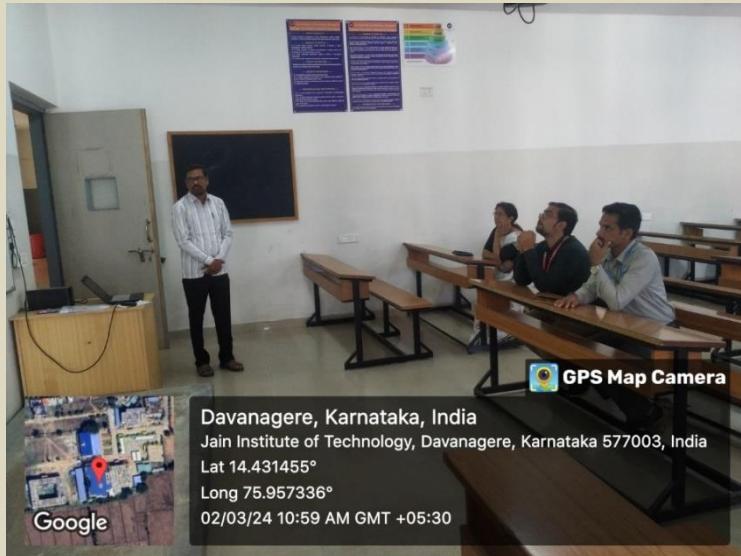
Image of Conduction of Comprehensive Viva.

    Mr. Ram Rao Boddu, Research student, Department of Electronics and Communication has presented his **Comprehensive Viva** on the research title **"Modified Hybrid Scheme of Orthogonal Frequency Division Multiplexing for Wireless Networks"** under the guidance of **Dr. Latha B M**, Professor and Head, Dept. of IS&E. During the Comprehensive Viva Doctoral committee member Dr. Rajashekhar B Somasagar, Associate Professor, Dept. of E&CE, GMIT, Davangere and Research center head, Dr. Santosh Herur, Professor and Head, Dept. of E&CE were present.

JITD in Media

    News in Janathavani Newspaper regarding Jain Institute of Technology Davanagere, students innovative projects granted with funds from MSME.

NAAC Review Meeting at Jain University Head Office, Bengaluru

Images of NAAC Review Meeting at Jain University Head Office.

NAAC Review meeting for Criteria-1 to 7 was held at Jain University Head Office, Bengaluru prior to IIQA Submission in presence of **Dr. Sundar Rajan**, Pro-Chancellor, Jain University and **Dr. Asha Rajiv**, Director-IQAC, Jain University. Few observations/remarks were made by the experts on some criteria that enable ourselves to submit IIQA for NAAC. **Dr. Santosh Herur**, Professor & Head, Dept. of ECE & Director- IQAC, JITD and **Dr. Rajendra Soloni**, NAAC Coordinator, JITD attended this review meeting on behalf of our Institute.

VTU LIC Visit for Permanent Affiliation

VTU LIC Committee visit was scheduled at 10:30 AM in the Department of E&CE. **Dr. S S Kerur**, Associate Professor, Dept. of E&CE, SDMCET, Dharwad & Expert member, VTU LIC Committee evaluated the files and its necessary supporting documents. He interacted with the faculty members of the department and also verified academic and co-curricular practices in the department. He gave valuable feedback and suggestions for the uplift of department. **Dr. Santosh Herur**, Professor & Head, Dept. of E&CE accompanied the expert member during the evaluation process. All the teaching and non-teaching staff members were present during the visit.

Image of VTU LIC Visit for Permanent Affiliation.

Student Achievement : Qualified in GATE Exam

Your GATE 2024 Result [EC]
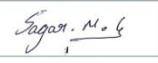
Name
SAGAR M G

Registration Number
EC24S71324117

Gender
Male

Parent's/Guardian's name
LINGARAJ M G

Date of Birth (YYYY-MM-DD)
2002-05-06

Examination Paper
Electronics and Communication Engineering (EC)

Photograph

Signature

Marks out of 100#	33			All India Rank in this test paper	2790
Qualifying Marks##	25	22.5	16.6	GATE Score	469
	General	OBC-NCL/EWS	SC/ST/PwD		

#Normalized marks in case of multisession papers (CE and CS).

##A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with the Score Card.

FAQ for GATE Score

Image of Student Achievement.

Sagar M G, 8th Semester student of E&CE Qualified in GATE-2024 Examination held during February-2024 with a score of **469** and a Ranking of **2790**. Hearty congratulations and best wishes from College Management, Principal, Advisor, HOD, Teaching and Non-teaching Staff members for his future endeavours.

Webinar on "Products, Patents, Proposal writing, Start-up's, Centre of Excellence for Skill Development"
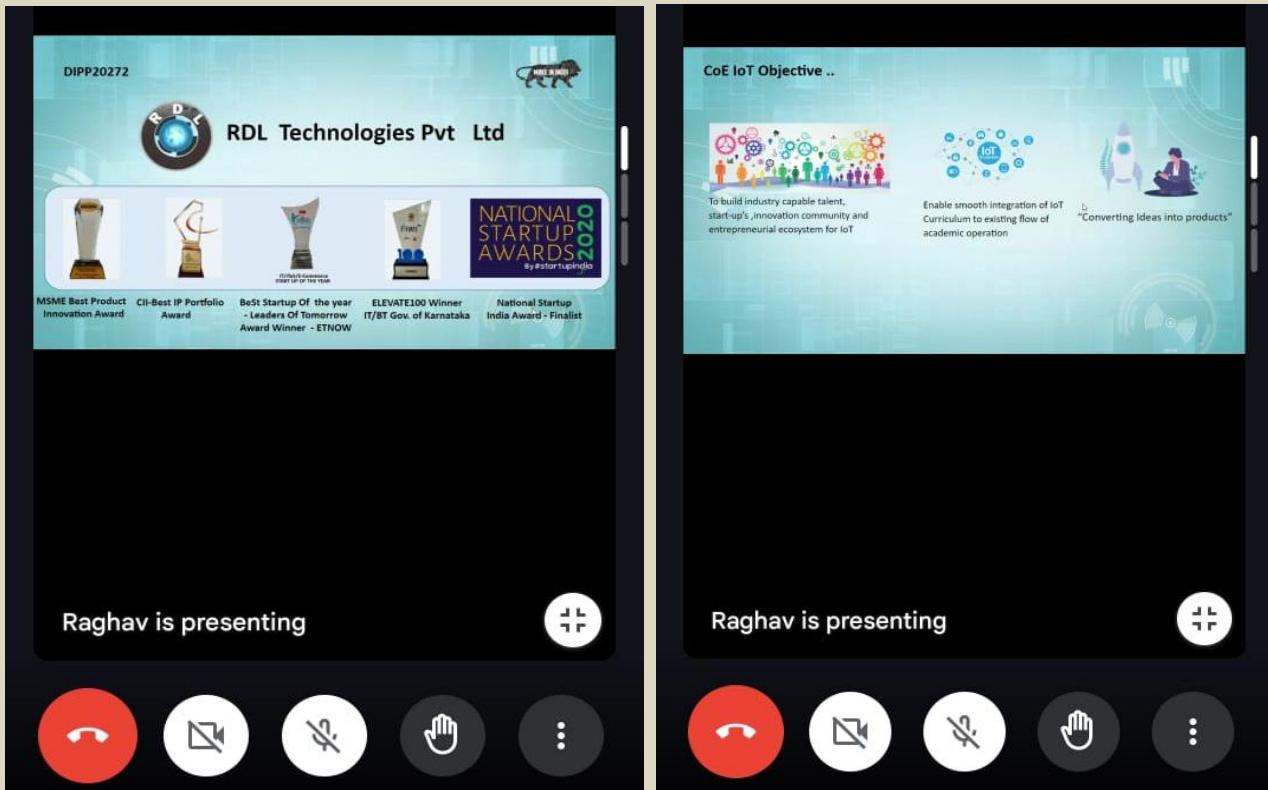
Images of Webinar on "Products, Patents, Proposal writing, Start-up's, Centre of Excellence for Skill Development.

The Webinar on "Products, Patents, Proposal writing, Start-up's, Centre of Excellence for Skill Development" was delivered by **Mr. Raghavendra Shetty**, CEO and MD, RDL Technologies, Mangalore which was organised by JIT, Davanagere on 20/03/2024 at 11:00AM. **Dr. Ganesh D B**, Principal and Director, **Dr. Manjappa Sarathi**, Advisor, all Deans and Heads of various Departments attended this webinar.

Celebration of Sri Ramanavami
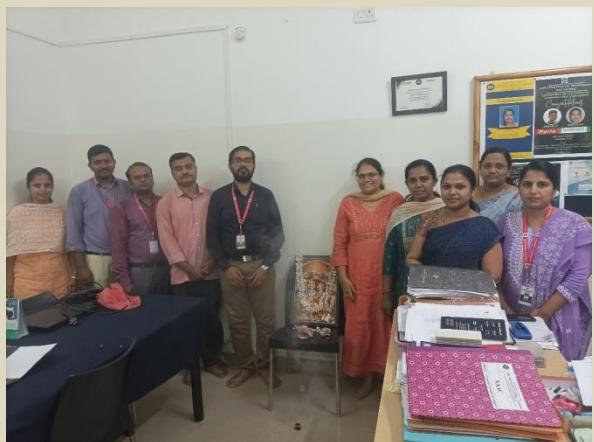
On the Occasion of Sri Ramanavami, Department of E&CE celebrated it by offering prayers to Lord Sri Rama. All the faculty members of E&CE Department took the blessings of Lord Sri Rama.

MoU Signed with EvoluteIQ Company,Bangalore
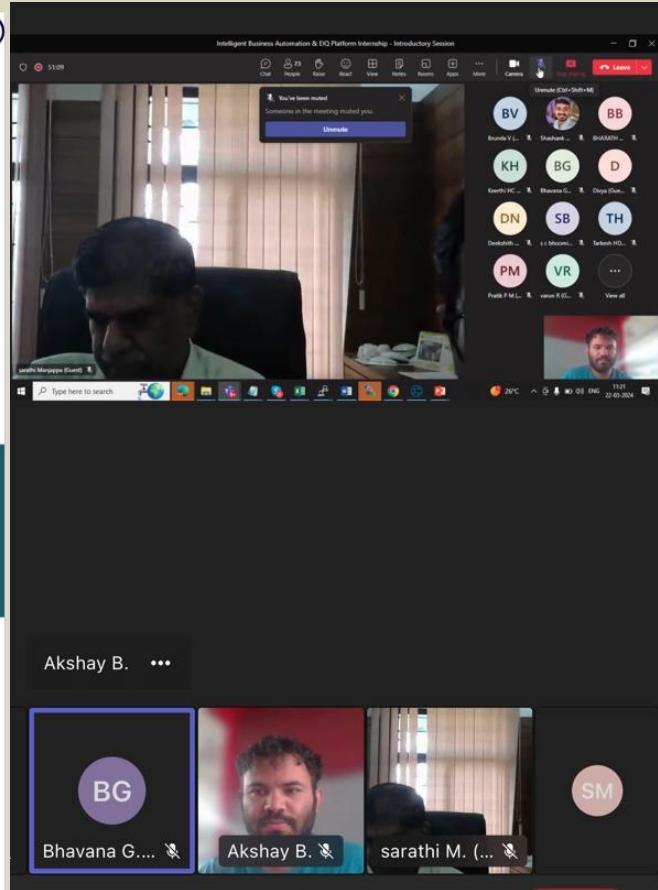
Images of MoU Signed with EvoluteIQ Company,Bangalore.

Jain Institute of Technology, Davangere has signed MoU with EvoluteIQ Company, Bangalore under the Guidance of **Dr. Manjappa S, Advisor, JITD**. The agenda of MoU is to provide Internship and Job opportunities upon successful completion of Internship to the students of JITD belonging to CS&E, E&CE and E&EE branches.

Under this MOU EvoluteIQ conducted an Introductory session on Intelligent Business Automation & EIQ Platform on 22/03/2024, Friday. Dr. Manjappa S, Advisor, JITD, addressed during the session, **Mr. Akshay Bhat,** Quality Analyst, EvoluteIQ, highlighted on the Intelligent Business Automation, DPA-Data driven Process Automation, RPA-Robotic Process Automation. **Dr. Santosh Herur, Professor & Head, Department of E&CE. Mr. Sunil M, Event Coordinator,** Department of E&CE. Faculties and Students from E&CE, CS&E and E&EE departments attended this introductory session.

Forum activity : Technical Talk on "Industry Blueprint: Understanding AI Trends for your future"

Images of Forum activity.

Department of E&CE organized a Technical Talk on **"Industry Blueprint: Understanding AI Trends for your future"** under ECE Forum Encore in EEE Seminar Hall for 1st year E&CE Students. **Mr. Shivaraj V S**, Product Application Manager, Abeyantrix Softlab was invited as the Resource Person.

Project Proposals Approved for 47th series KSCST Sponsorship

Project Team-1

The project proposal titled "**OBJECT DETECTION FOR BLIND PERSON USING YOLO ALGORITHM**" was submitted by the following students under the guidance of **Prof. RAVIKUMAR K. I.**, Assistant Professor, Dept. of E&CE. An amount of **Rs. 5,000.00/-** has been sanctioned.

Project Team Members

| SAGAR M G | VARSHA V | HALLI SANGEETHA | SHIVALINGASWAMY S V |

Project Team-2

The project proposal titled "**VOICE ASSISTANT SIGN LANGUAGE DETECTION USING MACHINE LEARNING**" was submitted by the following students under the guidance of **Prof. DEEPTHI G B**, Assistant Professor, Dept. of E&CE. An amount of **Rs. 5,000.00/-** has been sanctioned.

Project Team Members

| MALLIKARJUN M | PRATHIBHA M H | VARUN K | SAHANA T |

Student's Achievement : Campus Placement

Final year students of E&CE Department, **Mallikarjun M and Varsha V Patil**, has been placed in **KINGHILLS** Company during the recent placement drive. Hearty Congratulations to both the students from College Management, Principal, Advisor, Dean-Training & Placement, HOD, Placement Coordinators, Teaching and Non-teaching Staff and Students.

Industrial Visit

Images of Industrial Visit.

An Industrial Visit to **"Deshpande Startups",** Hubli was arranged by the department of E&CE to provide practical exposure to 8th Semester Students. Deshpande Startups is creating & nurturing an ecosystem in a non-metro with the objective to build an entrepreneurial ecosystem that serves the semi-urban and rural segments. In this Industrial visit, students gained information regarding the stages of startups, Problem statement, validation, solution, customer valuation, technology intervention, acoustic growth in startups. They also got insights about PCB designing, screen printing, conveyor and reflow and the further testing process on the PCB board like vibration test, dust test, resistance test. Prof. Anusha T S and Mr. Arun Kumar B N accompanied the students.

Faculty Achievement : Ph.D Thesis Defence Viva

Mr. Ravikumar K I, Assistant Professor, Dept. of E&CE has successfully completed his **Ph.D. Thesis Defence Viva** on the research topic entitled **"Design and Implementation of RRAM for ANN Based Applications"** under the supervision of Chairperson & Guide, **Dr. R. Sukumar**, Professor & Head, Department of ECE, Jain University, Bangalore and External Examiner **Dr. U B Mahadevaswamy**, Professor & Head, Department of ECE, Sri Jayachamarajendra College of Engineering, Mysore. The viva-voce was conducted at 11:00 AM in Jain Global Campus, Kanakapura Road, Bangalore.

Ananya V of 6th semester E&CE department received a scholarship amount of **Rs.30,000/-** from **Muthoot Fincorp** as an CSR Initative from Mutoot Group.

Image of Muthoot Fincorp Scholarship

Student's Mini-Project Implementation

Final year Student's of ECE Department, **Mallikarjun M, Varun K, Prathibha M H and Sahana T** successfully Implemented their Mini-Project **"IoT Based Smart Notice Board"** under the guidance of **Prof. Deepthi G B**. at the Entrance of E&CE Department.

Image of Student's Mini-Project Implementation.

SHRUNGA 2K24 GRAND INAUGAURATION

Images of Shrunga 2k24 Grand Inaugauration.

The Inauguration of Annual Sports and Cultural fest of Jain Institute of Technology Davanagere titled **"SHRUNGA"** for the year 2024 was held on Monday the 6th of May 2024. **Mr.Pruthvi Shamanoor**, Cine Actor & Ambassador of Election Commission, Davangere district graced the inauguration ceremony as the Chief Guest. Addressing the audience he called upon the students to take part actively and positively in electing our people representative in forming the government and keep up the democracy of our country. Expressing his wishes for the grand success of the shrunga 2k24 he called upon the students to utilize the platform to shine in their career. **Dr.Manjappa Sarathi**, Advisor, JITD and **Dr.Ganesh. D B**, Principal & Director encouraged the students to actively participate in all the sports and cultural activities. Convener of Shrunga-2K24**, Dr. Mouneshachari S** briefed about the shrunga website and activities for the sports and cultural events. Collage cultural coordinator, Prof.Muralidhar.S sang a invocation song.

Cooking without Fire

Images of Shrunga 2k24 Cooking without Fire Event.

Cooking without fire was organized by **Prof. Swetha B S** for 2nd sem D sec ISE students as a part of SFH activity on 4/5/24. The students participated actively and displayed hygiene, discipline by preparing healthy , delicious deserts and chats . **Prof. Basavaraja Patil** from CSE , **Dr.Latha B M**, **Prof. Kotramma T S** , **Prof. H S Saraswathi** judged the event. HoDs and faculties from various departments also graced the event and appreciated the efforts of the students.

Project Exhibition : P-EXPO-2024
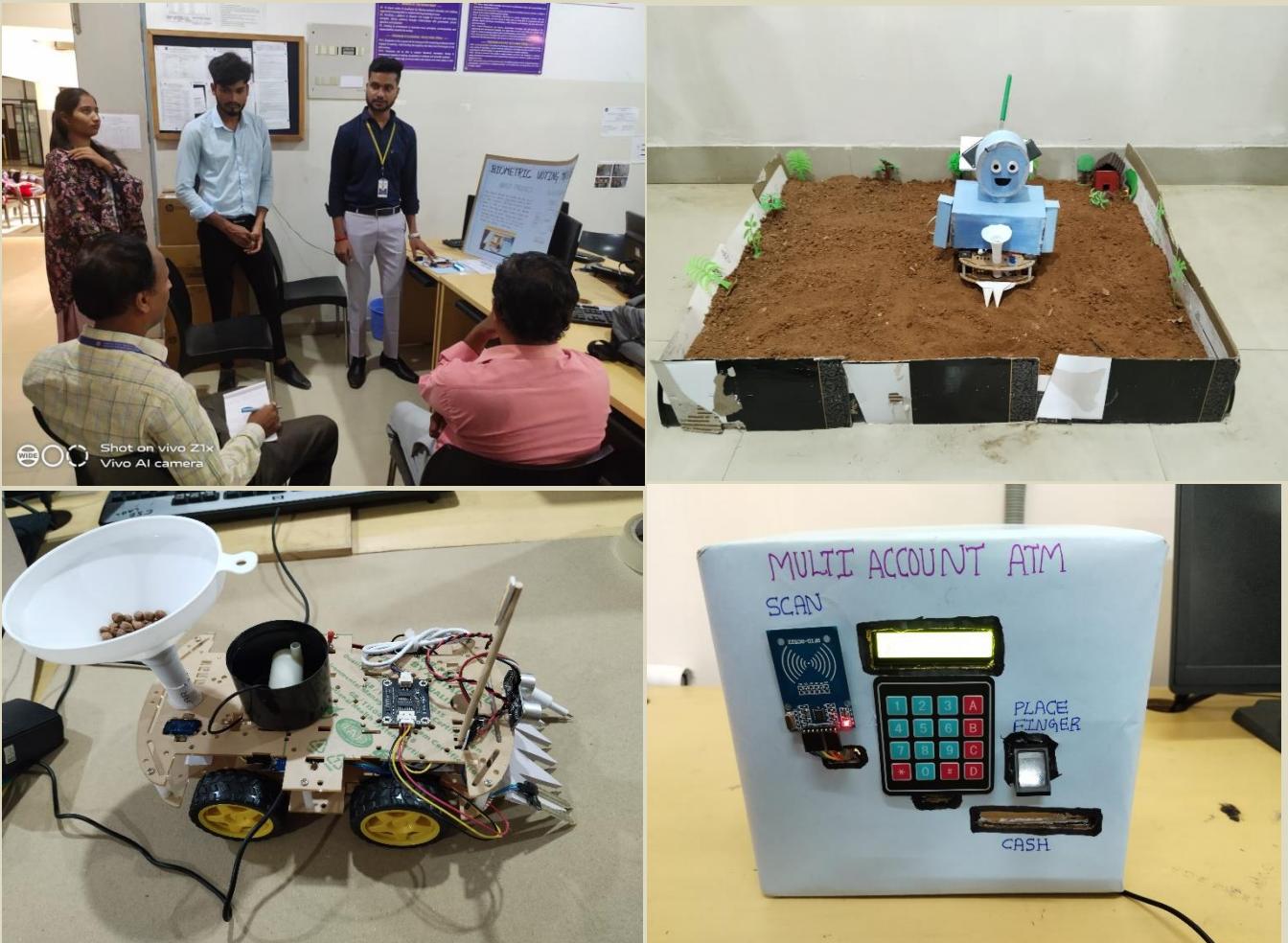
Images of Project Exhibition : P-EXPO-2024.

Among all the projects exhibited, the following **Two projects** were ranked as first and second by the Evaluator.

First Place

The Project work entitled **"CROPWISE REVOLUTIONIZING FARMING WITH DATA DRIVEN CROP SELECTION"** presented by the students : **Manjunatha M, Akash G J, Anith Baba A, Bharath B P** under the guidance of **Dr. SANTOSH HERUR** , Professor & Head, Dept.of E&CE has got First place in the Project Exhibition.

Second Place

The Project work entitled **"OBJECT DETECTION FOR BLIND PERSON USING YOLO ALGORITHM"** presented by the students : **Sagar M G, Varsha V, Halli Sangeetha** under the guidance of **Dr. RAVIKUMAR K I** ,Assistant Professor,Dept.of E&CE has got Second place in the Project Exhibition.

Images of Project Exhibition First place &Second Place.

Shrunga 2k24, Antyakshari Event

Images of Shrunga 2k24, Antyakshari Event.

As a part of Shrunga 2k24, the event "**Antyakshari**" was held on **May 17, 2024**, for the students of JIT. As many as 58 teams registered for the event, and 35 teams participated. The event was coordinated by **Mrs. Kotramma T S**, **Mr. Shriharsha**, and **Mr. Nagaraja K U** with **Dr. Mouneshachari S** serving as the convenor. The Cultural coordinator **Prof. Murulidhar**, along with other teaching and non-teaching staff of the college also attended the successful event.

Ethnic Day 2K24 : DASARA Theme

Images of Shrunga 2k24, Ethnic Day 2K24 : DASARA Theme.

On the occasion of SHRUNGA 2K24, Ethnic Day was celebrated in the department of Electronics & Communication Engineering with a Theme of **DASARA**. All the students created colorfull vibes of Dasara festival in the department by offering pooja to maa Durga and also presented a dance performance. All the faculty members and students walked up for a Ethnic Day Procession and exhibited Dasara theme. All the faculty members, students and non-teaching staff members dressed up in ethnic outfits and celebrated it.

Farewell Function : Yaadein – 2024 Output Batch

Images of Farewell Function : Yaadein – 2024 Output Batch.

n

Department of Electronics and Communication Engineering organized a **Farewell Function : Yaadein** for the final year students at JITD Auditorium. **Mr. Vijay Kumar N D**, Director, Proxelera India Pvt. Ltd., Banglore invited as chief guest for the event. **Dr. Ganesh D B**, Principal & Director, JIT presided over the function. **Dr. Manjappa Sarathi**, Advisor, JITD, **Dr.Santosh Herur**, HOD, Dept. of ECE, **Mr. Sharanabasaveshwara H B**, Forum coordinator,Dept. of ECE also chaired the stage. All the dignitaries inaugurated the function

by watering the plant. Mr. Vijay Kumar N D addressed the students regarding placement opportunities in VLSI domain, its necessary preparations and skills required. Dr.Santosh Herur, HOD, Dept. of ECE gave an overview about student's participation in academics and co-curricular activities and wished the best for their career and profession. Mr. Sharanabasaveshwara H B, Forum coordinator presented a report on forum activities conducted in the department. Advisor, Dr. Manjappa Sarathi encouraged the students to actively participate in all the events and gave his best wishes to all the final year students. Dr.Ganesh D B, Principal & Director, JITD gave presidential remarks and wished the best for all outgoing students and asked students to be in association with the college and also assured all support from the institution for their future endeavours.

Dr. Ravikumar K I, Assistant Professor, Dept. of ECE was felicitated for completion of Ph.D from Jain University. **Academic toppers, Campus Placed students, Winners of forum events, Placement coordintors, Forum representatives and Class representatives** were honoured with certificate. A total of **16 NPTEL certified students** were encouraged with cash Prizes. Final year students who contributed their **mini-project** to the institution were honoured. A momento was presented to all the outgoing students. Final year students, **Mallikarjun M and Prathibha M H** awarded as Best outgoing students of the year and **Varsha V** awarded as Best Performer. A photo session was arranged for the KSCST funded project batch students and Campus placed students. All the Teaching and Non-teaching staff members, 2nd and 3rd year students of the department were present during the event.

Image of Farewell Function : Yaadein – 2020-2024 Output Batch

47th series KSCST Student Project Programme Midterm Evaluation
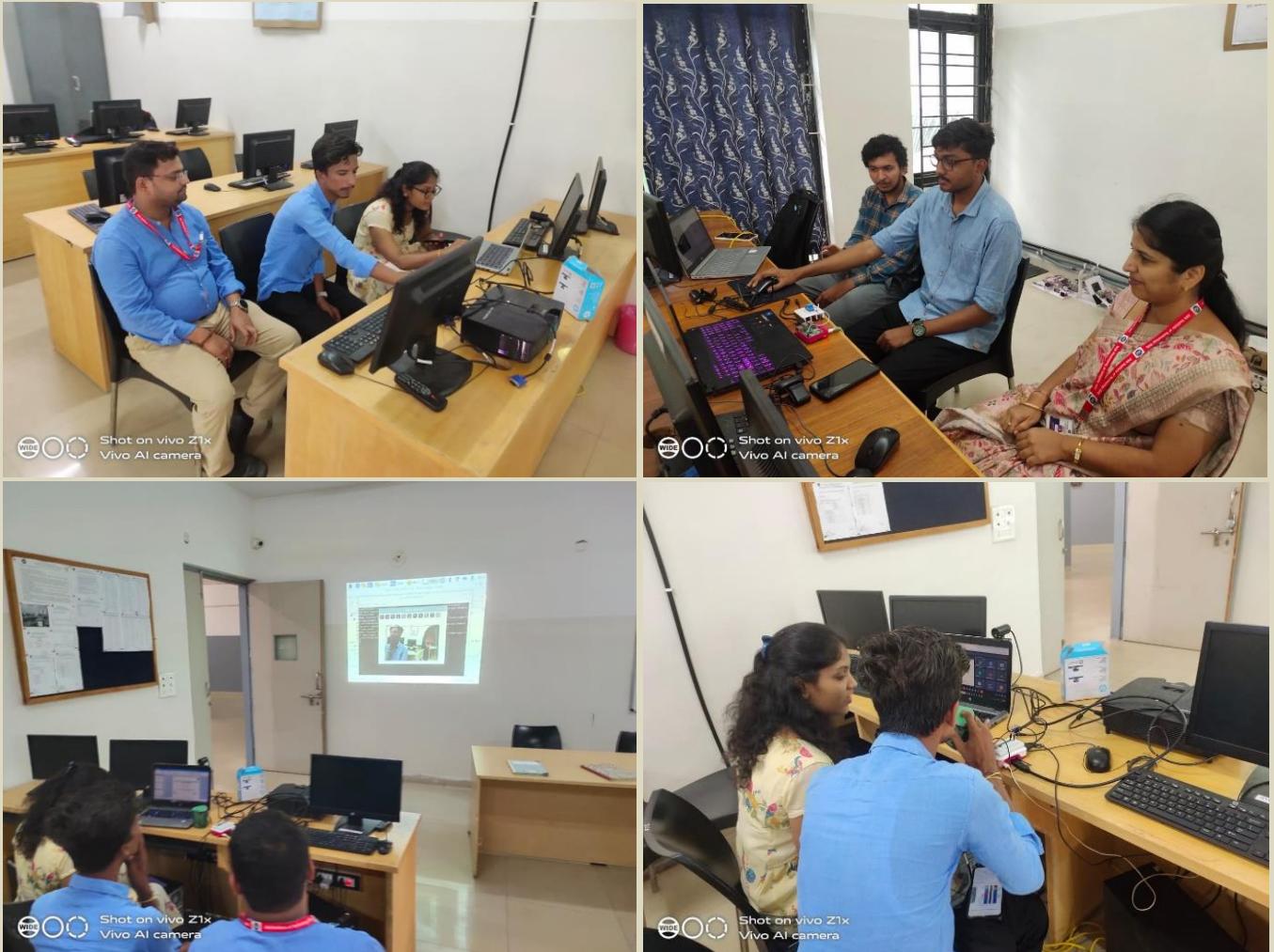
Images of 47th series KSCST Student Project Programme Midterm Evaluation.

In the 47th series of Student Project Programme (SPP): 2023-24, **Two Proposals** from the department of E&CE has been selected and sponsored. The Midterm Evaluation of both the Projects were conducted today in Online mode. Both Project teams presented their projects titled "**OBJECT DETECTION FOR BLIND PERSON USING YOLO ALGORITHM**" under the guidance of **Dr. Ravikumar K I,** Assistant Professor, Dept. of E&CE and "**VOICE ASSISTANT SIGN LANGUAGE DETECTION USING MACHINE LEARNING**" under the guidance of **Mrs. Deepthi G B**, Assistant Professor, Dept. of E&CE. **Dr. G. Mohan Rao**, Head, Research cell, Rajiv Gandhi University for Knowledge Technologies (APIIIT), Nuzvid, Andhrapradesh evaluated the Student's Projects as a KSCST Project Evaluator.

JAIN YOGA UTSAV 2024

Images of JAIN YOGA UTSAV 2024

Jain Institute of Technology in association with JITD NSS unit organized JAIN YOGA UTSAVA from 04th to 20th June 2024. As a ongoing day of this event Yoga training was given to students by the Yoga professionals, Mr Manjunath, International Yoga Award winner and Mrs Geetha M, Yoga ratna Award winner, SSGG Yoga guru. Program Coordinators Mr Basavaraj patil, Manjunath patil and NSS Program officer Veeresh Kumar K S Coordinated the event.

Student's Achievement : Placement

Congratulation Poster

Final year students of E&CE Department Mr. Mallikarjun M, Mr. Sagar M G, Ms.Kaveri, Ms Tejashwini M H, Ms. Varsha V, has been recruited for **Seventh Sense Talent Solutions** with an attractive salary package of **CTC 4.5 LPA**. Hearty congratulations and best wishes to all the placed students from College management, Principal, Advisor, Dean Training and Placement, HOD, Placement Coordinators, Teaching and Non-teaching staff members.

Ms Manasa S, Ms Prathibha M H, 8th semester students from Electronics and Communication Department has placed in FacePrep Company with package of 3.1 LPA. Hearty congratulations and best wishes to all the placed students from College management, Principal, Advisor, Dean Training and Placement, HOD, Placement Coordinators, Teaching and Non-teaching staff members.

Congratulation Posters

Congratulation Poster.

Mr. Sagar M G and Mr. Varun K , 8th semester students from the Department Electronics and Communication Engineering has been placed in **Kaynes Technology** with package of 2.4LPA. Hearty Congratulations and Best wishes from College Management, Principal, Advisor, Dean (T&P), Head of the Department, Placement Coordinators, Staff members and students.

Selected for State Level Poster Presentation & Exhibition

Screenshot of Selected for State Level Poster Presentation

Under 47th Series KSCST series of Student Project Programme (SPP) : 2023-24 the Project titled **Voice Assistant Sign Language Detection using Machine Learning'** has been selected for State Level Poster Presentation and Exhibition to be held at Sharnbasva University, Kalaburgi on 9th and 10th August 2024. Hearty Congratulations and best wishes to the **Project team members Mr. Mallikarjun M, Ms. Prathibha M H, Ms. Sahana T, Mr. Varun K and the Project Guide Prof. Deepthi G B** from College management, Principal, Advisor, HOD, All the teaching and Non-teaching staff members.

PMKVY 4.0 Examination

Images of PMKVY 4.0 Examination.

Department of Skill Development Cell organized **PMKVY 4.0 Examination** in association with the Department of Electronics and Communication Engineering. A total of 30 Students from 4th semester, E&CE department attended the examination on 11th July 2024 and successfully completed the Assessment.

NAAC Audit
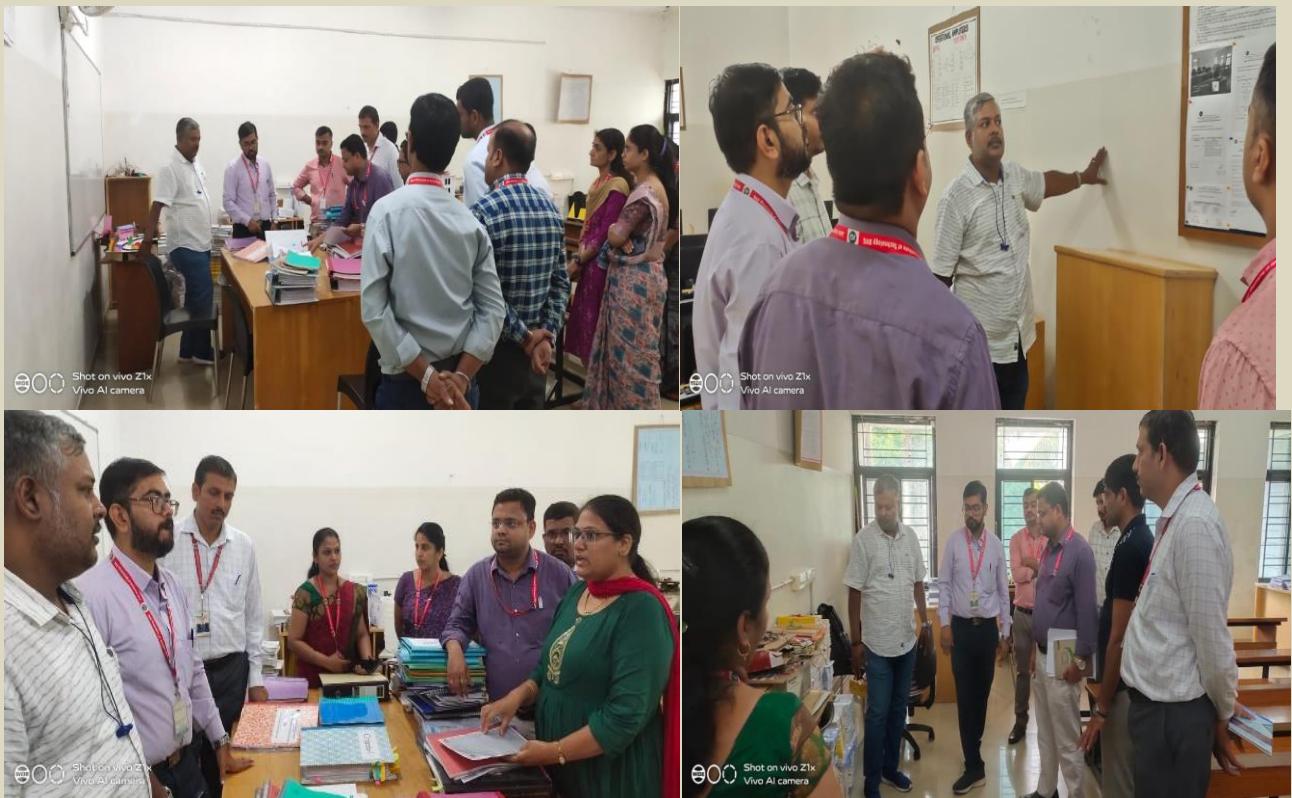
Images of NAAC Audit.

NAAC Audit was Conducted in the Department of E&CE. **Prof. Senthil V**. from Jain University Head office was invited as a Expert for auditing. He verified all the expected files pertaining to NAAC at department level and provided his valuable feedback and suggestions/observations to improve the quality of documents and presenting skills. He also visited labs and verified the necessary documents to be maintained in labs. During the Audit Process, Dr. Santosh Herur, Professor & Head, Dept. of ECE, IQAC-Director, Dr. Suhas D, Associate Professor and Head, Dept.of MBA, Dr. Ravikumar K I, Accreditation Coordinator, Dr. Rajendra Soloni, NAAC Coordinator and all the Teaching and Non-teaching staff members of the Department were present.

Two Day's Workshop on "Innovate with Arduino – Programming to Project "

Images of Workshop on "Innovate with Arduino – Programming to Project.

Two days' Workshop under Skill Development Program on **"Innovate with Arduino – Programming to Project "** is organized by the Department of Electronics and Communication Engineering, JITD in association with Abeyaantrix, Davangere on 19th & 20th of July, 2024 at Room No FF125. Mr. Shivraj M S and Mr. Mantesh B Shirur were invited as the Resource persons for the Program. Dr. Ganesh D B, Principal & Director, JITD and Dr. Manjappa Sarathi, Advisor, JITD extended conveyed their best wishes in their absence. Dr. Santosh Herur, Professor and Head, Department of E&CE addressed the gathering and encouraged the students to upskill their Programming knowledge using Arduino. Prof. Savita Mamadapur, Prof. Sunil M, Program Coordinators, Dept. of ECE were also present during the program. A total of 69 students from 4th semester of ECE attended the SDP. All the teaching and non-teaching staff members of the department were present during the Skill Development Program.

"Innovate with Arduino – Programming to Project " : DAY2
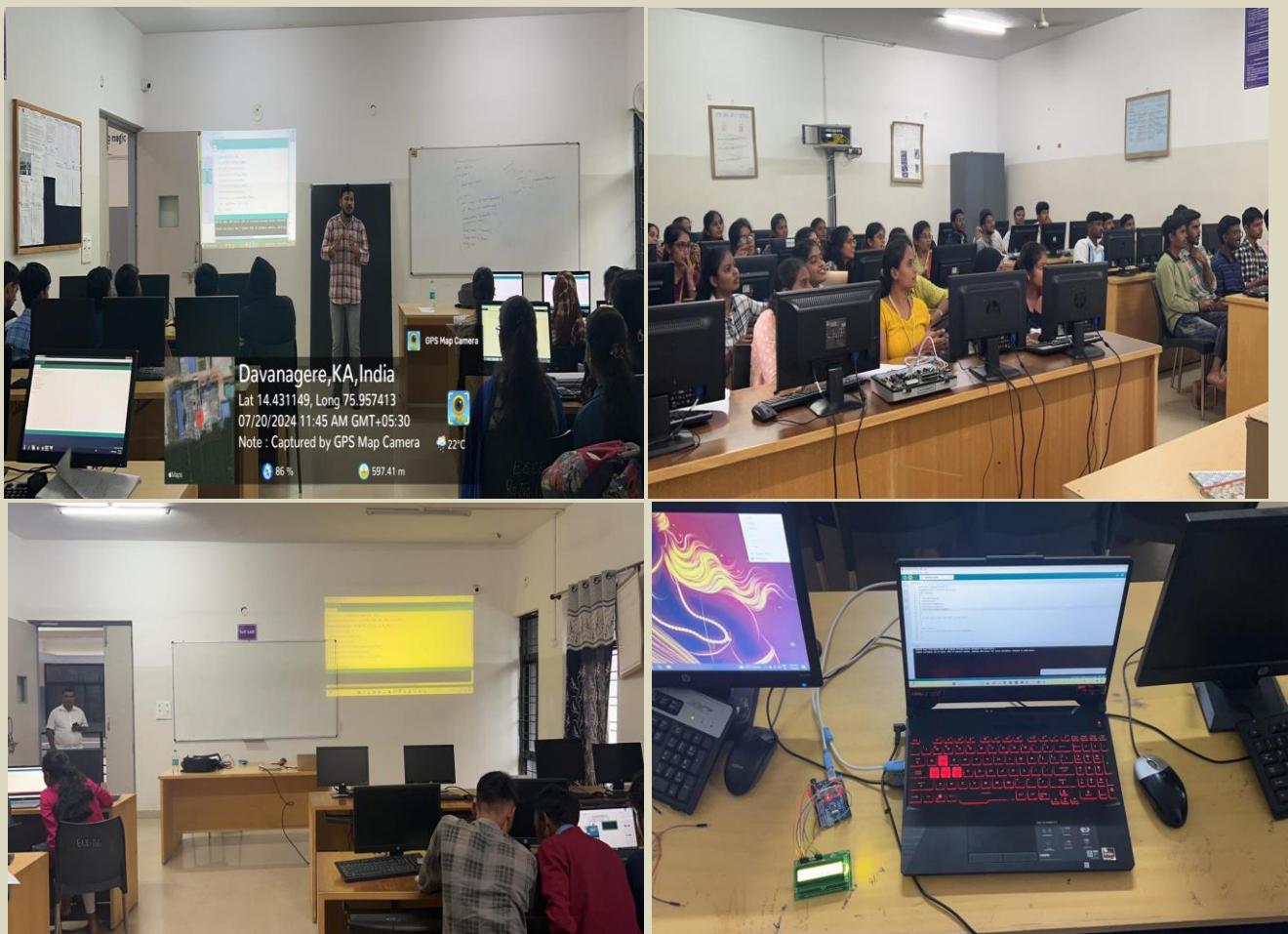
Day2-Images of Workshop on "Innovate with Arduino – Programming to Project.

On Day 2 of Two days' Workshop under Skill Development Program on **"Innovate with Arduino – Programming to Project"**conducted by the Department of Electronics and Communication Engineering, JITD in association with Abeyaantrix, Davangere, A Hands on session on Arduino was conducted by **Mr. Mantesh B Shirur**, Senior Embedded Software Engineer and **Mr. Rakesh K B ,** Software and Embedded Developer**.** Students performed various projects on Arduino and got practicle knowledge about Arduino and various sensors. **Prof. Savita Mamadapur** & **Prof. Sunil M**, Program Coordinators, Dept. of ECE were also present during the session. A total of 65 students from 4th semester of ECE attended the SDP.

DAY3 : Workshop on "Innovate with Arduino – Programming to Project "
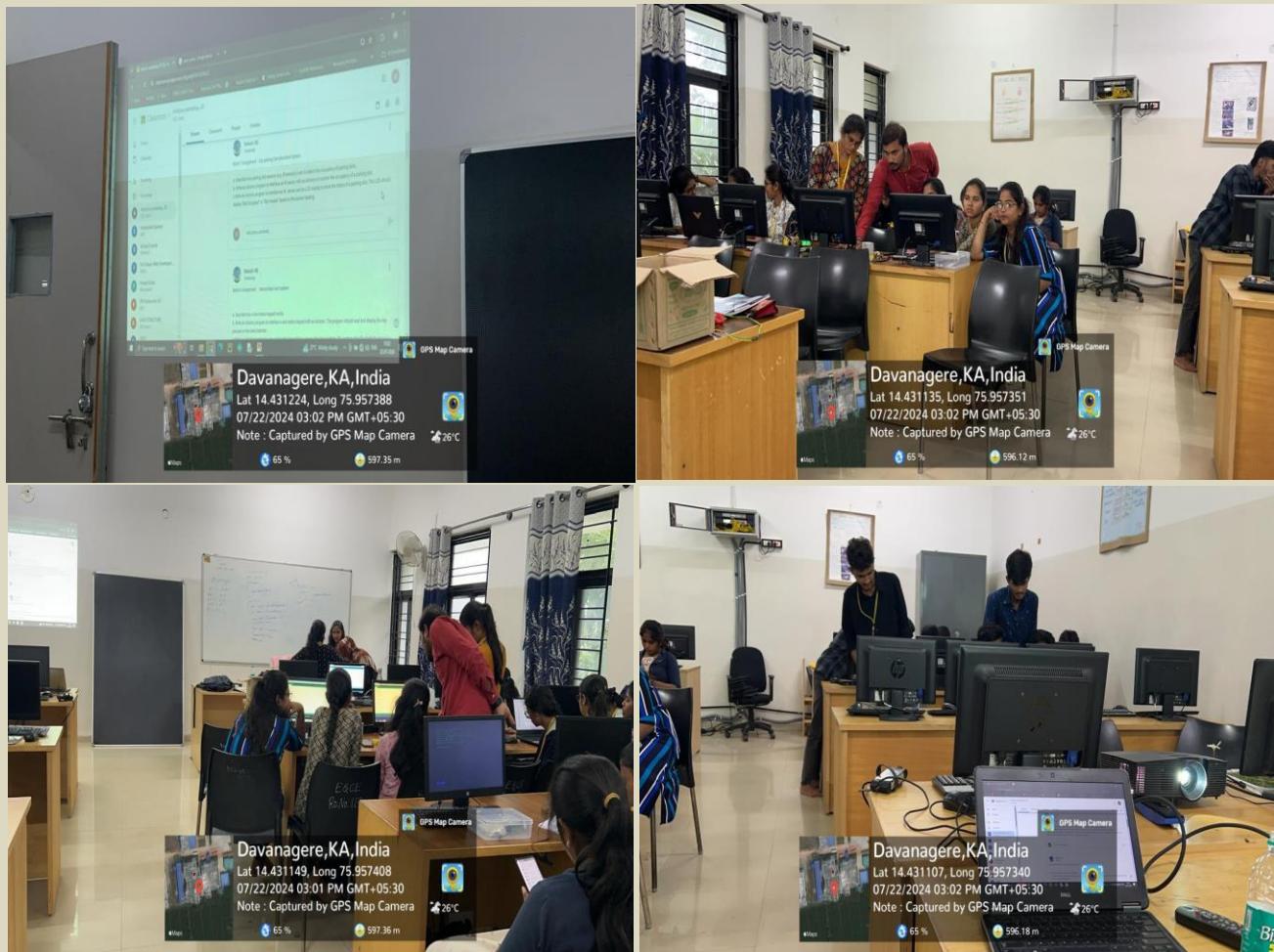
Day3-Images of Workshop on "Innovate with Arduino – Programming to Project

On Day 3 of a Workshop on **"Innovate with Arduino – Programming to Project "** organized by the Department of Electronics and Communication Engineering, JITD in association with **Abeyaantrix, Davangere** - A Hands on session on Arduino was conducted by **Mr. Mantesh B Shirur,** Senior Embedded Software Engineer and **Mr. Rakesh K B** , Software and Embedded Developer. Students were assigned with mini project titles and were asked to implement the working model using Arduino**. Prof. Savita Mamadapur, Prof. Sunil M**, Program Coordinators, Dept. of ECE were also present during the session. A total of 65 students from 4th semester of ECE attended the SDP and demonstrated their models using Arduino.

Valedictory : Workshop on "Innovate with Arduino – Programming to Project"

Images of Valedictory Workshop on "Innovate with Arduino – Programming to Project

A Valedictory of Six days' Workshop under Skill Development Program on **"Innovate with Arduino – Programming to Project "** was organized by the Department of Electronics and Communication Engineering, JITD in association with **Abeyaantrix, Davangere** on 24th of July, 2024 at JITD Auditorium. **Mr. Shivraj M S** and **Mr. Mantesh B Shirur**, Resource persons were invited as chief guests for the programme. **Dr. Santosh Herur**, Professor and Head, Dept. of E&CE addressed the gathering and gave an overview on the outcomes of Workshop and highlighted on the importance of hands-on session in academics for upgrading their practical knowledge using Arduino and also congratulated the students for successful completion of workshop. **Dr. Ganesh D B**, Principal & Director, JITD, appreciated student's effort and encouraged them to actively participate in all academic activities in his presidential marks. Students demonstrated their Arduino projects completed during the workshop and

Participation certificates were distributed to all the students. **Prof. Savita Mamadapur**, **Prof. Sunil M**, Program Coordinators, Dept. of ECE were also present during the program. A total of 66 students from 4th semester of ECE attended the SDP. All the Teaching and non-teaching staff members of the department were present during the Valedictory.

IIC : Workshop on "Prototype/Process Design & Development "

Images of Valedictory Workshop on "Innovate with Arduino – Programming to Project

JITD Institution's Innovation Council (IIC) in association with the Department of Electronics and Communication Engineering organized a Workshop on **"Prototype/Process Design & Development "** on 26th of July, 2024 at JITD Auditorium. **Mr. Harish Jain , Founder, Crop Secure Agri Tech, Pvt Ltd** and a Proud Alumni of E&CE dept, JITD was invited as the Resource person for the Program. **Dr. Ganesh D B**, Principal & Director, and **Dr. Manjappa Sarathi**, Advisor, JITD conveyed their best wishes in their absence. **Dr. Ravi Rayappa**, Associate Professor, Department of E&CE addressed the gathering and encouraged the students to upskill their knowledge in the Prototype Design and Development. **Prof. Harisk K S**, Convener, and **Prof. Anusha T S**, IIC Coordinator,Dept.of E&CE were also present during the program. A total of 100 students from 4th semester & 6th Semester of various department attended the Workshop. All the teaching and non-teaching staff members of the Institute were also present during the Workshop.

Student's Placement

Congratulation Poster.

Ms **Srushti S H and Mr Bharath B P**, 8th semester students from Electronics and Communication Engineering got placed in **BEST** (Bajaj Engineering Skills Training) as a Graduate Trainee Engineer. Hearty congratulations and best wishes to both the placed students from College management, Principal, Advisor, Dean Training and Placement, HOD, Placement Coordinators, Teaching and Non-teaching staff members.

Faculty Achievement

Prof. Savita Mamadapur, Assistant Professor, Department of E&CE, has successfully completed six months **IUCEE International Engineering Educator Certification Program (IIECP) with Distinction**. The IIECP is an esteemed program focused on enhancing the quality of engineering education through innovative teaching practices, course design, active learning strategies, and the integration of technology. This program was sponsored by VTU and JITD and Prof. Savita Mamadapur was nominated from the college to attend this program. This program's rigorous curriculum includes workshops, mentoring, and collaborative projects aimed at fostering dynamic learning environments. Prof. Savita Mamadapur's dedication and hard work have led to this outstanding achievement, demonstrating a commitment to excellence in education. Hearty Congratulations to her from College Management, Principal, Advisor, HOD, Teaching and Non Teaching Staff members.

NAAC Mock Audit
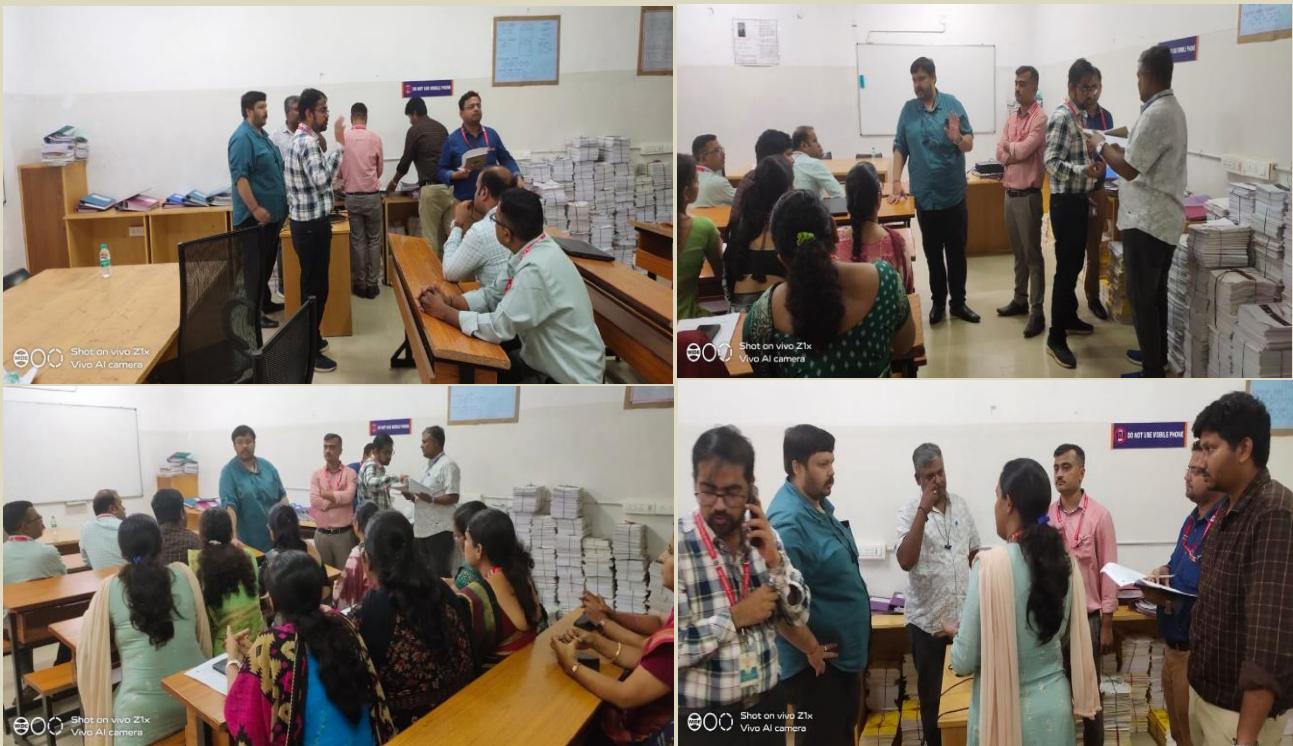
Images of NAAC Mock Audit

NAAC Audit was Conducted in the Department of E&CE on 3rd August 2024. **Prof. Senthil V** and **Prof. Vishal Petkar** from Jain University Head office were invited as Experts for auditing. They verified all the expected files pertaining to NAAC at department level and provided their valuable feedback and suggestions/observations to improve the quality of documents and presenting skills. They also visited labs and verified the necessary documents to be maintained in labs. During the Audit Process, **Dr. Santosh Herur**, Professor & Head, Dept. of ECE, IQAC-Director,**Dr. Ravikumar K I**, Accreditation Coordinator, **Dr. Rajendra Soloni**, NAAC Coordinator and all the Teaching and Non-teaching staff members of the Department were present.

Odd Semester Results of 2023-2024

Sl no.	Semester	Overall Result %
1	III	46/69==66.66
2	V	48/64==75
3	VII	50/53==94.33

Topper's List

3ʳᵈ Semester	USN	Name	Marks_Scored	Percentage %
First	4JD21EC051	Sudeep Reddy K	695/800	86.87
Second	4JD21EC048	Sindushree M J	687/800	85.87
	4JD21EC004	Akshatha H	677/800	84.62
	4JD21EC012	Chandrika N S	677/800	84.62
Third	4JD21EC024	Lingaraj B	677/800	84.62

5ᵀᴴ Semester	USN	Name	Marks_Scored	Percentage %
First	4JD20EC020	Mallikarjun M	780/900	86.66
Second	4JD20EC001	Abdul Mazeed Kaif N	717/900	79.66
Third	4JD20EC013	Kaveri	704/900	78.22

7ᵀᴴ Semester	USN	Name	Marks_Scored	Percentage %
First	4JD19EC005	Arpitha K R	681/800	85.13
Second	4JD19EC048	Trupthi G G	672/800	84
	4JD19EC010	Bhoomika G R	671/800	83.88
Third	4JD19EC052	Chandana G P	671/800	83.88

Even Semester Results of 2023-2024

Sl. no.	Semester	Overall Result %
1	IV	48/68==70.58
2	VI	50/64==78.12
3	VIII	50/53==94.33

Topper's List

4TH Semester	USN	Name	Marks_Scored	Percentage %
First	4JD21EC048	Sindushree M J	858/1000	85.8
Second	4JD21EC011	Bhumika S	852/1000	85.2
Third	4JD21EC053	Suvarna Shivappa Koppad	847/1000	84.7

6TH Semester	USN	Name	Marks_Scored	Percentage %
First	4JD20EC020	Mallikarjun M	705/800	88.13
Second	4JD20EC039	Sahana K P	656/800	82
Third	4JD20EC001	Abdul Mazeed Kaif N	639/800	79.88

8TH Semester	USN	Name	Marks_Scored	Percentage %
First	4JD19EC005	Arpitha K R	446/500	89.2
First	4JD19EC017	Bhavana T M	446/500	89.2
Second	4JD19EC009	Kavya Jain A	443/500	88.6
Second	4JD19EC050	Vimarsha S P	443/500	88.6
Third	4JD19EC048	Trupthi G G	437/500	87.4