



SEPTEMBER
2024

"ENGINEERS ARE THE
PROBLEM SOLVERS OF
THE WORLD"



"To give real
service, you
must add
something which
cannot be bought
or measured with
money."

SIR M VISVESVARAYA



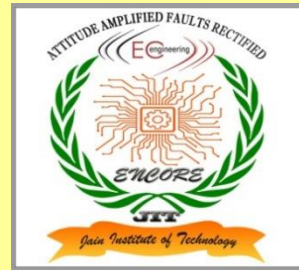
ARKA Educational & Cultural Trust (Regd.)

Jain Institute of Technology, Davangere

(A Unit of Jain Group of Institutions, Bangalore)



*Department of Electronics and
Communication Engineering*



Electro Infinity

Create, Enhance and Sustain



About the Department



Dr. Santosh Herur
Professor & Head
Dept. of E&CE
JIT, Davangere

Department of Electronics and Communication was started in the year 2011-12. The department emphasizes on imparting advanced technical knowledge in various fields like signal processing, VLSI Technology, Embedded system, wireless technology, microwave engineering, optical communication, and microelectronics and control systems. Highly qualified, experienced and dedicated staff members are the backbone of the department. Well equipped, sophisticated, precision and state of the art laboratories are centre of attraction in the campus. Another major strength of the department is its extensive collection of technical books, periodical and journals, ENCORE association of the department of E & CE aims at targeting innovations in the field of advanced communication and networking.

The Department has MOU with Karunadu Technologies Pvt. Ltd, Bengaluru, Abiyantrix, Soft Solutions, Davangere, Sai Tech, Mysore and PSN Hub IT consultancy services. The MOU organizations provide Internship training and conduct workshops on recent trends and organize seminars for the students to enhance their industrial exposure.

Editor's desk



Mrs ANUSHA K

*Assistant Professor
Dept. of E&CE,
JIT, Davangere*



Mr SUNIL M

*Assistant Professor
Dept. of E&CE,
JIT, Davangere*

*Warm greetings to the readers of **Electro Infinity**!!*

We are pleased to share with you the September issue of the newsletter for the year 2024 with a Heartfelt wishes of Ganesh Chaturthi. We express our considerable gratitude and appreciation to everyone who have contributed to this Newsletter magazine. These contributions have required a generous amount of time and effort. It is this willingness to share knowledge, concerns and special insights with fellow beings that has made this magazine possible.

Finally we would like to conclude with these words “Remember, your work may be only to sweep a railway crossing, but it is your duty to keep it so clean that no other crossing in the world is as clean as yours” – Sir M Visvesvaraya

Happy reading

Thank you

ANUSHA .K

SUNIL .M

Vision of the Department

Endeavours to achieve academic excellence by providing value based education to the students of Electronics and communication engineering and enable them to meet the ever changing needs of the society.

Mission of the Department

- ❖ *To emphasize on student focus learning environment.*
- ❖ *To prepare graduates who surpass in the engineering career, qualified to acquire technical knowledge and ethical values.*
- ❖ *To provide a platform to inculcate team spirit and leadership qualities to succeed in professional career.*

Program Specific Outcomes (PSOs)

- ❖ *PSO1: Analyze, simulate and design electronic and communication circuits for given specifications.*
- ❖ *PSO2: Design and test processor based systems*

Program Educational Objectives (PEOs)

- ❖ *PEO1: To create graduates with a strong base in Electronics & Communication Engineering.*
- ❖ *PEO2: To produce technically competent graduates with the ability to understand, analyse & develop electronic systems.*
- ❖ *PEO3: Ability to work with professional ethics and as a team member to realize the goals of the projects.*
- ❖ *PEO4: Excellence in professional career by attaining higher education.*

Teacher's Day Celebration



Images of Teachers Day Celebration.

Students of E&CE department celebrated Teacher's Day and made this day a special one.

Faculty Achievement : Patent Published



(12) PATENT APPLICATION PUBLICATION	(21) Application No.202411053453 A
(19) INDIA	
(22) Date of filing of Application :12/07/2024	(43) Publication Date : 26/07/2024
(54) Title of the invention : AI BASED SMART PERSONALIZED LEARNING PLATFORM USING STUDENT PERFORMANCE DATA AND MACHINE LEARNING ALGORITHMS	
(71)Name of Applicant : 1)Bickey Kumar Shah Address of Applicant :Scholar Department of Computer Engineering Delhi Technological University Delhi North West India ----- 2)Dr. Ravikumar K I 3)Thiyagarajan C 4)Dr. Neena Nanda 5)Chithra S Ravi 6)Shanmughapriya M 7)Raghi K. R 8)Dr. Akhilesh A. Wao 9)Dr. Jothi Prakash V 10)Dr. Arul Antraa Vijay S. Name of Applicant : NA Address of Applicant : NA (72)Name of Inventor : 1)Bickey Kumar Shah Address of Applicant :Scholar Department of Computer Engineering Delhi Technological University Delhi North West India ----- 2)Dr. Ravikumar K I Address of Applicant :Assistant Professor, Department of Electronics and Communication Engineering, Jain Institute of Technology Bada cross Avangere, Davangere 577003 Karnataka India ----- 3)Thiyagarajan C Address of Applicant :Assistant Professor Department of Computer Science, Japjaar University, Semmencherry, Chengalpatt, Tamilnada India ----- 4)Dr. Neena Nanda Address of Applicant :Associate Professor Department of Business Analytics, Vivekanand Education Society's Institute of Management Studies & Research, 495-497, Collectors Colony, Chembur , Mumbai - 400074 Maharashtra India ----- 5)Chithra S Ravi Address of Applicant :Assistant professor Department of Computer Science and Engineering, Mahaguru Institute of Technology/ KTU Alappuzha Kerala India ----- 6)Shanmughapriya M Address of Applicant :Assistant Professor Department of Information Technology, Sri Sakram Institute of Technology, West Tambaram Chennai - 600044 Tamil Nadu India ----- 7)Raghi K. R Address of Applicant :Assistant professor, Department of CSE, St. Joseph's College of Engineering, OMR, Chennai, Tamil Nadu, India ----- 8)Dr. Akhilesh A. Wao Address of Applicant :Associate Dean, Head and Professor Department of CS/IT/CSE AKS University, Samsa, Madhya Pradesh, India ----- 9)Dr. Jothi Prakash V Address of Applicant :Assistant Professor, Department of Information Technology, Karpagam College of Engineering, Mykripalayam village Coimbatore Tamilnada India ----- 10)Dr. Arul Antraa Vijay S. Address of Applicant :Associate Professor Department of Computer Science and Engineering Karpagam College of Engineering, Mykripalayam village Coimbatore Tamilnada India -----	
(51) International classification :G06Q0050200000, G09B0019000000, G09B0007020000, G09B0007000000, G09B0005060000	
(86) International Application No :NA Filing Date :NA (87) International Publication No :NA (61) Patent of Addition to Application Number :NA Filing Date :NA (62) Divisional to Application Number :NA Filing Date :NA	
(57) Abstract ABSTRACT: The world that we live in is being reshaped by artificial intelligence (AI), and this includes the methods in which we learn about it. The educational system has traditionally adhered to a one-size-fits-all strategy, in which all pupils are instructed in the same curriculum at the same pace on a consistent basis. On the other hand, this paradigm frequently fails to accommodate the requirements and preferences of students in terms of how they learn. When it comes to education, artificial intelligence gives teachers the ability to personalize the learning experience for each individual student. In this essay, we will explain how artificial intelligence in education provides personalized learning that is centered on the student. In addition to this, we will discuss the significance of artificial intelligence (AI) in the field of education as well as the utilization of adaptive learning platforms. In addition to this, we will investigate how artificial intelligence can be utilized in the field of education in the years to come.	
No. of Pages : 9 No. of Claims : 5	

Dr. Ravikumar K I, Assistant Professor, Department of E&CE has successfully published a Patent Titled “ **AI Based Smart Personalized Learning Platform using Student Performance Data and Machine Learning Algorithms** ”. Hearty Congratulations and Best wishes on your Achievement from College Management, Principal, Advisor, HOD and all the Teaching & Non-teaching staff members of the department.

Student's Achievement : 6th Semester Results



Bhumika S
(4JD20EC020)
90.5%



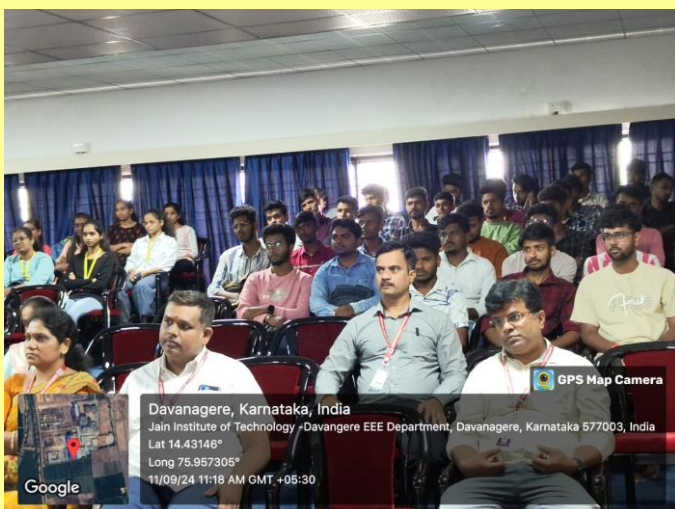
Priya R
(4JD20EC027)
87.1%



Sindhushree M J
(4JD20EC023)
85.5%

VTU 6th Semester CBCS Results for August 2024 Examinations have been released for all regions on 6th September 2024. Department of Electronics & Communication Engineering has achieved a passing percentage of **81.2%** in the 6th semester results. **Bhumika S** has secured 1st place with 90.5%, **Priya R** has secured 2nd place with 87.1%, **Sindhushree M J** has secured 3rd place with 85.5%. Hearty Congratulations to all the toppers of E & C E Department on behalf of College Management, Principal, HOD, Teaching and Non-teaching staff members.

SEMICON INDIA 2024 : Video Conference



Images of Semicon India 2024 Video Conference.

India is becoming a key player in the global semiconductor industry. In a significant move to further this progress, the **India Semiconductor Mission (ISM)**, under the Ministry of Electronics & Information Technology (MeitY), has organized the **SEMICON INDIA 2024** event on September 11th, 2024.

The event was inaugurated by the Hon'ble Prime Minister of India, Shri Narendra Modi, who addressed the nation and highlighted on India's advancements in the semiconductor sector. On this occasion he quoted his vision that, All the electronic gadgets across the world will have India's Semiconductor chips in it. This event have showcased emerging opportunities in this field. UP Chief Minister, Yogi Adityanath global stakeholders, including CEOs, international delegates, and key Government officials chaired the event and highlighted on the need of semiconductor chips and its importance in making digital revolution in India and also extended their collaboration to engage with India in this semiconductor Mission. Department of E&CE arranged to attend this video conference, SEMICON INDIA 2024 through YouTube live session at 11:00 AM in JITD Auditorium, EEE Seminar hall and CSE Seminar at JITD campus. **Dr. Ganesh D B**, Principal & Director, **Dr. Manjappa Sarathi**, Advisor, JITD, Heads & Deans of various departments, all the faculty members of JITD also attended the session. Dr. Ravikumar K I and Mr. Sharanabasaveshwara H B coordinated the event from Dept. of E&CE and Mr. Nagaraja Bodravara coordinated the event from Dept. of E&EE and Mr. Manjunath K G coordinated the event from Dept. of CS&E.

180 Students from CSE attended the session in CSE seminar hall.

240 Students from ISE and ECE attended the session in JITD Auditorium.

100 Students from EEE, ME and Civil students attended the session in EEE seminar hall.

Placement Training : Phase-IV



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TRAINING AND PLACEMENT CELL
Placement Training

Phase IV (Technical Training) - 24 hours

 **17/09/2024 to 20/09/2024**

 **9 am to 5 pm**

In association with



Seventh Sense Talent Solutions

For Seventh Semester Engineering Students

Event Coordinators

Mr. Babusab
Dept. of CS&E

Mrs. Vinutha M
Dept. of IS&E

Dr. Prakash M Walavalkar
Dean (Training & Placement)

Dr. Ganesh D B
Principal & Director



Poster of Phase IV Placement Training.

Placement Training : Phase-IV (Technical Training) will be conducted for all the 7th semester students by the Training and Placement Cell, JITD in association with Seventh Sense Talent Solutions from 17/09/2024 to 20/09/2024.

Yoga Session



Faculty - Outside World Interaction

Prerana Educational and Social Trust (R)
PES INSTITUTE OF TECHNOLOGY & MANAGEMENT
NH-206, SAGAR ROAD, SHIVAMOGGA-577204

Approved by AICTE, New Delhi | Affiliated to VTU, Belagavi | Recognized by Govt. of Karnataka
NBA Accredited Programs: CSE, CV, ECE, ISE, EEE & ME

Dept. of Electronics & Communication Engineering

in associate with
IETE Student Chapter

SDP_{on}

"Circuit and System Simulation"

19th to 21st Sep-2024

INPUT PROCESSING OUTPUT

Overview of SDP:
The three-day Student Development Program (SDP) focused on circuit and microcontroller based system design and simulation using freeware.

Objective of the SDP:
The students will develop professional skills that are required to design and simulate electronic circuits and microcontroller-based systems. With rapid validation and interface skills, students can quickly build their projects using freeware.

Resource Persons:

Prof. Sharanabasaveshwara H. B. Jain Institute of Technology, Davanagere. Rushikesh Garkal JSPM University, Pune. Dr. Saba Fatima JSPM University, Pune.	Dr. Mahantesh P Mattada Associate Professor, Dept. of ECE, PESITM, Shivamogga. Prof. Srilakshmi Nayaka S. R. Assistant Professor, Dept. of ECE, PESITM, Shivamogga.
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Coordinators: **Dr. Mahantesh P. Mattada**
Associate Professor, Dept. of ECE, PESITM, Shivamogga.
Contact - 9762857508

Dr. Om Prakash Yadav
Professor & Head
Dept. of ECE, PESITM, Shivamogga.

Prof. Srilakshmi Nayaka S.R.
Assistant Professor, Dept. of ECE, PESITM, Shivamogga.
Contact - 9108295523

Dr. Yuvaraju B.N.
Principal
PESITM, Shivamogga.



Images of the Event.

Mr. Sharanabasaveshwara H B, Assistant Professor, Dept. of E&CE invited as Resource Person to conduct two sessions on Pspice Simulation in the SDP at Dept. of ECE, PESIT Shivmogga on 19/09/2024. He explained about Design and analysis of complex circuits in the Student Development Program.

Student's Achievement : 4th Semester Results



Ayush G Y
(4JD22EC009)
86.22%



Shreya Narasagond
(4JD22EC048)
83.89%



Fathima Misba
(4JD22EC020)
83.89%



Bhavana Gubber
(4JD22EC011)
81.67%

Alumni Interaction



Images of Alumni Interaction

An Alumni interaction was organized by Electronics and Communication Engineering department. The event started with a warm welcome by the Co-ordinators, followed by brief introduction of the alumni, highlighting their career achievements since graduation.

Mr. Kaushik Itagi B, a Proud alumni of Dept. of ECE, JIT has an 8.4 years of diverse professional experience spanning software development, data science, and block chain technology. He has worked at Microsoft, where he contributed to building scalable software solutions. As a founding engineer at Sifchain, a crypto currency project, he played a pivotal role in developing block chain solutions and driving innovation in the DeFi space. Currently Kaushik Itagi B, working as a Senior Principal Engineer at Stefanini and will soon be joining Google. Kaushik Itagi B also pursuing a Postgraduate Diploma in

Quantum Computing, exploring the future of technology. He inspired the students to focus on academics and encouraged to develop interpersonal skills necessary for interviews. He also motivated students not to lose hope and shared his hard times and how to overcome the hurdles in the path. During the Interaction, Dr. Santosh Herur, Professor & Head, Dept. of ECE, Dr. Rajendra Soloni, Associate Professor, Dept. of ECE, and Event coordinators, Dr. Ravi Rayappa, Associate Professor and Mr. Sunil M, Assistant Professor, Dept. of ECE and the students of 3rd & 5th semester were present.