





Arka Educational & Cultural Trust (Regd.)
JAIN INSTITUTE OF TECHNOLOGY, DAVANGERE

(A Unit of Jain Group of Institutions, Bengaluru)
323, Near VeereshwaraPunyashrama, Avaragere, Davangere- 577005.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

List of Forum Events AY 2022-2023

Sl. No	Activity	Date	Participants																					
	<p>Arka Educational & Cultural Trust (Regd.) JGI Jain Institute of Technology, Davangere (A Unit of Jain Group of Institutions, Bangalore)</p> <p>FORUM ENCORE</p> <p>Department of Electronics & Communication Engineering</p> <table border="1"><thead><tr><th>Role</th><th>Semester</th><th>Name</th></tr></thead><tbody><tr><td>FR</td><td>VII</td><td>KARTHIK, VARSHA</td></tr><tr><td>FR</td><td>V</td><td>MANJUNATH, NIKITHA</td></tr><tr><td>FR</td><td>III</td><td>TIPPESH, DISHA</td></tr><tr><td>CR</td><td>VII</td><td>ABHISHEK, BHOOMIKA</td></tr><tr><td>CR</td><td>V</td><td>MALLIKARJUN, SAHANA</td></tr><tr><td>CR</td><td>III</td><td>SINDHUSHREE, KIRANKUMAR</td></tr></tbody></table> <p>Mr. Sharanabasaveshwara H B Forum Coordinator</p> <p>Dr. Santosh Herur Forum Chairman, HoD, ECE</p> <p>Forum Encore Representatives</p> <p>Forum Student body will be made by election among the students.</p>	Role	Semester	Name	FR	VII	KARTHIK, VARSHA	FR	V	MANJUNATH, NIKITHA	FR	III	TIPPESH, DISHA	CR	VII	ABHISHEK, BHOOMIKA	CR	V	MALLIKARJUN, SAHANA	CR	III	SINDHUSHREE, KIRANKUMAR		
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1	<p style="text-align: center;">Forum Inauguration</p>  <p>Forum Inauguration was done on 15 december 2022 and chief guest gave talk on opportunities in the field of VLSI Design and her struggles in the initial days after her graduation.</p>	15/12/2022 2	176
2	<p style="text-align: center;">OUTCOME BASED ENGINEERING EDUCATION IN ADVANCED ROBOTICS AUTOMATION AND EMERGING TECHNOLOGIES</p>  <p>All the students of ECE and Mechanical were given insights on robotics and automation</p>	19/12/2022 2	204

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Circuit Mania

03/11/2022
2

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Department of Electronics & Communication Engineering

CIRCUIT MANIA
 A Forum Encore Initiative
 Date: 03-11-2022 @ 2.00 P M
 Venue: CR-124

Mr. Sharanabasaveshwara H B
 Forum Coordinator

Dr. Santosh Herur
 HoD, ECE

Dr. Ganesh D B
 Principal, JITD

The students of V Semester participated in the event circuit mania conducted by Mr. Sharanabasaveshwara H B as a forum Encore initiative in association with ISTE. There were 54 participants. Participants were divided into 9 teams and each team was assigned with 5 mini-projects, which were as below:

- i. Touch Sensor
- ii. Automatic night light
- iii. Water level indicator
- iv. Darkness sensor
- v. LED Blinker

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Activity on Plantation & Adoption of Tree

26/11/2022
2

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Department of Electronics and Communication Engineering

In Association with
NSS Cell, JITD jointly organize

MODULE 1 - AN EDUCATION ON ADOPTION OF PLANT AND PLANTATION PROCESS (21UH36)

Venue: JITD Auditorium,
 Target Audience: All III Semester Students
 Date - 26-11-2022 @ 10.30 AM

-By, Mr. Darshan Naik L R
 Range Forest Officer
 Davangere Social Forestry Range & Division

Prof. Sharan H B Prof. Veeresh Kumar K S Dr. Santosh Herur Dr. Ganesh D B
 Prof. Rajeshwari H M NSS Program Officer Associate Prof & Head Principal & Director
 Event Coordinators Dept. of Mechanical Engg Dept. of E&CE

Electronics and Communication Engineering Department of

Jain Institute of Technology, Davangere in Association with NSS Unit JIT, Organized Activity on Plantation and Adoption of a Tree to the 2nd Year Students of JIT as per VTU curriculum on 26/11/2022.

Dr. Santosh Herur, Associate Professor & Head of E&C Department Enlightened the students regarding Social connect and Responsibility Subject as per VTU Syllabus.

Chief guest Mr. DarshanNaik L R, Range Forest Officer, Davangere Social Forestry Range, delivered a speech and Presentation on Different species of Trees and its advantages on Society and Health

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Forum Social Initiative

11/10/2022



The students of VII Semester distributed 40 daily use kits. The initiative was taken by forum Encore and VII semester students. The kits were distributed to 40 differently-abled students of Veereshwara Punyashrama.

Each kit contained the following items,

Sl.no.	Item	Quantity
1	Santoor Soap	1
2	Colgate Toothpaste	1
3	Tide washing powder	1
4	Rin washing soap	1
5	Clinic plus shampoo	10
6	Coconut Hair Oil	1

6

Group Discussion



Group discussion was conducted for the III semester students of ECE on 17/11/2022 by Mr. Sharanabasaveshwara H B, Forum Coordinator and a total of 53 students participated in the event. There were a group of 6 students each and 9 groups were formed. The groups received I, II and III places. Below are the few images of the event.

17/11/2022

53

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Industrial Visit



To,
The Principal,
Jain Institute of Technology
Davangere.

Date: 08/06/2023

Sub: Industrial Visit.

Dear Sir/Madam,

With reference to your mail, 49 students from Department of Ele Communication were accompanied by Mrs. Savita Mamada Sharanabasaveshwara H B to **Kaynes Technology India Ltd** Unit 2, for Industrial

Visit on

08-06-2023.



Students of VII semester visited kaynes technology and learnt about vlsi fabrication processes

08/06/2023

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Intel Learning Links Activity for Women

19/01/2023
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Department of Electronics and Communication Engineering
 In Association with
TPO Cell, JITD jointly organizes

  
LEARNING LINKS FOUNDATION

INTEL INDIA STEM SKILLS PROGRAM FOR WOMEN

19 January 2023 at 10.00 A.M
2nd and 3rd Year (CSE,ECE,EEE) 10.00 A.M to 1.00 P.M
1st Year (CSE,ISE,ECE,EEE) 2.00 P.M to 5.00 P.M
Venue: JITD Auditorium

Resource Person
Mr. Sachin Patil & Team
Learning Links Foundation

Mr. Sharana H B **Mr. Prakash Walavalkar**
 Program Co-ordinator Program Convener

Dr. Ganesh D B **Dr. Santosh Herur**
 Principal & Director HOD,ECE

All the girl students of circuit branches (CSE,ISE,ECE,EEE) attended an event on INTEL INDIA STEM SKILLS PROGRAM FOR WOMEN by Mr. Sachin Patil and his team on 19/01/2023.

Mr. Sachin Patil, Learning Link Foundation, helped students to register to the LMS from INTEL.

Mr. Ganyaneshwar thought students about the basics of VLSI and informed the students the importance of VLSI and companies related to this stream.

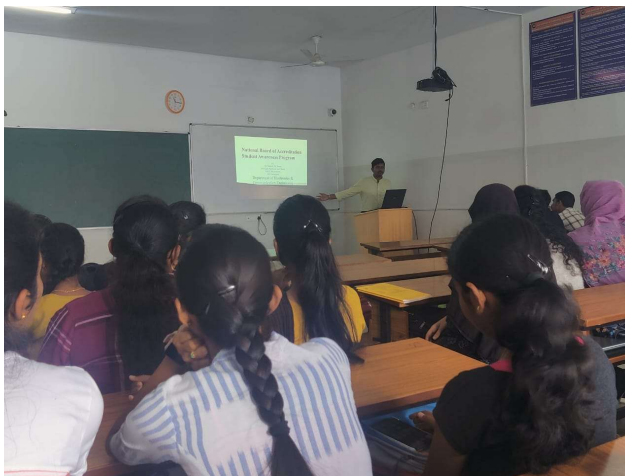
Ms. Poornima thought students about the soft skills and steps, methods to set goals. She also instructed students about time management.

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NBA Awareness to students

11/10/2023
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The students of VII Semester were informed about the NBA and given a session on importance of NBA towards outcome based education by Mr. Sharanabasaveshwara H B, Assistant professor, Dept. of ECE, JITD.

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SDP on Matlab

25/07/202
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Department of Electronics and Communication




**Skill Development Program on Model
Based Approach with
MATLAB Simulink**

25th - 28th July 2023 at 10.00 A.M
 Venue: FF 125
 Resource Person
Mr. Shivaraj V S
 Abeyaantrix Softlab, Davangere

Presided by
Dr. Ganesh D B
 Principal & Director, JIT, Davangere
Dr. Santosh Herur
 Associate Professor & Head, Dept. of ECE

Convener
Dr. Rajendra S Soloni
 Associate Professor
 Dept. of E&C Engg.

Coordinator
Mr. Sharan H B
 Assistant Professor
 Dept. of E&C Engg

The 65 students of IV Semester attended the student development program from Abeyaantrix Softlab. The SDP was sponsored by Skill lab JITD. The Resource persons were Mr. Shivaraj V S and Mr. Rakesh. Students were exposed to the various tool kits available in matlab and simulink. The SDP was organized from 25/07/2023 to 28/07/2023 (4 Days). The SDP was convened by Dr. Rajendra S Soloni and coordinated by Mr. Sharanabasaveshwara H B. Dr. Santosh Herur, HoD, Dept. of ECE and Dr. Ganesh D B, Principal JITD presided the SDP.

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**“Lazy Portado” by Mrs. Sudhamurthy and Analytical Fun Game
Ludo King**

24/11/202
2

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Forum activity on Listen to a story in 10 minutes on “Lazy Portado” by Mrs. Sudhamurthy and Analytical Fun Game Ludo King was conducted for the III semester students of ECE on 24/11/2022 by Mr. Sharanabasaveshwara H B, Forum Coordinator and a total of 40 students participated in the event. There were a group of 4 students each and 10 groups were formed.

12	<p style="text-align: center;">A Technical Talk on Advance VLSI Design circuits and Trends Semiconductor Industry</p>  <p style="text-align: center;">A technical talk was given for V and VII semester students on the VLSI topic.</p>	21/07/2023	104
13	<p style="text-align: center;">Webinar on VLSI Chip Design</p>  <p>The students of VII Semester attended the webinar on 27/09/2022 at 3.30 PM by Entuple Technologies. The discussion was based on the career opportunities in VLSI domain. There was a brief light thrown on different aspects of VLSI domain like Physical design, Design for Test, Design Verification, Analog memory layout. The resource persons were Mr. V. Navaneetha Krishnan, Sr. Application Engineer and Mr. Mamidi Nagaraju, Manager (Design and Support).</p>	27/09/2022	54