



Arka Educational and Cultural Trust®
JAIN INSTITUTE OF TECHNOLOGY
Davanagere

(A Unit of Jain Group of Institutions, Bangalore)
Accredited by NBA, New Delhi: EEE, ECE & CSE. (Valid Up to : 30.06.2026)



JIT EEE-NEWSLETTER

BI ANNUAL JULY- DECEMBER 2025

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



Best Student Award-2025 **ISTE Student chapter**



Vinay S B

“The Best Student Award -2025” was presented to Vinay S B during the 21st ISTE Karnataka state level student Convention held at Yenepoya Institute of Technology, Moodbidri. Congratulations from the Management, Principal & Director Dr. Ganesh D B, Advisor Dr. Manjappa Sarathi, HoD dept. of EEE Dr. Preetha P S, Faculty, Staff, and Students of Jain Institute of Technology Davanagere, His achievement bring pride to the Department of Electrical & Electronics Engineering and the entire JITD Family.

EDITORIAL TEAM

PROF. PRADEEP KANAVI
ASSISTANT PROFESSOR

MR. SANDEEP PATIL M S
FOREMAN



Visit us at: www.jitd.ac.in

PROGRAM OUTCOMES

1. **Engineering knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and computer science and engineering to the solution of complex engineering problems.
2. **Problem analysis:** Identify, formulate, review research literature, and analyze complex computer engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
3. **Design/development of solutions:** Design solutions for complex computer engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
4. **Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
5. **Modern tool usage:** Create, select, and apply appropriate techniques, resources, and modern computer engineering and IT tools including prediction and modelling complex computer engineering activities with an understanding of the limitations.
6. **The engineer and society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
7. **Environment and sustainability:** Understand the impact of the professional computer engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
8. **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the computer engineering practice.
9. **Individual and team work:** Function effectively as an individual, and as member or leader in diverse teams, and in multidisciplinary settings.
10. **Communication:** Communicate effectively on complex computer engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
11. **Project management and finance:** Demonstrate knowledge and understanding of the computer engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
12. **Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

PROGRAM SPECIFIC OUTCOMES (PSO's)

A graduate of the Electrical & Electronics Engineering Program will demonstrate:

PSO1: Apply the knowledge of mathematics, science and electrical engineering fundamentals to solve complex problems in electrical machines, control systems, power systems and electronics.

PSO2: Specify, architect and analyze power systems that efficiently generate, transmit, distribute and utilize electrical power in the present context.

PSO3: Analyze, design & maintenance of modern electrical drives system and modern lighting system to conserve electrical energy.

PROGRAM EDUCATIONAL OBJECTIVES (PEO's)

A graduate of the Electrical & Electronics Engineering Program will demonstrate:

PEO1: To become the resource of Electrical and Electronics engineering.

PEO2: To work in multidisciplinary areas involving core engineering and engaged in lifelong learning for successful professional career.

PEO3: To be successful in taking leadership roles and entrepreneurial positions.

MESSAGE FROM THE CHAIRMAN

We are a school that prides itself not in fostering academic excellence but also in making our learners append at the school satisfying, fulfilling and joyous. I intend to take the JGI Group to the next level, employ more effective methods to make the students think differently and instill in them with positive attitude towards inclusive growth that benefits them and the society at large.

DR. CHENRAJ ROYCHAND
CHAIRMAN JGI
BENGALURU



MESSAGE FROM THE PRINCIPAL

It's my pleasure to introduce this latest issue of the JIT, EEE Newsletter. I would like to congratulate Editorial team for bringing out a beautiful and informative EEE newsletter which has showcased the various activities, events in the department, and the achievements of students and faculties. I look forward to achieve greater heights in the years to come.

DR. GANESH D B
PRINCIPAL & DIRECTOR JIT
DAVANAGERE



MESSAGE FROM THE HEAD OF THE DEPARTMENT

Welcome to the Department of Electrical and Electronics Engineering at JIT Davangere. We started our journey in the year of 2011 with an intake of 60 students. The department has full-fledged laboratories as prescribed by VTU syllabus with modern equipment's. We have grown our expertise, competence.

Core Electrical and Electronics Engineering curriculum and research. We have a strong undergraduate program in Electrical and Electronics Engineering in B.E. We conduct many activities throughout the academic year like Workshops, Seminars, technical talks, skill enhancement programs and other co-curricular activities under the department Form "POTENZA". We are happy to bring out our department EEE newsletter, which showcases all the activities of the department. I congratulate the team for their continuous effort.

DR. PREETHA P S
ASSOCIATE PROFESSOR AND HEAD,
DEPT. OF ELECTRICAL & ELECTRONICS ENGINEERING, JITD



AWARENESS SESSION ON HONORS DEGREE OPPORTUNITIES

6TH SEPTEMBER 2025



The Department of Electrical and Electronics Engineering at Jain Institute of Technology organized a awareness session today aimed at guiding 5th-semester students through the opportunities available in Honors degree programs. The session, held in the seminar hall, was designed to clarify the academic pathways and career advantages of pursuing specialized Honours courses alongside the regular curriculum.

Dr. Prakash K. B., Academic Dean of JIT, served as the resource person for the event. In his detailed presentation, Dr. Prakash meticulously broke down the eligibility criteria required for students to enroll in Honours programs. He emphasized that these programs allow students to gain deeper knowledge in niche areas of EEE, making them more competitive in the job market and better prepared for higher education.

The event was graced by the presence of Proff. Nagaraj Bodravar and Dr. Preetha P. S., Head of the Department of EEE. Their participation in the event exemplified the department's focus on student development and academic excellence. The interactive session concluded with a lively Q&A round, where students had their queries regarding credit transfers, subject combinations, and project opportunities addressed by the Dean and the faculty.

WORKSHOP ON ASSISTIVE TECHNOLOGY

6TH SEPTEMBER 2025



In a significant step toward fostering technological innovation with a social impact, the Department of Electrical and Electronics Engineering (EEE) at Jain Institute of Technology (JIT) organized a specialized workshop on Assistive Technology. Held in the Abdul Kalam Seminar Hall, the session aimed to introduce students to the world of devices and solutions designed to aid individuals with disabilities.

The workshop, featured Mr. Jibanjeet Mohanty, a Prosthetist and Orthotist from the Composite Regional Center Davanagere, as the guest speaker. With his extensive clinical and technical expertise, Mr. Mohanty provided students with a comprehensive overview of the latest advancements in prosthetics, orthotics, and assistive devices.

Mr. Mohanty captivated the audience by demonstrating how engineering principles intersect with healthcare to restore mobility and independence. He discussed the design, fabrication, and real-world application of assistive technologies, emphasizing the critical role that electrical and electronic engineers play in innovating smart, affordable solutions for those in need.

"Technology reaches its full potential when it is inclusive," remarked Mr. Jibanjeet Mohanty during his session. "For engineering students, assistive technology offers a unique opportunity to apply your skills to solve real human challenges and make a tangible difference in the quality of life for millions."

The session provided a unique platform for 5th-semester students to explore a niche yet rapidly growing field that combines engineering, empathy, and entrepreneurship.

Highlights of the Workshop

- Expert Guidance: Insights into the development of prosthetic limbs and orthotic devices.
- Real-World Impact: Case studies on how technology is enabling accessibility.
- Career Pathways: Exploration of opportunities in the biomedical and assistive tech sectors.
- Interactive Session: An engaging Q&A where students discussed the future of smart assistive devices.

ONE-WEEK SKILL DEVELOPMENT PROGRAM ON "EMBEDDED SYSTEMS FOR PROJECT/PRODUCT DEVELOPMENT"

29TH SEPTEMBER 2025



In a continued effort to enhance industry-ready competencies, the Department of Electrical & Electronics Engineering (EEE), under its technical forum FORUM POTENZA, in association with the Skill Development Centre, JITD, successfully inaugurated a One Week Skill Development Program titled "Embedded System for Project/Product Development." The program is being held from October 6th to 11th, 2025, at the Seminar Hall, Department of EEE, JITD, with 5th-semester students as the target audience.

one week-long program is designed to provide students with a deep, practical understanding of embedded systems, focusing specifically on their application in real-world project and product development.

The department has collaborated with Abeyaantrix Edusoft LLP to ensure high-quality industry exposure. The sessions are being led by a team of seasoned professionals:

- Mr. Shivaraj V S, Manager, Abeyaantrix Edusoft LLP
- Mr. Rakesh K B, Embedded System Engineer, Abeyaantrix Edusoft LLP

Their expertise is guiding students through the complete life cycle of embedded product development, from conceptualization and design to implementation and testing.

The comprehensive curriculum is structured to transform theoretical knowledge into practical skills. Key modules covered during the week include:

- Fundamentals of Embedded Systems: Architecture, microcontrollers, and interfacing techniques.
- Programming for Embedded Platforms: Hands-on coding sessions for real-time applications.
- Sensor Integration & Data Acquisition: Working with industrial sensors and actuators.
- Project/Product Lifecycle Management: From prototyping to final product deployment.
- Industry Case Studies: Analysis of successful embedded products in the market.

The faculty coordinators, Mr. Pradeep Kanavi and Mr. Anil Kumar V S, who emphasized the importance of such skill-based learning initiatives. They encouraged students to leverage this opportunity to build projects that can potentially be developed into market-ready products.

TECHNICAL TALK ON "CAREER PATHS, SKILLS & OPPORTUNITIES"

29TH SEPTEMBER 2025



The Department of Electrical & Electronics Engineering (EEE) at Jain Institute of Technology, Davanagere, under its technical forum FORUM POTENZA, successfully organized a technical talk on "Career Paths, Skills & Opportunities – Beyond the Classroom" for 3rd-semester students. The session was held at the Seminar Hall, Department of EEE, JITD, at 9:30 AM.

The talk was designed to broaden the horizons of students by providing insights into the diverse career trajectories available in the field of electrical and electronics engineering, emphasizing the importance of skills that extend beyond the traditional academic curriculum.

Emerging Career Paths: Exploration of various domains within and allied to EEE, including embedded systems, automation, renewable energy, and smart grid technologies. Essential Skill Sets: Identification of technical and soft skills that are highly valued by employers in the current job market. Mr. Shivaraj encouraged students to think proactively about their careers and to continuously upgrade their skills to stay relevant in a rapidly evolving technological landscape. Distinguished Dignitaries and Coordinators.

ENERGY SWARAJ AMBASSADORS8TH SEPTEMBER 2025

Students of the EEE Department at Jain Institute of Technology, Davangere—namely Angel W, Gowri R A, Pooja P J, Santrupty S, and Ayesha Siddiqua A—were officially certified as FEM Ambassadors by the Energy Swaraj Foundation. This prestigious recognition followed their active participation and successful completion of the Climate Literacy and Energy Literacy (CLE) Training program under the Finite Earth Movement (FEM). The Energy Swaraj Foundation, founded by Dr. Chetan Singh Solanki of IIT Bombay, is a non-profit initiative dedicated to promoting energy literacy and sustainability to combat climate change, while the Finite Earth Movement is a global effort inviting individuals and institutions to become climate and energy literate, and to practice and preach energy conservation. The newly appointed ambassadors, hailing from the Department of Electrical & Electronics Engineering, are now equipped to lead awareness campaigns, workshops, and projects both within and beyond the campus, driving the message of sustainable energy use that lies at the core of modern electrical engineering. This initiative marks JITD's official commitment to the global movement, and the certification ceremony was held in the presence of the Principal, the Director, the Head of the Department of EEE, the FEM Program Advisor, and representatives of the institution's management. Their collective guidance and encouragement were instrumental in empowering these students to become change-makers in creating awareness about energy conservation and climate responsibility.

ONE-DAY INTERNATIONAL FDP ON "GROWING GLOBALLY COMPETITIVE CURRENCY IN HIGHER EDUCATION"

10TH OCTOBER 2025



The Department of Electrical & Electronics Engineering (EEE), through the ISTE Faculty Chapter JITD, successfully conducted a one-day International Faculty Development Program (FDP) on the theme "Growing Globally Competitive Currency in Higher Education" at Jain Institute of Technology, Davanagere. The program, held in association with futred | Innovation Studios, witnessed active participation from faculty members across various disciplines.

The FDP was designed to address the evolving demands of global higher education and to equip educators with innovative strategies for enhancing institutional and individual competitiveness on an international scale.

Inaugural Ceremony: The program commenced with a formal inaugural session graced by the presence of esteemed dignitaries:

- Patrons:
 - Dr. Ganesh D B, Principal & Director, JITD
 - Dr. Manjappa S, Advisor, JITD
- Convenor:
 - Dr. Preetha P S, Associate Professor & HOD, Department of Electrical & Electronics Engineering
- Co-Convenor:
 - Dr. Chetan H R, Assistant Professor, Department of Electrical & Electronics Engineering
 - Prof. Praveena Anaji, Assistant Professor, Department of Electrical & Electronics Engineering

The dignitaries emphasized the importance of continuous professional development for faculty members and the need to align teaching methodologies with global standards to produce industry-ready graduates.

Distinguished International Resource Persons

The FDP featured two eminent resource persons who brought global perspectives and industry insights to the sessions:

- Mr. Michael Clements, Emeritus Professor
- Dr. Manish Malhotra, Founder and CEO - Futred

Their expertise provided participants with valuable exposure to international educational frameworks and emerging trends in higher education.

Key Themes Explored

The sessions were structured around six critical objectives, ensuring a comprehensive learning experience for all attendees:

Objective Focus Area

- Advancing Graduate Employability through industry-aligned curricula
- Adopting Adaptive Teaching Practices for diverse learner needs
- Strengthening International isolation & Strategic Partnerships
- Enhancing Research Excellence & Global Impact
- AI for Educators: Hands-on session on integrating AI in teaching
- Growing the Globally Competitive Mindset among faculty and students..

Valedictory Session: The valedictory session featured feedback from participants who expressed their appreciation for the high-quality content and the interactive nature of the sessions. Certificates of participation were distributed to the attendees, marking the successful conclusion of the FDP.

INAUGURATION FORUM-POTENZA 2025 & FRESHER'S DAY

7TH NOVEMBER 2025



The Department of Electrical & Electronics Engineering at Jain Institute of Technology, Davangere, successfully inaugurated Forum-Potenza 2025 along with the much-awaited Fresher's Day celebrations on 7th November 2025 at the JITD Auditorium. The event marked a memorable beginning for the new academic year, bringing together students, faculty, and distinguished guests in a spirit of innovation and camaraderie.

The ceremony was graced by Mr. Mallikarjun Kadakol, Director of Vrutti Rangabhumi Rangayana, Davangere (Karnataka Government), as the esteemed Chief Guest. The event was presided over by Dr. Ganesh D B, Principal & Director of JITD, with Dr. Manjappa Sarathi, Advisor of Jain Group of Institutions, attending as the Guest of Honour. The program was convened by Dr. Preetha P S and coordinated by Dr. Chetan H R from the Department of EEE.

The inaugural ceremony commenced with a traditional lamp lighting, followed by inspiring addresses from the dignitaries who appreciated the department's initiatives and encouraged students to explore their potential. The formal proceedings were seamlessly blended with vibrant cultural performances by freshers, including singing, dancing, skits, and the eagerly contested Mr. & Ms. Fresher competition. The day witnessed enthusiastic participation from students, creating an atmosphere of joy and togetherness. The event successfully achieved its objective of welcoming new students into the JITD family while introducing them to the Forum-Potenza platform for innovation and excellence. Interactive sessions between juniors and seniors fostered meaningful connections, and the talent showcased by freshers left everyone impressed.

The Department extends its heartfelt gratitude to the Chief Guest, Guests of Honour, Principal, faculty members, student coordinators, and volunteers for their invaluable contributions in making this event a resounding success. Special thanks to all the freshers for their enthusiastic participation, which made the day truly memorable.

"PLANTATION AND ADOPTION OF TREES"

8TH NOVEMBER 2025



The Department of Electrical & Electronics Engineering at Jain Institute of Technology, Davangere, successfully organized the "Plantation and Adoption of Trees" event on 8th November 2025 in the EEE Seminar Hall. The initiative aimed to promote environmental awareness and encourage social responsibility among students and staff.

The event was graced by our esteemed Guest, Dr. Prakash K B, Academic Dean, who delivered an inspiring talk on the importance of green initiatives and the role of educational institutions in fostering environmental sustainability. His encouraging words motivated everyone present to actively participate in campus greening efforts and adopt trees as a personal commitment to nature.

NSS ACTIVITY – EMPOWERING FUTURE INNOVATORS**9TH NOVEMBER 2025**

Second-year students from the Department of Electrical and Electronics Engineering at Jain Institute of Technology, Davangere, embarked on an inspiring industrial visit to Tadhvanam at Nittur, Harihara Taluk, Davangere, on 9th November 2025. The visit provided students with valuable exposure to the world of entrepreneurship and sustainable development.

The students had the opportunity to explore how local women entrepreneurs are leading the way in herbal and millet-based product manufacturing. They gained hands-on insights into the entire production process, from sourcing raw materials to packaging and marketing. More importantly, they witnessed firsthand how women's empowerment, teamwork, and social impact come together to create successful community enterprises.

The Department extends its heartfelt gratitude to the team at Tadhvanam for opening their doors and warmly welcoming our students. Their willingness to share their journey and showcase their work embodies the true spirit of entrepreneurship and sustainable development.

Experiences like these motivate and empower the next generation of engineers to create meaningful change in society. The Department of EEE remains committed to providing such enriching opportunities that blend technical education with social awareness.

NSS ACTIVITY – NATURAL FARMING AWARENESS**9TH NOVEMBER 2025**

Second-year students from the Department of Electrical & Electronics Engineering, JITD, participated in an enlightening NSS activity focused on Natural Farming Awareness on 11th November 2025. The visit to Kariganuru provided students with a unique opportunity to explore eco-friendly natural farming methods and sustainable agriculture practices firsthand.

The students engaged in practical observation sessions, learning about traditional and modern natural farming techniques that promote environmental sustainability. They studied how natural farming methods contribute to soil health, water conservation, and chemical-free food production. The visit also highlighted the vital role of technology in modern farming and how engineering solutions can support agricultural development.

The Department extends its sincere gratitude to the local community at Kariganuru for their warm hospitality and willingness to share their knowledge and practices. Such immersive learning experiences enrich our students' understanding and inspire them to contribute meaningfully to society.

PRESTIGIOUS ISTE BEST STUDENT AWARD 2025 TO VINAY S B

9TH NOVEMBER 2025



The Department of Electrical and Electronics Engineering at Jain Institute of Technology, Davangere, proudly announces that Mr. Vinay S B, 7th-semester student, has been honored with the prestigious "Best Student Award 2025" from ISTE Karnataka!

The award was presented during the 21st ISTE Karnataka State Level Student Convention held at Yenepoya Institute of Technology, Moodabidri, on 12th November 2025. This recognition celebrates Vinay's outstanding academic performance, exemplary leadership qualities, and significant contributions to ISTE activities throughout his academic journey.

Vinay has consistently demonstrated excellence in both curricular and extracurricular spheres, setting a benchmark for his peers. His dedication to professional development, active participation in technical events, and commitment to upholding the values of the Indian Society for Technical Education have made him a deserving recipient of this honor.

The entire department, faculty members, and fellow students extend their heartfelt congratulations to Vinay on this remarkable achievement. His success serves as an inspiration to all students and uplifts the reputation of our department and institution.

KANNADA RAJYOTSAVA & KANNADOTSAVA 2025

21ST NOVEMBER 2025



Jain Institute of Technology, Davangere, celebrated the 70th Kannada Rajyotsava and Kannadotsava 2025 with great enthusiasm and cultural fervor on 19th November 2025 at the JITD Campus. The event commenced at 9:00 AM and witnessed vibrant participation from students, faculty, and staff, all coming together to honor the rich heritage and glory of the Kannada language and culture.

The celebration was graced by esteemed dignitaries who added luster to the occasion. We were honored to have Mr. Pruthvi Shamnur, renowned Kannada film actor, as the Chief Guest of the event. Also present was Mr. K.G. Yallappa from Karnataka Rakshana Vedike, who joined as a special guest, reinforcing the importance of preserving and promoting Karnataka's rich cultural identity.

The event was a colorful extravaganza featuring a variety of cultural programs that showcased the artistic talents of our students. The program included soulful songs, energetic dances, thought-provoking dramas, melodious folk verses, and a range of other cultural performances that reflected the essence of Karnataka's diverse traditions.

The Department extends its heartfelt gratitude to all the student performers, organizers, faculty members, and staff.

PARAMPARE 2025 – ETHNIC DAY CELEBRATIONS

22ND NOVEMBER 2025



Jain Institute of Technology, Davangere joyfully celebrated Ethnic Day – Parampare 2025 on 22nd November 2025 at the JITD Campus with the captivating theme "Chitra Katha Vaibhava", highlighting India's rich cultural narratives, traditions, and visual heritage. The campus was transformed into a colorful and vibrant space as students showcased the grandeur of Indian culture through traditional attire and artistic expressions.

The event aimed to revive and appreciate the legacy of Indian folklore, stories, and art forms, encouraging students to connect with the roots of tradition and cultural diversity.

We are thrilled to announce that students from the Department of Electrical & Electronics Engineering won the FIRST PRIZE in the Ethnic Day event! Their outstanding performances, creative presentations, and vibrant traditional attire impressed the judges and audience alike, bringing laurels to the department.

STUDENTS PARTICIPATE AT BENGALURU TECH SUMMIT 2025

24TH NOVEMBER 2025



We are delighted to announce that students from the Department of Electrical and Electronics Engineering, Jain Institute of Technology, Davangere, actively participated in the prestigious Bengaluru Tech Summit 2025, held on 24th November 2025 at Bengaluru, Karnataka.

Recognized as Asia's largest business technology summit, this flagship event brought together global technology leaders, innovators, entrepreneurs, and policymakers under one roof. The summit provided our students with a unique platform to explore cutting-edge innovations, interact with industry experts, and gain insights into emerging technological trends shaping the future.

JITD SHINES AT IDEATHON, GM UNIVERSITY DAVANGERE

24TH NOVEMBER 2025

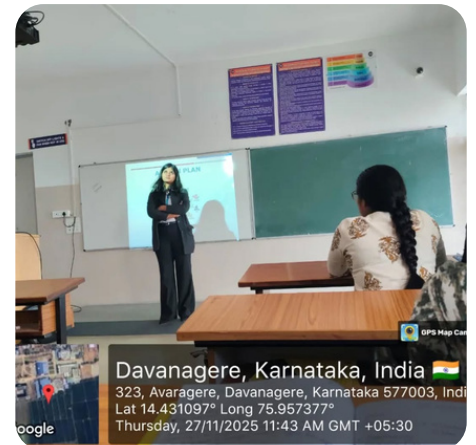
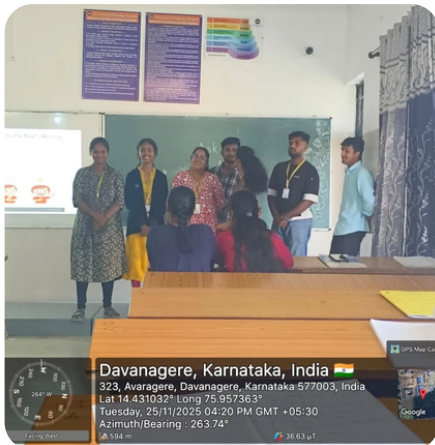


We are proud to announce that five talented teams from the Department of Electrical and Electronics Engineering at Jain Institute of Technology, Davangere, showcased their innovative projects at the Ideathon organized by GM University, Davangere, demonstrating the department's unwavering commitment to technical excellence and societal progress. Our students presented creative and forward-thinking solutions spanning multiple engineering domains, including Automation, Robotics and Industry 4.0, Artificial Intelligence and Smart Computing, Smart Education and Digital Campus, as well as Infrastructure and Smart Cities. Each team delivered innovative ideas with real-world applications, reflecting strong creativity, technical depth, social relevance, and future readiness aligned with emerging technologies and industry trends.

The participation at Ideathon provided our students with a valuable platform to present their ideas, receive feedback from experts, and compete with peers from other institutions. The Department extends hearty congratulations to all the participating teams for their dedication, hard work, and innovative spirit, which brings pride to the department and inspires fellow students to pursue creative problem-solving. We thank the Principal and Management for their encouragement and support, the faculty mentors for guiding the students throughout their project journey, and GM University, Davangere, for organizing this excellent platform for innovation. All participants represented JITD with excellence and enthusiasm, reaffirming our institute's position as a hub of innovation and engineering excellence.

PHASE-1 PLACEMENT TRAINING – 2ND YEAR STUDENTS

24TH NOVEMBER 2025



The Training & Placement Cell at Jain Institute of Technology, Davangere, in association with Seventh Sense Talent Solutions, successfully launched the Phase-1 Placement Training on Communication & Soft Skills for 3rd semester engineering students from 24th to 28th November 2025, with the inaugural day on 24th November witnessing enthusiastic participation from students across all departments. The first day of this five-day training program focused on building confidence in communication and teamwork, preparing students for campus recruitment processes, and helping them understand corporate expectations and professional etiquette through interactive sessions, group activities, and practical exercises designed to strengthen interpersonal skills essential for workplace success. The program is being coordinated by Mr. Praveena Anaji, Mr. Madhu H T, and Mr. Kiran Kumar M S, under the valuable guidance of Dr. Prakash M Walavalkar, Dean – Training & Placement, and Dr. Ganesh D B, Principal & Director, JITD.

This Phase-1 Placement Training aims to equip students with essential soft skills and communication abilities crucial for succeeding in campus recruitment drives and professional careers, with the interactive methodology adopted by Seventh Sense Talent Solutions ensuring that students not only learn but also apply these skills in real-world scenarios. The Department extends its sincere gratitude to the Principal, Dean, coordinators, training partners, and all participating students for their enthusiastic involvement in making this initiative a resounding success.

PARENTS' AND TEACHER'S MEET – 2ND YEAR STUDENTS

1ST DECEMBER 2025



The Department of Electrical and Electronics Engineering conducted a Parents' Meet for 3rd semester EEE students on 1st December 2025. The event aimed to strengthen the collaboration between parents and the department by discussing students' academic progress, attendance, and overall performance. Faculty members briefed the parents about the curriculum, laboratory activities, upcoming internal assessments, and support systems available for students' improvement. Parents actively interacted with the staff, shared valuable feedback, and appreciated the department's efforts in ensuring quality education and student development.

MINI PROJECT & PRODUCT EXHIBITION EXPO – 2025

4TH DECEMBER 2025



The Department of Electrical & Electronics Engineering at Jain Institute of Technology, Davangere, in association with IIC JITD and Forum Potenza, successfully organized the "Mini Project Expo – 2025: Product and Project Exhibition" on 4th December 2025 at the EEE Department premises from 2:30 PM to 4:30 PM. The event witnessed an impressive showcase of innovative mini projects and products developed by EEE students, providing a vibrant platform for creativity, teamwork, and practical learning. The expo featured over 30 innovative projects spanning automation, renewable energy, IoT, and smart systems, with participation from students across all semesters of the department.

The projects were meticulously evaluated by Dr. Jayadevappa B N, Professor, Department of Electrical & Electronics Engineering, who served as the esteemed judge for the expo. With his vast academic experience and technical expertise, Dr. Jayadevappa assessed each project thoughtfully, providing valuable feedback and constructive suggestions that will help students refine their work for future competitions and larger platforms. Students enthusiastically explained their projects to visitors, demonstrating not only their technical knowledge but also their communication and presentation skills. The expo witnessed enthusiastic participation from faculty members across various departments, students from all engineering branches, department heads, and teaching staff, all of whom appreciated the creativity and hard work of the young innovators.

The event successfully provided a platform for students to showcase their talent, encouraged innovation and practical application of knowledge, fostered teamwork and collaborative spirit, and bridged the gap between theoretical learning and hands-on experience. The Department extends its sincere gratitude to Dr. Ganesh D B, Principal & Director, for his constant encouragement; Dr. Preetha P S, Head of Department, for her visionary leadership; Dr. Jayadevappa B N for graciously accepting to judge the expo and providing expert guidance; IIC JITD and Forum Potenza for their valuable association; all faculty members for guiding and mentoring students; the organizing team for their meticulous planning; all student participants for their remarkable projects; and visitors for their appreciation and support.

P-EXPO-2025 – PROJECT & PRODUCT EXHIBITION

5TH DECEMBER 2025





Jain Institute of Technology, Davangere, successfully organized the P-EXPO-2025 – Project & Product Exhibition on 6th December 2025 at the JITD Auditorium, with the event being organized by the institution in association with the EDC Cell and IIC, providing a dynamic platform for students to showcase their innovative projects and creative products. The exhibition witnessed an impressive display of engineering brilliance, with students from various departments presenting their working models, prototypes, and innovative solutions addressing real-world challenges.

The event was graced by the presence of esteemed dignitaries including Chief Guest Mr. Raghav G Shetty, CEO & Managing Director of RDL Technologies, Adyar, Mangaluru; Guests of Honour Mr. Naveen Udoshi, Founder & Director of Noeticminds Consulting (India & USA), and Mr. Harsha N S, Assistant Vice President of OSS-Network Operation Center, Reliance JIO, Bengaluru; along with Dr. Ganesh D B, Principal & Director; Dr. K B Prakash, Dean Academics; Dr. Manjappa S, Advisor; Dr. Ramesh R S, Program Convener; and members of IIC & EDC, JITD. The projects were meticulously evaluated by a distinguished panel of judges including Prof. Karibasavaraju T S from BIET, Davanagere, whose extensive academic expertise and technical knowledge provided valuable insights and constructive feedback to the young innovators, helping them refine their work for future platforms and competitions.

The program commenced with a traditional lamp lighting ceremony by the dignitaries, followed by inspiring addresses from the chief guest and guests of honour who emphasized the importance of innovation and entrepreneurship among engineering students, encouraging them to transform their ideas into viable products and startups while appreciating the quality of projects displayed and lauding the institution's efforts in fostering a culture of innovation. The exhibition featured over 50 innovative projects spanning automation and robotics, renewable energy systems, IoT and smart technologies, artificial intelligence and machine learning, Industry 4.0 solutions, and sustainable development innovations, with students enthusiastically explaining their work to visitors, industry experts, faculty members, and fellow students. The event successfully provided a platform for networking and knowledge exchange, direct interaction with industry leaders, recognition of outstanding projects by the judging panel, and encouragement for entrepreneurship and startup culture among students.

The institution extends its sincere gratitude to Mr. Raghav G Shetty, Chief Guest; Mr. Naveen Udoshi and Mr. Harsha N S, Guests of Honour; Prof. Karibasavaraju T S, Judge from BIET, Davanagere; Dr. Ganesh D B, Principal & Director; Dr. K B Prakash, Dean Academics; Dr. Manjappa S, Advisor; Dr. Ramesh R S, Program Convener; all members of IIC & EDC; all faculty members for mentoring students; all student participants for their remarkable projects; and visitors and well-wishers for their appreciation and support.

WATER CONSERVATION FIELD ACTIVITY

17TH DECEMBER 2025



The Department of Electrical & Electronics Engineering, Jain Institute of Technology, Davangere, successfully organized the 'Water Conservation' field activity at the Kunduvada Water Treatment Plant on 17th December 2025 at 2:00 PM under Social Connect & Responsibility (BSCK307). The event was accomplished under the guidance of Mr. Pradeep Kanavi, Assistant Professor, and Mr. Sandeep Patil M S, Foreman, who led the initiative and ensured its successful execution. This valuable field visit provided students with firsthand exposure to water treatment processes, including sedimentation, filtration, and disinfection, while helping them understand the critical importance of water conservation and the technology behind ensuring safe drinking water for our community. The activity emphasized the message that "water is life's matter and matrix" and encouraged future engineers to appreciate how every drop counts in sustaining life and community well-being.

ANNUAL ALUMNI MEET-SAMANVAYA 2025

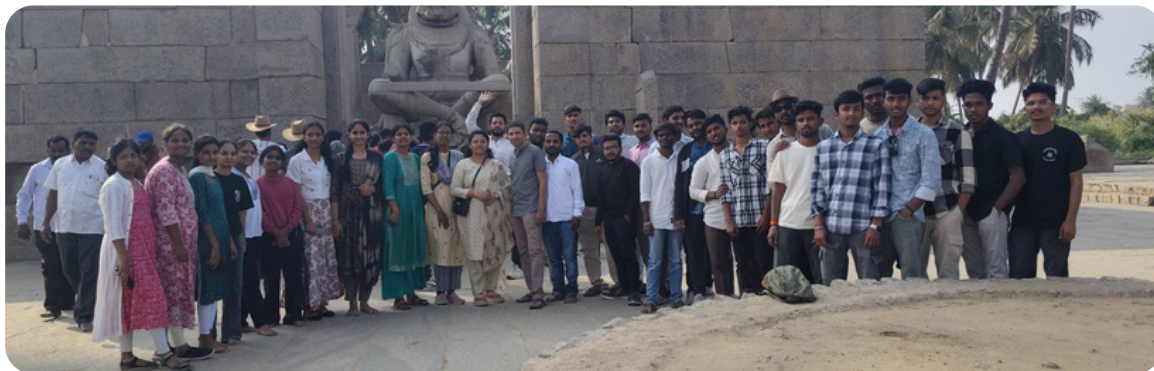
20TH DECEMBER 2025



The Jain Institute of Technology (JIT), Davangere, successfully hosted its much-anticipated annual Alumni Meet, SAMANVAYA 2025-26, on December 20, 2025, transforming the campus into a vibrant hub of nostalgia and professional camaraderie. The event witnessed enthusiastic participation from alumni across various batches, who returned to their alma mater to reconnect with classmates and faculty, and to share the professional experiences they have gained since graduating. The gathering was graced by the presence of Chief Guest Mr. Satish Kumar L, Board Member of Vision Davangere, alongside distinguished members of the institute including Dr. Ganesh D B (Principal & Director), Dr. Manjappa S (Advisor), Dr. K B Prakash (Dean Academics), and Dr. Nataraj G (Secretary, JITAA). The program was designed to foster meaningful connections, featuring a campus tour that allowed alumni to witness the institution's growth, followed by interactive sessions where they shared valuable industry insights with current students. The collective reminiscence and exchange of ideas rendered the meet a truly memorable and inspiring gathering, reinforcing the strong and enduring bond within the JIT family.

HERITAGE AT HAMPI UNDER SOCIAL CONNECT INITIATIVE

24TH DECEMBER 2025



A Heritage Walk to the historic temple town of Hampi was successfully completed by enthusiastic students of the Department of Electrical and Electronics Engineering (EEE) at Jain Institute of Technology, Davangere. The excursion provided students with a unique opportunity to experience history, architecture, and culture beyond the confines of the traditional classroom. Organized as part of the Social Connect and Responsibility activity, the visit to the UNESCO World Heritage Site allowed the budding engineers to explore the majestic ruins of the Vijayanagara Empire, including the iconic stone chariot and ancient market streets, fostering an appreciation for historical ingenuity and cultural heritage.

The Department of Electrical and Electronics Engineering extends its gratitude to the dedicated faculty members who made this enriching experience possible, including Assistant Professors Mr. Pradeep Kanavi, Ms. Sanchitha, and Ms. Sushmitha, along with Instructor Rajashekhar, who successfully accomplished the tour. Their efforts ensured that students gained valuable insights beyond their academic curriculum, reinforcing the institute's commitment to holistic education and social responsibility.

STUDENTS ACADEMIC TOPPERS

3RD SEMESTER



K M POORNIMA
79.12%



SAMYUKTHA
77.8%



VIKAS S
77%

5TH SEMESTER



SHALINI H J
75.8%



KAVITHA M C
75.1%



ASHWINI H Y
74.3%

7TH SEMESTER



VINAY S B
88%



K ANKITA
87.1%



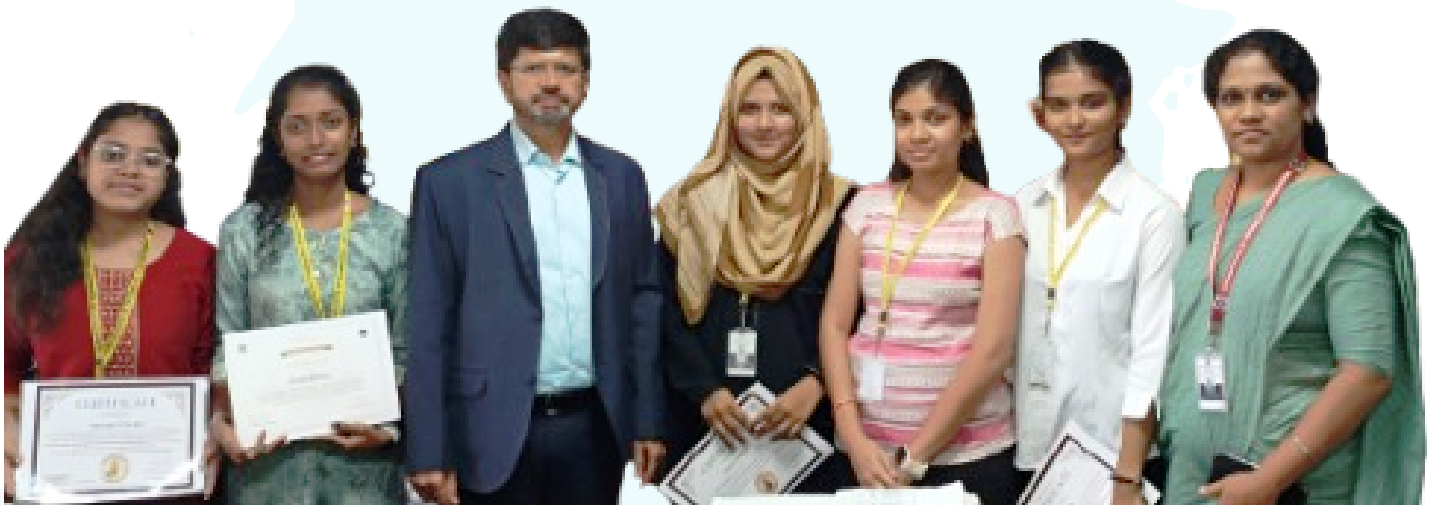
SANJANA S
82.5%

ENERGY SWARAJ AMBASSADORS



Energy Swaraj Foundation

Energy by Locals for Locals



Proudly recognized by the Energy Swaraj Foundation for completing the Climate Literacy & Energy Literacy (CLE) Training under the Finite Earth Movement (FEM).

Santrupti S, Gowri R A, Ayesha Siddiqa A, Pooja P J, Angel W and other dedicated student leaders from the Department of Electrical & Electronics Engineering, Jain Institute of Technology, Davangere. These ambassadors now lead the way in energy literacy, sustainability, and climate action—on campus and beyond.



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