



Arka Educational and Cultural Trust®  
**JAIN INSTITUTE OF TECHNOLOGY**  
**Davanagere**  
(A Unit of Jain Group of Institutions, Bangalore)  
Accredited by NBA, New Delhi: EEE, ECE & CSE. (Valid Up to : 30.06.2026)



# JIT EEE-NEWSLETTER

BI ANNUAL JANUARY- JUNE 2025

## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



### “VISION”

“To create technically superior, highly competent, ethically strong and socially responsible Electrical and Electronics Engineers with innovative and entrepreneurship qualities.”

### “MISSION”

**M1 - To impart excellent Electrical and Electronics Engineering Education by providing Innovative teaching, career focused education and experiential learning.**

**M2-To create an environment for innovative thinking and contribute to the advancement of science and technology to meet the ever changing needs of society.**

**M3-To provide student centric platform to produce socially responsible, competent engineers and entrepreneurs with ethical standards.**



### EDITORIAL TEAM

**PROF. PRADEEP KANAVI**  
ASSISTANT PROFESSOR

**MR. SANDEEP PATIL M S**  
FOREMAN



Visit us at: [www.jitd.ac.in](http://www.jitd.ac.in)

## PROGRAM OUTCOMES

1. **Engineering knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and computer science and engineering to the solution of complex engineering problems.
2. **Problem analysis:** Identify, formulate, review research literature, and analyze complex computer engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
3. **Design/development of solutions:** Design solutions for complex computer engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
4. **Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
5. **Modern tool usage:** Create, select, and apply appropriate techniques, resources, and modern computer engineering and IT tools including prediction and modelling complex computer engineering activities with an understanding of the limitations.
6. **The engineer and society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
7. **Environment and sustainability:** Understand the impact of the professional computer engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
8. **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the computer engineering practice.
9. **Individual and team work:** Function effectively as an individual, and as member or leader in diverse teams, and in multidisciplinary settings.
10. **Communication:** Communicate effectively on complex computer engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
11. **Project management and finance:** Demonstrate knowledge and understanding of the computer engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
12. **Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

## PROGRAM SPECIFIC OUTCOMES (PSO's)

**A graduate of the Electrical & Electronics Engineering Program will demonstrate:**

PSO1: Apply the knowledge of mathematics, science and electrical engineering fundamentals to solve complex problems in electrical machines, control systems, power systems and electronics.

PSO2: Specify, architect and analyze power systems that efficiently generate, transmit, distribute and utilize electrical power in the present context.

PSO3: Analyze, design & maintenance of modern electrical drives system and modern lighting system to conserve electrical energy.

## PROGRAM EDUCATIONAL OBJECTIVES (PEO's)

**A graduate of the Electrical & Electronics Engineering Program will demonstrate:**

PEO1: To become the resource of Electrical and Electronics engineering.

PEO2: To work in multidisciplinary areas involving core engineering and engaged in lifelong learning for successful professional career.

PEO3: To be successful in taking leadership roles and entrepreneurial positions.

## MESSAGE FROM THE CHAIRMAN

We are a school that prides itself not in fostering academic excellence but also in making our learners append at the school satisfying, fulfilling and joyous. I intend to take the JGI Group to the next level, employ more effective methods to make the students think differently and instill in them with positive attitude towards inclusive growth that benefits them and the society at large.

**DR. CHENRAJ ROYCHAND**  
CHAIRMAN JGI  
BENGALURU



## MESSAGE FROM THE PRINCIPAL

It's my pleasure to introduce this latest issue of the JIT, EEE Newsletter. I would like to congratulate Editorial team for bringing out a beautiful and informative EEE newsletter which has showcased the various activities, events in the department, and the achievements of students and faculties. I look forward to achieve greater heights in the years to come.

**DR. GANESH D B**  
PRINCIPAL & DIRECTOR JIT  
DAVANAGERE



## MESSAGE FROM THE HEAD OF THE DEPARTMENT

Welcome to the Department of Electrical and Electronics Engineering at JIT Davangere. We started our journey in the year of 2011 with an intake of 60 students. The department has full-fledged laboratories as prescribed by VTU syllabus with modern equipment's. We have grown our expertise, competence.

Core Electrical and Electronics Engineering curriculum and research. We have a strong undergraduate program in Electrical and Electronics Engineering in B.E. We conduct many activities throughout the academic year like Workshops, Seminars, technical talks, skill enhancement programs and other co-curricular activities under the department Form "POTENZA". We are happy to bring out our department EEE newsletter, which showcases all the activities of the department. I congratulate the team for their continuous effort.

**DR. SANTOSH .M. NEJAKAR**  
PROFESSOR & HEAD,  
DEPT. OF ELECTRICAL & ELECTRONICS ENGINEERING, JITD



# EEE STUDENTS COMPLETE MATLAB & SIMULINK CERTIFICATION

26<sup>TH</sup> FEBRUARY 2025



The Department of Electrical & Electronics Engineering at JITD is proud to announce that 64 students from the third year have successfully completed the 'MATLAB & Simulink Basics' certification course. This comprehensive program, authored by Starweaver and offered on the Coursera platform, was completed on February 26, 2025, in online mode.

This initiative was taken to bridge the gap between academic curriculum and industry requirements, providing our students with hands-on experience in software that is the cornerstone of simulation, design, and analysis in the global engineering sector. MATLAB and Simulink are industry-standard tools used by leading companies and research institutions worldwide for:

- Algorithm development
- Data analysis and visualization
- Modeling and simulating dynamic systems (like control systems, power systems, and signal processing)
- Model-Based Design for complex projects.

Mastering these tools significantly enhances a student's profile for placements, higher studies, and project work.

Dr Nandish B M Assistant Professor coordinated the program and Dr. Santosh M Nejekar, Head of the EEE Department, stated, "We are consistently striving to provide our students with opportunities that give them a competitive edge. The successful completion of this certification by 64 of our students is a testament to their dedication and our department's commitment to outcome-based education. Skills in MATLAB and Simulink are non-negotiable for a successful career in EEE, and we are thrilled to see our students embracing this learning."

## WORKSHOP ON INDUSTRIAL AUTOMATION

2<sup>ND</sup> MAY 2025



The Department of Electrical & Electronics Engineering, under the banner of Forum Potenza, successfully conducted an immersive workshop on "Industrial Automation - Industry 4.0". The event was specifically curated for EEE students, providing them with a crucial insight into the technologies shaping the future of manufacturing and automation.

The workshop delved into the core concepts of the Fourth Industrial Revolution, covering topics such as the Internet of Things (IoT), Robotics, Artificial Intelligence (AI), and Cyber-Physical Systems. The session was designed to bridge the gap between theoretical knowledge and its practical industrial applications, preparing our students to be industry-ready innovators.

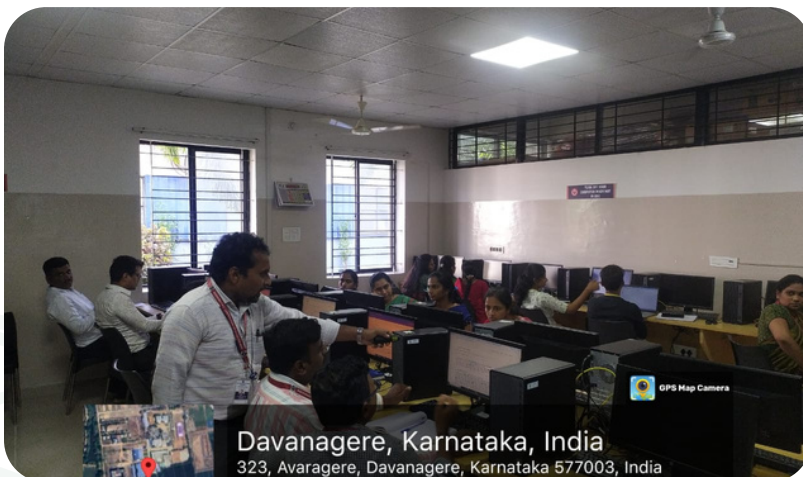
The department extends its heartfelt gratitude to the coordinators and leadership whose efforts made this event a resounding success:

- Workshop Coordinator: **Dr. Jayadevappa B N**, Professor, EEE Dept.
- Forum Coordinator: **Dr. Chetan H R**, Head of Department, EEE Dept.
- Special Thanks: **Dr. Santosh M Nejekar** for his invaluable support.

Venue: Abdul Kalam Seminar Hall, Jain Institute of Technology Davanagere

# JITD-IIC EMPOWERS INNOVATORS: A WORKSHOP ON IPR PROTECTION FOR STARTUPS

2<sup>ND</sup> MAY 2025



The JITD Institution's Innovation Council (JITD-IIC), in association with the Department of Research & Development, organized a highly valuable One-Day Workshop on "Protecting Intellectual Property Rights (IPRs) & IP Management for Startups" on 2nd May 2025 at the CNR Rao Laboratory.

We were privileged to have Dr. Santosh M. Nejakar, Professor & Head of EEE Department and Dean of R&D, as the resource person. The session provided critical insights into the integral role of IPR in academia.

The workshop served as an excellent professional development opportunity, focusing on:

- Integrating IPR into Curriculum: Strategies for embedding IP awareness into student projects and coursework.
- Protecting Research Output: Guidance on identifying patentable research within academic work and the procedural aspects of filing.
- Commercialization Pathways: Exploring avenues to translate departmental research into marketable products through licensing and startups.
- Supervisor Guidance: Equipping faculty to better guide M.Tech and Ph.D. scholars on protecting their thesis-related innovations.

**EMERGE -2025: STATE LEVEL TECHNICAL SYMPOSIUM****24<sup>TH</sup> APRIL 2025**

The Department of Electrical and Electronics Engineering at JIT Davangere, in collaboration with the IEEE Student Chapter, proudly organized and participated in "EMERGE 2025", a state-level technical symposium held on April 24, 2025. The event was inaugurated by our esteemed chief guest, Dr. Suresh, Director of ISTE Karnataka Chapter and Director of CIT, Gubbi.

The campus buzzed with innovation and competitive spirit as 1200 students from various engineering colleges across Karnataka converged to showcase their technical prowess in a multitude of events including Tech-Art, Hackathon, Ideaton, E-Fashion, Gizmo-Guess, Isotrix, and Tech Crossword Hunt.

The Department of Electrical & Electronics Engineering had the distinct honor of hosting the "Tech Crossword Hunt." The event was designed to test participants' technical vocabulary, logical reasoning, and speed, and it witnessed enthusiastic participation from teams across the state.

The successful execution of this event was made possible by the dedicated efforts of our coordinators:

- Student Coordinator: Vinay S Band
- Staff Coordinators: Dr. Nandish B M, Dr. Chethan H R, Mr. Pradeep Kanavi, Mr. Sandeep Patil M S

We are thrilled to announce the brilliant winners of this challenging event:

- 🏆 First Prize: Team EEE Sparkers., Dept. of EEE, Jain Institute of Technology, Davangere.
- 🥈 Second Prize: Team GMIT ., GM Institute of Technology, Davangere

The symposium provided a dynamic platform for students to:

- Innovate through the Hackathon and Ideaton.
- Showcase creativity in Tech-Art and E-Fashion.
- Demonstrate quick thinking in Isotrix and Gizmo-Guess.
- Network and exchange ideas with peers from across the state.

The event perfectly aligned with our mission to foster technical excellence, innovation, and collaborative learning beyond the classroom.



# EEE DEPARTMENT'S TECH CROSSWORD HUNT EVENT

24<sup>TH</sup> APRIL 2025



Department of Electrical and Electronics Engineering recently electrified the campus with its thrilling "Tech Crossword Hunt," an event designed to test the limits of technical vocabulary, logical reasoning, and speed.

The competition witnessed enthusiastic participation from numerous talented teams across the state, creating an atmosphere of intense and friendly rivalry. Participants dove deep into puzzles that challenged their core technical knowledge and quick-thinking skills.

This successful and seamless event was made possible by the unwavering dedication and hard work of our coordinators.

- Student Coordinator: Vinay S B
- Staff Coordinators: Mr. Pradeep Kanavi, Dr. Nandish B M, Dr. Chethan H R, , Mr. Sandeep Patil M S

The department extends its heartfelt gratitude to everyone involved for their exceptional effort in making this event a memorable one.

Winners Are...!

After a fiercely competitive round, we are thrilled to announce the brilliant winners of this challenging event:

-  First Prize: Team EEE Sparkers „Dept. of EEE, Jain Institute of Technology, Davangere.
-  Second Prize: Team GMIT „, GM Institute of Technology, Davangere.

**FAREWELL 2021-25 BATCH & POTENZA FORUM VALEDICTORY****30<sup>TH</sup> MAY 2025**

The Forum Potenza, the official student forum of the EEE department, successfully hosted its Valedictory and Farewell Function for the graduating batch of 2021-2025 on 30th May 2025 at the JITD Auditorium. The event was a memorable occasion, blending celebration with invaluable career guidance. The function was graced by **Mr. Santosh S**, an Embedded Software Developer at Honeywell Technology and a distinguished alumnus of the EEE department, JITD, who served as the Chief Guest. Chief guest Mr. Santosh S delivered an insightful talk on "Preparation for Campus to Corporate." Drawing from his own journey from JITD to a global tech giant like Honeywell, he provided pragmatic advice to the graduating students.

- The essential technical and soft skills sought by core engineering companies.
- Strategies for transitioning from academic projects to real-world industry challenges.
- Navigating the first few years of a corporate career for long-term growth.
- The enduring value of the foundational knowledge gained at JITD.

The event was presided over by **Dr. Ganesh D B, Principal & Director of JITD**. The forum coordinator, **Dr. Chetan H R**, presented the annual report of Forum Potenza's activities. The Head of the EEE Department, **Dr. Santosh M Nejekar**, and the JIT advisor, **Dr. Manjappa Sarathi**, addressed the gathering, extending their best wishes to the outgoing batch.

"The function was a perfect blend of emotion and inspiration, sending off our graduates with both fond memories and a road map for their future."

## WORLD ENVIRONMENT DAY 2025

5<sup>TH</sup> JUNE 2025



Jain Institute of Technology, Davangere (JITD) marked World Environment Day 2025 with a powerful collaborative event, uniting with the Davanagere Jila Panchayat, Davanagere City Corporation, Forest Department, and Pollution Control Board for a impactful campus event on June 5th, 2025.

The institute had the honour of hosting the following chief guests, who inspired the audience with their insights and commitment to environmental conservation:

- **Sri. Gangadhar Swamy G. M.**, IAS, District Commissioner, Davanagere
- **Sri. Suresh B. Itnal**, IAS, Chief Executive Officer, Zilla Panchayat, Davanagere
- **Smt. Uma Prashanth**, IPS, Superintendent of Police, Davanagere District
- **Sri. A. H. Annaya**, District Judge, Davanagere

In their addresses, the dignitaries emphasized the critical role of educational institutions, government bodies, and citizens in collaborating to address pressing environmental challenges such as climate change, waste management, and biodiversity loss. They applauded JITD's initiative in spearheading such a collaborative effort and urged students to become ambassadors for sustainable practices in their communities.

- **Plantation Drive:** A symbolic tree planting ceremony on the JITD campus.
- **Pledge Taking:** A collective pledge by students, faculty, and staff to adopt eco-friendly habits.
- **Expert Talks:** Sessions by officials from the Forest Department and Pollution Control Board on local environmental issues and solutions.
- **Awareness Campaigns:** Student-led rallies or exhibitions showcasing innovative ideas for sustainability.

The presence of such high-ranking officials from the civil administration, police, and judiciary underscored the shared responsibility towards protecting our planet and aligned with the global theme for World Environment Day 2025.

The management, principal, faculty, and students of JITD gratefully acknowledge the participation and guidance of all the chief guests and partnering departments.

## ROAD SAFETY AWARENESS PROGRAM

12<sup>TH</sup> JUNE 2025



In a significant stride towards fostering community welfare and student awareness, the NSS Unit of Jain Institute of Technology, Davanagere (JITD), in association with the Department of Police, Davanagere, successfully conducted a comprehensive Road Safety Awareness Program on June 12, 2025.

The event was graced by eminent personalities from the police department, who underscored the critical importance of road discipline and safety protocols.

The program was honored by the presence of our Chief Guest, **Smt. Uma Prashanth IPS**, Superintendent of Police, Davanagere. Her insightful address on the role of youth in building a responsible society set the tone for the day.

She was accompanied by **CPI Manjunath (Nalawagala)** and Police Inspector **Y.S. Shilpa**, who actively engaged with students and shared valuable, on-ground experiences.

- **Inaugural Address:** Smt. Uma Prashanth IPS highlighted alarming statistics on road accidents and emphasized that careful driving and adherence to traffic rules are not just legal obligations but a moral duty towards one's family and society.
- **Expert Sessions:** Officers from the Davanagere Police conducted sessions on critical topics such as:
  - The importance of helmets, seat belts, and avoiding speeding.
  - The dangers of distracted and drunk driving.
  - Understanding traffic signs and signals.
  - Legal repercussions of violating traffic rules.
- **Interactive Q&A:** Students participated in a vibrant question-and-answer session, clarifying doubts on various traffic laws and scenarios.
- **Pledge Taking:** A collective pledge was taken by all attendees to always follow traffic rules and become ambassadors of road safety.

## PROJECT PROPOSALS APPROVED FOR 48<sup>TH</sup> SERIES KSCST SPONSORSHIP

In the 48th series of Student Project Programme (SPP): 2024-25, Two Proposals from the department of E&E has been selected and sponsored. Hearty Congratulations to both the Project Guides and Students from College Management, Advisor, Principal, HOD and all the faculty members.

### PROJECT TEAM-1

#### AUTOMATIC POWER FACTOR CORRECTION USING ARDUINO

The project proposal titled "Automatic power factor correction using arduino" was submitted by the following students under the guidance of **Dr. Jayadevappa. B M**, Professor, Dept. of E&E. An amount of Rs. 4,000.00/- has been sanctioned.



**Dr. Jayadevappa. B M**



**Dhanushree K H**



**Karthik S M**



**Neha. R**



**Harshitha H N**

### PROJECT TEAM-2

#### ARDUINO BASED TRAIN ENGINE MONITORING SYSTEM

The project proposal titled "Arduino based Train engine monitoring system" was submitted by the following students under the guidance of **Dr. Marulasiddappa H B**, Associate Professor, Dept. of E&E. An amount of Rs. 4,000.00/- has been sanctioned.



**Dr. Marulasiddappa H B**



**Rakshitha A M**



**Ranjitha B N**



**Rekha s**



**Suchithra H S**

## STUDENTS ACADEMIC TOPPERS

### 4<sup>TH</sup> SEMESTER



AMRUTHA R  
78%



KAVITHA M C  
77%



KEERTHANA K  
76%

### 6<sup>TH</sup> SEMESTER



VINAY S B  
92%



K ANKITA  
89.4%



LAKSHMI K  
88%

### 8<sup>TH</sup> SEMESTER



SOUJANYA H  
98%



VIBHA C M  
97.5%



JOSHUA E PETER  
97%

## JITD PLACEMENTS



MANOJ R



SUSHANK M



JOSHUA E PETER



ARIS ALI H



VARUN B K



JAYANTH RAKSHITH K



SHARATH KUMAR R C



VIBHA C M



as a Technical Engineer

**PACKAGE : 4.5 LPA**

**BRANCH: ELECTRICAL & ELECTRONICS ENGINEERING**

**BRANCH:2024-25**



CHINMAYEE B



# CHIDANAND K H



**LAYAM FLEXI SOLUTIONS PVT.LTD**

**Package: 3 LPA**

**Branch: Electrical & Electronics Engg.**

**Batch:2024-25**

## JITD PLACEMENTS



TARUN KUMAR



KARTHIK S M



VIBHA C M



**Syrma SGS Technologies**

Domain : Supply Chain Management

Package: 3.5 LPA

**Branch: Electrical & Electronics Engg.**



**RAHUL SALABANNAVAR**



**Reliance Industries Ltd.**

Stream: Bio Energy

Package: 7.5 LPA

**Branch: Electrical & Electronics Engg.**

**Batch:2024-25**

## EMERGE TECH EVENT PHOTO GLIMPSE

